

**Fujitsu SPARC M12 and
Fujitsu M10/SPARC M10**

Glossary

FUJITSU

ORACLE

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Contents

Preface	v
List of Abbreviations	1
Glossary	3

Preface

This document describes the terms used in the documents of the SPARC M12/M10 from Oracle or Fujitsu.

Fujitsu SPARC M12 is sold as SPARC M12 by Fujitsu in Japan.
Fujitsu SPARC M12 and SPARC M12 are identical products.

Fujitsu M10 is sold as SPARC M10 by Fujitsu in Japan.
Fujitsu M10 and SPARC M10 are identical products.

The preface includes the following sections:

- Audience
- Related Documentation
- Document Feedback

Audience

This document is designed for system administrators with advanced knowledge of computer networks and Oracle Solaris.

Related Documentation

All documents for the server you use are available online at the following locations.

- Sun Oracle software-related documents (Oracle Solaris, etc.)
<http://docs.oracle.com/en/>
- Fujitsu documents
Global site
<http://www.fujitsu.com/global/products/computing/servers/unix/sparc/downloads/manuals/>
Japanese site
<http://www.fujitsu.com/jp/products/computing/servers/unix/sparc/downloads/manual/>

For a system using the SPARC M12, see the manuals listed in "[Documentation Related to the SPARC M12.](#)"

For a system using the SPARC M10, see the manuals listed in "[Documentation Related to the SPARC M10.](#)"

Documentation Related to the SPARC M12

Manual Names (*1)

Fujitsu SPARC M12 Product Notes

Fujitsu SPARC M12 Quick Guide

*Fujitsu SPARC M12 Getting Started Guide (*2)*

*Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Important Legal and Safety Information (*2)*

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Safety and Compliance Guide

Software License Conditions for Fujitsu SPARC M12 and Fujitsu M10/SPARC M10

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Security Guide

Fujitsu SPARC Servers/SPARC Enterprise/PRIMEQUEST Common Installation Planning Manual

Fujitsu SPARC M12-1 Installation Guide

Fujitsu SPARC M12-2 Installation Guide

Fujitsu SPARC M12-2S Installation Guide

Fujitsu SPARC M12 PCI Card Installation Guide

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Domain Configuration Guide

*Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 RCIL User Guide (*3)*

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF MIB and Trap Lists

Documentation Related to the SPARC M12 (*continued*)

Manual Names (*1)

Fujitsu SPARC M12-1 Service Manual

Fujitsu SPARC M12-2/M12-2S Service Manual

Crossbar Box for Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Service Manual

PCI Expansion Unit for Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Service Manual

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Glossary

*1 The listed manuals are subject to change without notice.

*2 Printed manuals are provided with the product.

*3 This document applies specifically to the SPARC M12/M10 and FUJITSU ETERNUS disk storage system.

Documentation Related to the SPARC M10

Manual Names (*1)

Fujitsu M10/SPARC M10 Systems Product Notes

Fujitsu M10/SPARC M10 Systems Quick Guide

*Fujitsu M10/SPARC M10 Systems Getting Started Guide (*2)*

*Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Important Legal and Safety Information (*2)*

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Safety and Compliance Guide

Software License Conditions for Fujitsu SPARC M12 and Fujitsu M10/SPARC M10

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Security Guide

Fujitsu SPARC Servers/SPARC Enterprise/PRIMEQUEST Common Installation Planning Manual

Fujitsu M10-1/SPARC M10-1 Installation Guide

Fujitsu M10-4/SPARC M10-4 Installation Guide

Fujitsu M10-4S/SPARC M10-4S Installation Guide

Fujitsu M10/SPARC M10 Systems PCI Card Installation Guide

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Domain Configuration Guide

*Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 RCIL User Guide (*3)*

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF MIB and Trap Lists

Fujitsu M10-1/SPARC M10-1 Service Manual

Fujitsu M10-4/Fujitsu M10-4S/SPARC M10-4/SPARC M10-4S Service Manual

Crossbar Box for Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Service Manual

PCI Expansion Unit for Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Service Manual

Manual Names (*1)

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Glossary

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- Global site
<http://www.fujitsu.com/global/contact/>
- Japanese site
<http://www.fujitsu.com/jp/products/computing/servers/unix/sparc/contact/>

List of Abbreviations

This section describes the abbreviations used with the SPARC M12/M10 and the formal names of these abbreviations.

Note - For the terms related to Oracle VM Server for SPARC, see "Glossary" in the *Oracle VM Server for SPARC Administration Guide* of the version used.

Note - This section does not describe generally well-known terms.

BB

building block

CMU

CPU memory unit

DR

dynamic reconfiguration

FANU

fan unit

FRU

field replaceable unit

LSB

logical system board

OPNL

operation panel

PHP

PCI hot plug

PCL

PPAR configuration list

PDU

power distribution unit

POST

power-on self test

PPAR

physical partition

PPAR DR

physical partition dynamic reconfiguration

PSB

physical system board

PSU

power supply unit

PSUBP

PSU backplane

RCIL

remote cabinet interface over LAN

SSCP

SP to SP communication protocol

XBBOX

crossbar box

XBBPU

crossbar backplane unit

XBU

crossbar unit

XSCF

extended system control facility

XSCFIFU

XSCF interface unit

XSCFU

XSCF unit

Glossary

This section mainly describes the specific terms used with the SPARC M12/M10.

Note - For the terms related to Oracle VM Server for SPARC, see "Glossary" in the *Oracle VM Server for SPARC Administration Guide* of the version used.

Note - This section does not describe generally well-known terms.

BB-ID

The ID used for identifying the SPARC M12-2S or SPARC M10-4S in a building block configuration.

building block (BB) configuration

A configuration connecting multiple SPARC M12-2S or SPARC M10-4S units in the building block method. A single SPARC M12-2S or SPARC M10-4S is called the 1BB configuration, and a configuration with n units of the SPARC M12-2S/M10-4S is called the nBB configuration. (Example: 4BB configuration)

building block (BB) method

The method of connecting multiple SPARC M12-2S or SPARC M10-4S units through a high-speed interconnect, which is a unique technology developed by Fujitsu. This method enables expansion up to 16 units.

control domain console

The console for operating the control domain.

CPU Activation

The mechanism for using CPUs in units of cores in the SPARC M12/M10. CPU core resources are available with a CPU Activation key. CPU Activation may be written as "COD" or "CoD" in system messages and documents.

CPU Activation key

The key that enables use of CPU cores. You can obtain it by purchasing a CPU

Activation.

CPU memory unit (CMU)

The unit consisting of a CPU, memory, etc. Two CPU memory units can be mounted in the SPARC M12-2/M12-2S/M10-4/M10-4S. The one mounted in the lower location is called CMUL, and the one mounted in the upper location is called CMUU.

crossbar backplane unit (XBBPU)

The unit connecting the internal units composing the crossbar box with each other.

crossbar box (XBBOX)

The unit connecting building blocks (BBs) with each other in a large-scale building block configuration.

crossbar cable

The cable connecting building blocks (BBs) with each other in a building block configuration. There are two types of crossbar cables: optical cables and electrical cables.

crossbar unit (XBU)

The external interface unit connecting building blocks (BBs) with each other in a building block configuration.

degraded

The action to release a unit or function where a failure may have occurred. The action to prevent the failure from affecting the system operation.

domain

The environment where Oracle Solaris configured on the SPARC M12/M10 system can run independently. In the SPARC M12/M10 system, you can configure multiple domains and assign hardware resources to each domain.

eXtended System Control Facility (XSCF)

The firmware that controls and monitors the SPARC M12/M10.

fan unit (FANU)

The unit for cooling components inside the chassis of the SPARC M12/M10 system.

field replaceable unit (FRU)

The unit that can be used for replacement, expansion, or reduction of one of the components composing the SPARC M12/M10.

I/O board

One of the components composing the PCI expansion unit.

logical system board (LSB)

The board that is associated with a BB in order to be recognized from the domain side when the physical partition (PPAR) is configured. It means a single SPARC M12/M10.

master XSCF

The XSCF that monitors, manages, and controls the entire system in a building block configuration.

operation panel (OPNL)

The operation panel installed at the front of the server main unit.

PCI expansion unit

An optional device for PCIe slot expansion. It is connected by an optical or electrical cable via a link card mounted in the SPARC M12/M10.

physical partition (PPAR)

The unit of physical resources divided by partitioning in a configuration connecting multiple SPARC M12-2S or SPARC M10-4S units in the building block method.

physical partition dynamic reconfiguration (PPAR DR)

The function that enables the addition and deletion of hardware resources (CPU, memory, and I/O) in a physical partition while continuing business on the physical partition.

physical system board (PSB)

The smallest unit of hardware resource for the configuration of a physical partition (PPAR). It means a single SPARC M12/M10.

power distribution unit (PDU)

The unit mounted in the expansion rack to supply power to the crossbar box and SPARC M10-4S.

power supply unit (PSU)

The unit for supplying power to the SPARC M12/M10 system.

power-on self test (POST)

The diagnosis test that is automatically executed when the SPARC M12/M10 is powered on. This test determines whether memory or other hardware has failed.

PPAR configuration list

Information on the hardware resources in each physical partition.

PPAR-ID

The ID used for identifying a physical partition. You can assign up to as many PPAR-IDs as the number of SPARC M12 or SPARC M10 units in a configuration in which multiple SPARC M12-2S units or SPARC M10-4S units are connected in the building block method. For a system in a single-unit configuration, the PPAR-ID is fixed at 0.

- SPARC M12-2S/M10-4S: The PPAR-ID identifies a physical partition in a range of PPAR-ID 0 to 15.
- SPARC M12-1/M12-2/M10-1/M10-4: The PPAR-ID is fixed at 0.

PSU backplane (PSUBP)

The PSU backplane supplies power from the power supply unit to the units composing the SPARC M12/M10.

RCIL

The interface using IPMI (Intelligent Platform Management Interface) over LAN to implement functions such as power control and network state monitoring.

service processor

The processor to control and monitor the SPARC M12/M10.

slave XSCF

The state of an XSCF that is neither a master XSCF nor standby XSCF in a building block configuration. A slave XSCF operates in cooperation with the master XSCF, and it monitors, manages, and controls the unit in which it is installed.

SP to SP communication protocol (SSCP)

The protocol for an inter-XSCF network used in a building block configuration.

standby XSCF

The XSCF that operates as the backup to the master XSCF in a building block configuration. If a failure occurs in the master XSCF, the standby XSCF is switched to the master XSCF to continue system control and monitoring.

system control network

The network used for maintenance and management of the SPARC M12/M10. The term is synonymous with "XSCF network."

takeover IP address

A virtual IP address to be assigned to the master XSCF and standby XSCF in a

building block configuration. A virtual IP address allows you to log in to the master XSCF by using an invariable IP address even when the master XSCF and standby XSCF are switched to each other.

XCP

The package that includes multiple firmware for controlling the SPARC M12/M10. It consists of the following firmware:

- XSCF
- OpenBoot PROM
- Hypervisor
- Power-on self test (POST)

XSCF interface unit (XSCFIFU)

The interface unit that is mounted only in a crossbar box and connects the XSCF of the crossbar box and the one of the SPARC M12-2S/M10-4S.

XSCF network

The network used by the XSCF firmware to manage and monitor the SPARC M12/M10. The term is synonymous with "system control network."

XSCF shell

The command-line interface (CLI) function of the XSCF.

XSCF unit (XSCFU)

The unit where the service processor with the installed XSCF firmware is mounted. The unit is mounted in a crossbar box and the SPARC M12-2/M12-2S. For convenience, a component that has the mounted service processor in the SPARC M12-1/M10-1/M10-4/M10-4S is sometimes called the XSCF unit.

XSCF Web

The browser user interface (BUI) function of the XSCF firmware.

