# **Preface**

Thank you for your continuing loyalty to Fujitsu's semiconductor products.

Electronic equipment is continually becoming smaller, lighter, and less expensive while also growing more advanced in terms of function and performance. As a result, applications for semiconductor devices such as IC and LSI are rapidly increasing.

Given this environment, package technology is rapidly increasing in importance. Fujitsu is working hard to develop packages that permit improved mounting efficiency.

This data book demonstrates Fujitsu's technologies that are capable of responding to the growing diversification of packages, and includes all of Fujitsu's IC packages, from general-purpose packages to those that are still under development.

This data book is intended for engineers who are using Fujitsu packages in the design of their products, and therefore focuses on the package outline drawings.

Fujitsu Limited
Electronic Devices

# **Safety Precautions**

To prevent possible danger, damage, and bodily harm, understand and follow the precautions below to use each product safely.



#### WARNING

Inappropriate handling of a product contrary to a WARNING note could result in death or serious injury.

 Avoid contact with chemicals.
 Letting the product come into contact with an acid or alkaline chemical may generate harmful gas from dissolved product material.

# Î

#### **CAUTION**

Inappropriate handling of a product contrary to a CAUTION note may result in personal injury or damage to the product.

- Use the product only within each maximum rating. Exceeding any of the
  maximum ratings may adversely affect the features of the product, or cause the
  product to overheat, smoke or burn, producing harmful gas.
- Read the manuals for modules, cards, and hybrid products.
   When connecting any component to the main unit of the equipment, incorrect
  handling may result in malfunction or damage to the product and danger of injury
  from electric shock.
- When handling the product, use meticulous care to protect it from static electricity.
   Take measures against static electricity when handling the product. Static electricity can damage the product, adversely affect its features, or cause a malfunction.
- When designing products to be mounted, take account of the effects of heating.
   Since some products heat up considerably, handling with bare hands may result in burn injuly, or they may transfer heat to components mounted around them.



#### **CAUTION**

Inappropriate handling of a product contrary to a CAUTION note may result in personal injury or damage to the product.

•When mounting the product, satisfy the mounting conditions recommended by Fujitsu.

Disregarding any of the mounting conditions may adversely affect the features of the product or dissolve its material, producing harmful gas.

 When mounting a heat sink plate or fin on the product, be careful not to deform the product.

If the part is mounted inappropriately, it may adversely affect the features of the product.

- Be careful to avoid injury from pins.
   Some products have sharp-ended pins for functional purpose.
- Be careful during ultrasonic cleaning.
   Ultrasonic cleaning of ceramic packages or ceramic modules may adversely affect them, for example, by vibrating internal wires, resulting in breaks. For plastic packages, observe the cleaning conditions recommended by Fujitsu.
- When mounting modules, cards, or hybrid products, use a non-deforming method at an appropriate temperature.

Incorrect mounting may result in defective products.

- Do not use the product where corrosive gas is generated.
   Corrosive gas may adversely affect the features of the product, for example, by degrading its characteristics by corrosion.
- When discarding the product, refer to an authorized disposal or recycling company.

Burning the product for disposal may generate harmful gas.

Any semiconductor devices have inherently a certain rate of failure. The possibility of failure is greatly affected by the conditions in which they are used (circuit conditions, environmental conditions, etc.). This page describes precautions that must be observed to minimize the chance of failure and to obtain higher reliability from your FUJITSU semiconductor devices.

#### 1. Precautions for Product Design

This section describes precautions when designing electronic equipment using semiconductor devices.

#### 1.1 Absolute Maximum Ratings

Semiconductor devices can be permanently damaged by application of stress (voltage, current, temperature, etc.) in excess of certain established limits, called absolute maximum ratings. Do not exceed these ratings.

#### 1.2 Recommended Operating Conditions

Recommended operating conditions are normal operating ranges for the semiconductor device. All the device's electrical characteristics are warranted when operated within these ranges.

Always use semiconductor devices within the recommended operating conditions. Operation outside these ranges may adversely affect reliability and could result in device failure.

No warranty is made with respect to uses , operating conditions, or combinations not represented on the data sheet. Users considering application outside the listed conditions are advised to contact their FUJITSU sales representative beforehand.

#### 1.3 Processing and Protection of Pins

These precautions must be followed when handling the pins which connect semiconductor devices to power supply and input/output functions.

#### (a) Preventing Over-Voltage and Over-Current Conditions

Exposure to voltage or current levels in excess of maximum ratings at any pin is likely to cause deterioration within the device, and in extreme cases leads to permanent damage of the device. Try to prevent such over-voltage or over-current conditions at the design stage.

#### (b) Protection of Output Pins

Shorting of output pins to supply pins or other output pins, or connection to large capacitance can cause large current flows. Such conditions if present for extended periods of time can damage the device.

Therefore, avoid this type of connection.

#### (c) Handling of Unused Input Pins

Unconnected input pins with very high impedance levels can adversely affect stability of operation. Such pins should be connected through an appropriate resistance to a power supply pin or ground pin.

#### 1.4 Latch-up

Semiconductor devices are constructed by the formation of P-type and N-type areas on a substrate. When subjected to abnormally high voltages, internal parasitic PNPN junctions (called thyristor structures) may be formed, causing large current levels in excess of several hundred mA to flow continuously at the power supply pin. This condition is called latch-up.

**CAUTION:** The occurrence of latch-up not only causes loss of reliability in the semiconductor device, but can cause injury or damage from high heat, smoke or flame. To prevent this from happening, do the following:

- (a) Be sure that voltages applied to pins do not exceed the absolute maximum ratings. This should include attention to abnormal noise, surge levels, etc.
- (b) Be sure that abnormal current flows do not occur during the power-on sequence.

#### 1.5 Observance of Safety Regulations and Standards

Most countries in the world have established standards and regulations regarding safety, protection from electromagnetic interference, etc. Customers are requested to observe applicable regulations and standards in the design of products.

#### 1.6 Fail-Safe Design

Any semiconductor devices have inherently a certain rate of failure. You must protect against injury, damage or loss from such failures by incorporating safety design measures into your facility and equipment such as redundancy, fire protection, and prevention of over-current levels and other abnormal operating conditions.

#### 1.7 Precautions Related to Usage of Devices

FUJITSU semiconductor devices are intended for use in standard applications (computers, office automation and other office equipment, industrial, communications, and measurement equipment, personal or household devices, etc.).

**CAUTION:** Customers considering the use of our products in special applications where failure or abnormal operation may directly affect human lives or cause physical injury or property damage, or where extremely high levels of reliability are demanded (such as aerospace systems, atomic energy controls, sea floor repeaters, vehicle operating controls, medical devices for life support, etc.) are requested to consult with FUJITSU sales representatives before such use. The company will not be responsible for damages arising from such use without prior approval.

#### 2. Precautions for Package Mounting

Package mounting may be either lead insertion type or surface mount type. In either case, for heat resistance during soldering, you should only mount under FUJITSU's recommended conditions. For detailed information about mount conditions, contact your FUJITSU sales representative.

#### 2.1 Lead Insertion Type

Mounting of lead insertion type packages onto printed circuit boards may be done by two methods: direct soldering on the board, or mounting by using a socket.

Direct mounting onto boards normally involves processes for inserting leads into through-holes on the board and using the flow soldering (wave soldering) method of applying liquid solder. In this case, the soldering process usually causes leads to be subjected to thermal stress in excess of the absolute ratings for storage temperature. Mounting processes should conform to FUJITSU recommended mounting conditions.

If socket mounting is used, differences in surface treatment of the socket contacts and IC lead surfaces can lead to contact deterioration after long periods. For this reason it is recommended that the surface treatment of socket contacts and IC leads be verified before mounting.

#### 2.2 Surface Mount Type

Surface mount packaging has longer and thinner leads than lead-insertion packaging, and therefore leads are more easily deformed or bent. The use of packages with higher pin counts and narrower pin pitch results in increased susceptibility to open connections caused by deformed pins, or shorting due to solder bridges.

You must use appropriate mounting techniques. FUJITSU recommends the solder reflow method, and has established a ranking of mounting conditions for each product. Users are advised to mount packages in accordance with FUJITSU ranking of recommended conditions.

#### 2.3 Storage of Semiconductor Devices

Because plastic chip packages are formed from plastic resins, exposure to natural environmental conditions will cause absorption of moisture. During mounting, the application of heat to a package that has absorbed moisture can cause surfaces to peel, reducing moisture resistance and causing packages to crack. To prevent, do the following:

- (a) Avoid exposure to rapid temperature changes, which cause moisture to condense inside the product. Store products in locations where temperature changes are slight.
- (b) Use dry boxes for product storage. Products should be stored below 70% relative humidity, and at temperatures between 5 °C (41 °F) and 30 °C (86 °F).
- (c) When necessary, FUJITSU packages semiconductor devices in highly moisture-resistant aluminum laminate bags, with a silica gel desiccant. Devices should be sealed in their aluminum laminate bags for storage.
- (d) Avoid storing packages where they are exposed to corrosive gases or high levels of dust.

#### 2.4 Baking

Packages that have absorbed moisture may be de-moisturized by baking (heat drying). Follow the FUJITSU recommended conditions for baking.

#### 2.5 Static Electricity

Because semiconductor devices are particularly susceptible to damage by static electricity, you must take the following precautions:

- (a) Maintain relative humidity in the working environment between 40% and 70%. Use of an apparatus for ion generation may be needed to remove electricity.
- (b) Electrically ground all conveyors, solder vessels, soldering irons and peripheral equipment.
- (c) Eliminate static body electricity by the use of rings or bracelets connected to ground through high resistance (on the level of 1MW).

Wearing of conductive clothing and shoes, use of conductive floor mats and other measures to minimize shock loads is recommended.

- (d) Ground all fixtures and instruments, or protect with anti-static measures.
- (e) Avoid the use of styrofoam or other highly static-prone materials for storage of completed board assemblies.

#### 3. Precautions for Use Environment

Reliability of semiconductor devices depends on ambient temperature and other conditions as described above. For reliable performance, do the following:

(a) Humidity

Prolonged use in high humidity can lead to leakage in devices as well as printed circuit boards. If high humidity levels are anticipated, consider anti-humidity processing.

(b) Discharge of Static Electricity

When high-voltage charges exist close to semiconductor devices, discharges can cause abnormal operation. In such cases, use anti-static measures or processing to prevent discharges.

(c) Corrosive Gases, Dust, or Oil

Exposure to corrosive gases or contact with dust or oil may lead to chemical reactions that will adversely affect the device. If you use devices in such conditions, consider ways to prevent such exposure or to protect the devices.

(d) Radiation, Including Cosmic Radiation

Most devices are not designed for environments involving exposure to radiation or cosmic radiation. Users should provide shielding as appropriate.

(e) Smoke, Flame

**CAUTION:** Plastic molded devices are flammable, and therefore should not be used near combustible substances. If devices begin to smoke or burn, there is danger of the release of toxic gases.

Customers considering the use of FUJITSU products in other special environmental conditions should consult with FUJITSU sales representatives.

# Organization of This Data Book

This data book consists of six chapters.

#### **Chapter 1: Introduction to Packages**

This chapter provides an overview of packages, and describes their organization, forms, and structure, and also discusses future trends in packages.

#### **Chapter 2: Package Mounting Methods**

This chapter explains mounting methods, humidity resistance characteristics, and handling, focusing especially on surface mounting packages since they require particular care in terms of mounting techniques.

#### **Chapter 3: Package Lineup**

This chapter shows the correspondence between package form and the number of pins, and lists the package lineup.

#### **Chapter 4: Package Outline Diagrams**

This chapter first describes how the package dimensions are displayed and also explains the package codes. The remainder of the chapter is devoted to the package outline diagrams, showing one package per page.

#### Chapter 5: Sockets

This chapter explains sockets.

#### **Chapter 6: Packaging for Shipment**

This chapter explains packaging for shipment.

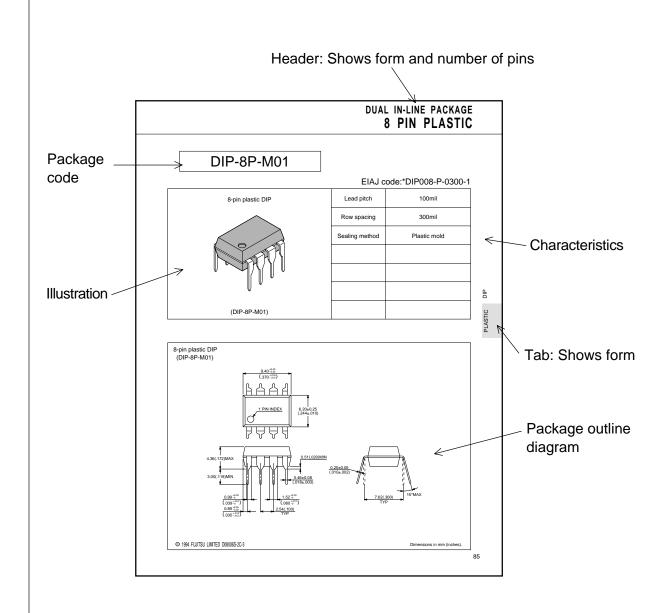
# **How to Use This Document**

When you want to find a particular piece of information within a given section, there are the following additional means for locating that information, aside from the normal table of contents and index:

- Searching for information in the package lineup

  The package lineup is displayed in charts grouped according to the package
  form and material. The package codes are listed in the chart in sequence,
  starting from the least number of pins. (Refer to section 2 of chapter 3.)
- Searching for information from the package form and number of pins
  The thumb indices and headers are convenient. Each page in the package
  outline diagram section has a thumb index and a header. The thumb index
  indicates the package form, while the header indicates the form and the number
  of pins.

# Package Outline Diagram Page Layout Used in This Data Book



#### 1.1 Overview

Fujitsu provides semiconductor packages as a kind of "interposers" for protecting semiconductor devices and getting the full benefit of them. Fujitsu has developed and released a diversified series of "general-purpose package families" supporting a wide range of applications to suit customers' needs. The packages include through-hole type packages such as DIPs and PGAs; QFPs and SOPs that contributed to setting the trend of surface mounting; and multi-pin QFPs, TCPs, and SVPs supporting high-tensity mounting.

In addition, Fujitsu has developed and provided custom packages, cards, and modules for specific customers.

This chapter begins with Fujitsu's package lineup, followed by descriptions of package shapes and structures.

This chapter also describes the package dimension display conventions and package code based on the EIAJ and JEDEC<sup>\*1</sup> standards to help you use this data book more efficiently as a source of information for you.

Also, this chapter introduces Fujitsu's basic concept of package development for future packages.

The electronic device marketplace has been demanding more advanced and diversified highdensity mounting technologies.

Fujitsu has developed new packages such as SONs and FBGAs to meet the needs of the industry. To support customers for easier use of these new packages, at the same time, Fujitsu has made a strong commitment to standardization of the packages by EIAJ\*2.

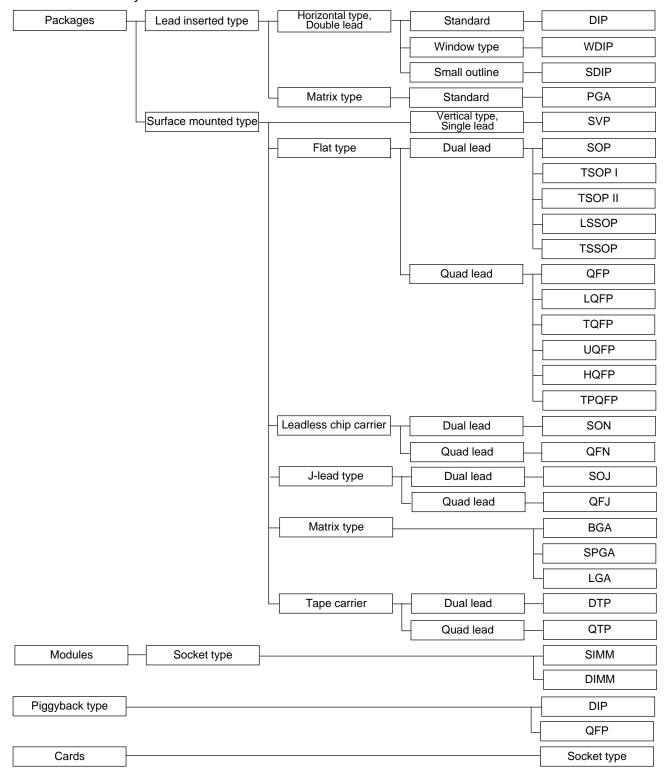
\*1: Joint Electron Device Engineering Council

\*2: Electronic Industries Association of Japan

# **Package Lineup**

#### 1.2 Package Lineup

The packages are classified as follows, according to form, material, and the mounting methods for which they are suited.



# Package Lineup

Name of package	Description	Lead pitch *1 (mm)	Row space *1 (mm)	
DIP	Dual In-line Package	2.54	7.62/10.16/15.24/22.86	
SH-DIP*3	Shrink Dual In-line Package	1.778	_	
SK-DIP*3	Skinny Dual In-line Package	2.54	7.62	
SL-DIP*3	Slim Dual In-line Package	2.54	10.16	
SZIP	Shrink Zig-Zag In-line Package	0.89	1.778	
PGA	Pin Grid Array Package	1.27/2.54	_	
SVP	Surface Vertical Package	0.5/0.65	_	
SOP	Small Outline Package (straight lead) Small Outline L-Leaded Package	1.27	_	
SOL*3	Small Outline L-Leaded Package(JEDEC*2)	1.27	_	
SSOP	Shrink Small Outline L-Leaded Package	0.65/0.80/1.00	_	
TSOP (I)	Thin Small Outline L-Leaded Package (I)	0.50/0.55/0.60	_	
TSOP (II)	Thin Small Outline L-Leaded Package (II)	0.50/0.80/ 1.00/1.27	_	
SON	Small Outline Non-Leaded Package	0.50/1.00	_	
QFP	Quad Flat Package(straight lead) Quad Flat L-Leaded Package	0.40/0.50/ 0.65/0.80/ 1.00	_	
LQFP*3	Lowprofile Quad Flat L-Leaded Package	0.40/0.50/ 0.65/0.80	_	
TQFP	Thin Quad Flat L-Leaded Package	0.40/0.50	_	
HQFP	QFP with Heat Sink	0.40/0.50/ 0.65	_	
TPQFP	QFP with Test Pad	0.30	_	
LCC*3	Leadless Chip Carrier	1.016/1.27		
QFN	Quad Flat Non-Leaded Package (EIAJ)	1.010/1.27	_	
PCLP*3	Printed Circuit-board Leadless Package	0.50/0.65	_	
QFJ	Quad Flat J-Leaded Package (EIAJ)	1.27	_	
SOJ	Small Outline J-Leaded Package	1.27	_	
BGA	Ball Grid Array	1.5/1.27/1.0	_	
DTP	Dual Tape Carrier Package	_	_	
QTP	Quad Tape Carrier Package	_	_	
SIMM	Single Inline Memory Module	1.27/2.54	_	
DIMM	Dual Inline Memory Module	1.27	_	

Row space

\*1: These columns indicate the dimensions shown at right.

\*2: Joint Electron Device Engineering Council

\*3: Package name used by Fujitsu

Lead pitch 100 mil = 2.54 mm

## **Package Forms**

#### 1.3 Package Forms

Packages can be broadly classified into two types according to the mounting method used:

Lead inserted type: The leads on the package are inserted into through holes in a printed circuit board, etc., and then soldered in place.

Surface mounted type: The device lays flat on surface of the circuit board and the leads are soldered directly to the wires.

In addition, each of the various package forms has its own unique features.

#### 1.3.1 Lead insertion types

Illustration	Name of package	Features	Lead pitch
	DIP	The leads on this package extend down from the sides of the package in two rows. This is currently the most typical standard package. The row spacing varies according to the number of pins, as follows:  8 to 20 pins: 300 mil 24 to 52 pins: 600 mil 22 to 28 pins: 400 mil 64 pins or more: 900 mil	Standard : 100 mil
	SH-DIP'	This is a standard DIP with the lead pitch reduced from 100 mil to 70 mil (1.778 mm). In some versions, both the lead pitch and the row spacing are reduced. The benefit of the reduced pitch is greatest when there are a large number of leads.	Standard: 70 mil
ANN MARKET STATE OF THE STATE O	SK-DIP' SL-DIP'	This is a standard DIP with the row spacing reduced to 300 mil in the case of the "SK-DIP" and 400 mil in the case of the "SL-DIP." SK-DIP: 300 mil, 22/22/28/32 pins SK-DIP: 400 mil, 24/28 pins	Standard : 100 mil
	PGA	The leads on this package extend straight down from the bottom of the package in a grid arrangement. This package is suited for high-density mounting of packages with 64 or more pins. A special version with a lead pitch of 50 mil is available.	Standard : 100 mil
	SVP	This type of package is placed perpendicular to the printed circuit board and can then be surface mounted.	0.50mm 0.65mm
THE PROPERTY OF THE PARTY OF TH	SOP SOL	The leads on these packages extend out from two edges of the package; the leads are either gullwing (L-shaped) or straight. Packages that conform with JEDEC specifications are called "SOL".	Standard: 50 mil

<sup>\*:</sup> Package name used by Fujitsu.

## 1.3.2 Surface mounted types

Illustration	Name of package	Features	Lead pitch		
	QFP	The leads on this package extend out from four sides of the package; the leads are either gullwing (L-shaped) or straight.	1.00mm 0.80mm 0.65mm		
	SSOP LQFP	These packages are compact versions of the SOP and QFP. (The lead pitch and body size are smaller.)	SSOP:0.65mm/0.80mm/ 1.00mm LQFP:0.40mm/0.50mm		
	TSOP TQFP	These packages are thinner versions of the SOP and QFP. (Mounted height: 1.27 mm max.)	TSOP: 0.50mm/ 0.55mm/0.60mm TQFP: 0.40mm/0.50mm		
	SON	This type of package has external electrodes provided in two directions on the surface of the package. The package is a smaller version of the TSOP. It can be handled easily because of no bent leads.	0.5mm/1.0mm		
	TPQFP	This is a fine-pitch QFP package with fixed test pads located around the periphery of the package and body. Excellent lead precision is possible by mounting a holder.	0.30mm		
	LCC QFN	This package has no leads; instead, it has only electrode pads for soldering. A ceramic leadless chip carrier is a compact, high-reliability representative of this type of package.	Standard: 50 mil Among LCCs with many pads, 40-mil, 25-mil and other fine-pitch packages are currently under development.		
	PCLP	This package has no leads; instead, it has only electrode pads for soldering. A plastic leadless chip carrier is a compact representative of this type of package.	0.50mm 0.65mm		

<sup>\*:</sup> Package name used by Fujitsu.

# **Package Forms**

## (continued)

Illustration	Name of package	Features	Lead pitch
THE REAL PROPERTY OF THE PARTY	QFJ SOJ	The leads on this type of package are bent down from the sides of the package in a J shape. Of these packages, those with leads on Quad are called QFJ packages, while those with leads on dual are called SOJ packages.	Standard: 50 mil
	DTP QTP	This type of package, generally called a "TAB package," consists of an IC chip mounted by means of TAB technology on a tape on which the wiring pattern is formed; the chip is then coated with resin. This package is suited for the increasing number of pins required in chips and for high-density mounting. There are three tape widths: 35 mm, 48 mm, and 70 mm.	0.50 to 0.15mm

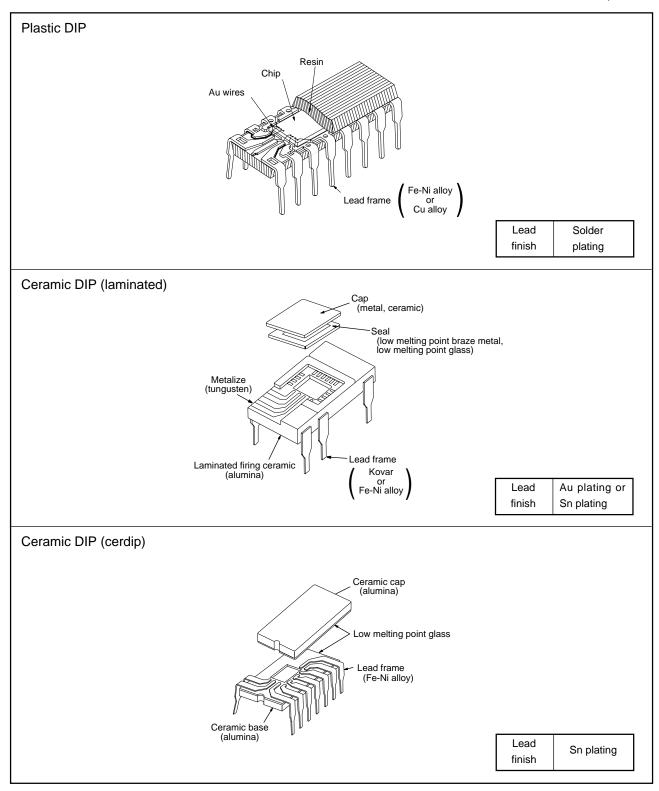
## 1.3.3 SMD module, piggyback, card

Illustration	Name of package	Features
	SMD module	This module consists of multiple small surface mounted packages (SMDs) on a ceramic or resin motherboard. These modules are primarily used for memory and permit higher densities and more advanced systems. The pins are arranged in either a SIP, DIP or ZIP pattern. The module is also available in a socket form that permits easy insertion and removal for future memory expansion.
	Piggyback	This package consists of a ceramic package with a socket mounted on it, and can be used to plug in LCC, DIP and other types of packages. The pins are arranged in either DIP form or QFP form. This type of package is used for program evaluation and system operation testing in the development of microcomputer-based systems.
	Card	This type of package consists of multiple elements or chip-type passive elements mounted on a resin wiring substrate. Cards are used for PC cards, DRAM cards, miniature cards, etc.

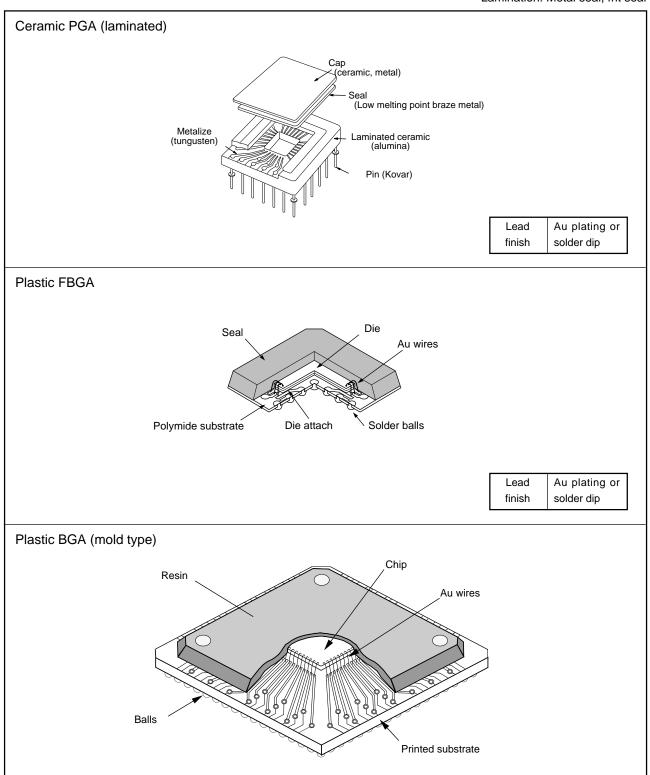
## 1.4.1 Structure diagrams

Structure diagrams for typical packages are shown below.

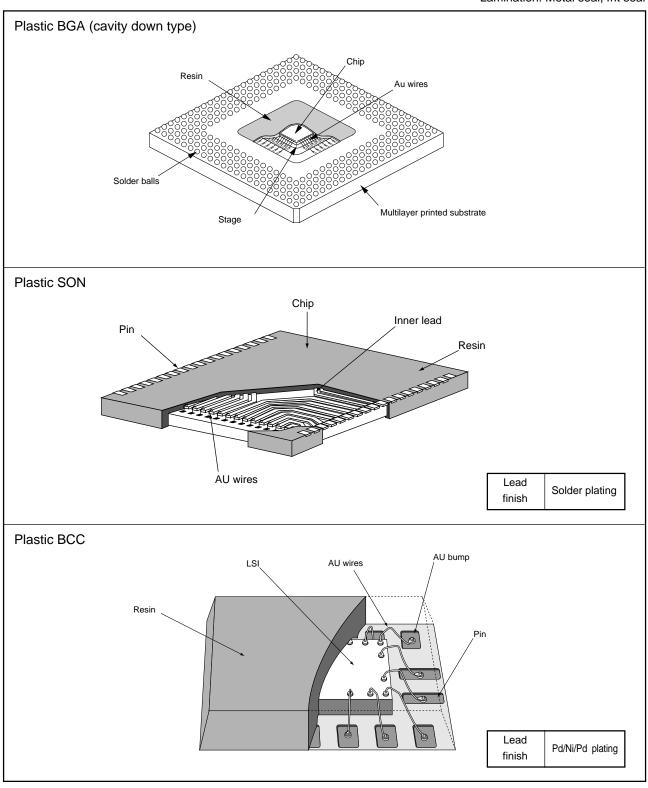
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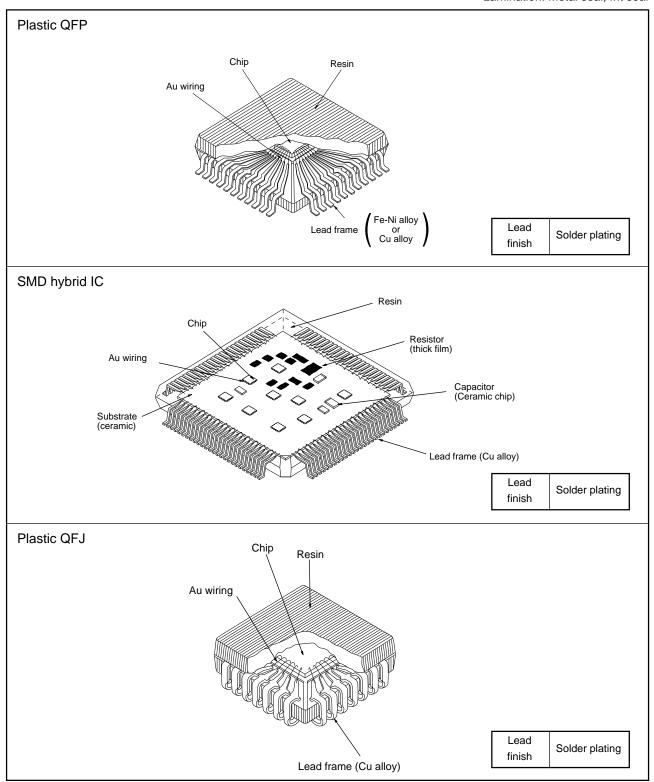
Lamination: Metal seal, frit seal



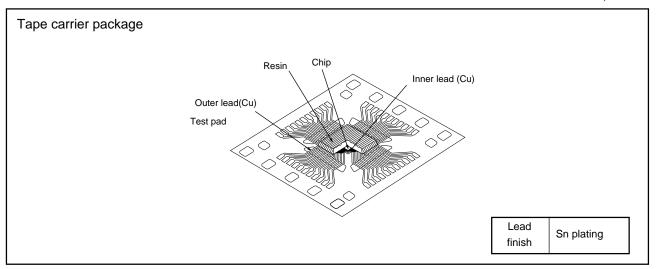
Lamination: Metal seal, frit seal



Lamination: Metal seal, frit seal



Lamination: Metal seal, frit seal



#### 1.4.2 Structural materials

Some of the materials of which packages are composed are described below. In addition, their general characteristics are shown in Table 1.

Al <sub>2</sub> O <sub>3</sub> 90 to 95%. Used as a substrate material in typical ceramic packages. Substrates are divided into several different types according to the percentage content of Al <sub>2</sub> O <sub>3</sub> , with each
demonstrating slightly different physical properties.
Primary components include PbO, B <sub>2</sub> O <sub>3</sub> , SiO <sub>2</sub> , and Al <sub>2</sub> O <sub>3</sub> . Primarily
used for seal between the ceramic substrate and the lead frame in
cerdip packages, or for sealing the ceramic cap on a laminated ceramic package.
Raw material for plastic packages; phenol-hardened epoxy resin is primarily used.
An iron-nickel-copper alloy. Because it has a coefficient of thermal expansion near that of ceramics, it is used primarily for metal caps and external leads in laminated ceramic packages.
Iron-nickel alloy (42% nickel). Generally used as the lead frame
material in cerdip packages and plastic packages. Also used as
external lead material in laminated ceramic packages.
A copper alloy (a copper-nickel-tin alloy) is used as the lead frame
material in plastic packages. Also used as a structural material in
ceramic packages. When lowering thermal resistance is an
objective, a copper film, a copper-molybdenum compound or a
copper alloy may be used as the intermediate metallic material
between the bottom of the chip and the heat dissipation fins.
Copper has also recently gained attention for use in bonding wires.
Raw material for metallized paste used in the wiring patterns
(internal wiring) of laminated ceramic packages. The paste is
screen printed on the unsintered ceramic substrate and is then
sintered simultaneously with the ceramic.
A molybdenum film is sometimes used for the bottom substrate in a
chip in order to increase the heat dissipation effect of a ceramic
package. A molybdenum-manganese paste is also sometimes used for the metallized paste.

Silver (Ag)	There are partially silver-plated inner pattern tips and portions of
	the stage with chip in the lead frame of a plastic package. Silver is
	also used in the metallized paste used in the chip mount in a cerdip
	package. A silver paste is also used as an adhesive between the
	chip and substrate.
Aluminum (Al)	Used as a wire material for wire bonding (ultrasonic type). In
	addition, aluminum is sometimes vapor deposited or pressed onto
	the tips of the inner pattern of the lead frame in a cerdip package for
	its bonding characteristics. Aluminum is also often used for heat
	dissipation fins.
Gold (Au)	Used as a wire material for wire bonding (nailhead type). Gold
	plating is also often used for the metallized pattern and external
	leads in a laminated ceramic package.
Tin (Sn)	The external leads of most cerdip packages are often tin-plated. A
	gold-tin alloy (20% tin) is also used as a sealing solder for the metal
	cap on a ceramic package.
Solder (Pb/Sn)	Solder with slightly different characteristics can be obtained by
	altering the lead/tin ratio. At present, the external leads of most
	plastic packages are plated with a mixture of lead and tin
	(commonly referred to as "solder plating"). A lead-tin mixture is
	also used as a sealing braze for the metal cap on a ceramic
	package. This mixture is also used for the solder dip for external
	leads.
Polyimide tape	This is the primary material in the tape used for TCP. This tape is
	generally made from pyromellitic dianhydride and aromatic
	diamine. In addition to the ability to withstand high temperatures,
	this tape also possesses excellent mechanical, electrical, and
	chemical characteristics.

**Table 1. General Characteristics of Package Materials** 

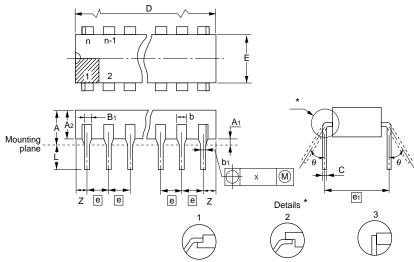
Alumina (Al <sub>2</sub> O <sub>3</sub> 90% to 95%)	Low melting point glass (LS-0110)	Kovar	42alloy	Tungsten (W)	Molybde- num (Mo)	Al	Au	Ag	Cu	Epoxy resin	Pb/Sn (eutectic)	Sn
3.6 to 3.9	4.8	8.4	8.2	19.1	10.2	2.69	19.3	10.5	8.93	to 1.8	8.45	7.3
to 6.5 to 7.0	to 5.3	(5.1 to 5.5)	(6.7 to 7.4)	to 4.5	(3.7 to 5.3)	(23 to 29)	to 14	to 19	to 16.7	to 18	24.5	to 22
0.04	0.0032	0.04	0.03	0.423	0.342	0.569	0.762	1.022	0.963	to 1.4x 10 <sup>-3</sup> >	0.122	0.16
to 0.20	_	0.11	_	0.033	0.072	0.21	0.030	0.056	to 0.091	_	to 0.042	0.05
10 <sup>12</sup> to 10 <sup>14</sup>	to 10 <sup>9</sup>	49x10 <sup>-6</sup>	58x10 <sup>-6</sup>	5.5x10 <sup>-6</sup>	5.6x10 <sup>-6</sup>	2.75x10 <sup>-6</sup>	2.4x10 <sup>-6</sup>	1.62x10 <sup>-6</sup>	1.72x10 <sup>-6</sup>	10¹⁵≥	17x10 <sup>-6</sup>	11.5x10 <sup>-6</sup>
8.7 to 9.6	to 12	_	_	_	_	_	_	_	_	to 4.3	_	_
6.5 to 8.9	to 19	_	_	_	_	_	_	_	_	_	_	_
to 1100 to 1300	_	to160	170 to 240>	250 to 490	_	_	_	_	to 80	_	_	_
26 to 30	6.87	to14	(15 to 16)	to 37	to 35	7.17	to 7.95	to 8.1	to 12.5	1.4<	to 3.2	4.99
_	_	(5000 to 9000)	_	(1300 to 47000)	_	to1020	(2040 to 2550)	_	to 2500	_	to 560	_
to 2100 to 2800	to 450	_	_	to 5200	_	_	_	_	_	to 1000>	_	_
	90% to 95%)  3.6 to 3.9  to 6.5 to 7.0  0.04  to 0.20  10 <sup>12</sup> to 10 <sup>14</sup> 8.7 to 9.6  6.5 to 8.9  to 1100 to 1300  26 to 30  —	Alumina (Al2O3 gook to 95%)  3.6 to 3.9  4.8  to 6.5 to 7.0  to 5.3  0.04  0.0032  to 0.20  —  1012 to 1014  to 109  8.7 to 9.6  to 12  6.5 to 8.9  to 19  to 1100 to 1300  —  26 to 30  6.87  —  —  —	Alumina (Al2O3 90% to 95%)   melting point glass (LS-0110)    3.6 to 3.9   4.8   8.4    to 6.5 to 7.0   to 5.3   (5.1 to 5.5)    0.04   0.0032   0.04    to 0.20   —   0.11    10¹² to 10¹⁴   to 10°   49x10⁻⁶    8.7 to 9.6   to 12   —    6.5 to 8.9   to 19   —    to 1100 to 1300   —   to 160    26 to 30   6.87   to 14    —   —   (5000 to 9000)	Alumina (Al <sub>2</sub> O <sub>3</sub> 90% to 95%)   melting point glass (LS-0110)   Kovar   42alloy    3.6 to 3.9   4.8   8.4   8.2    to 6.5 to 7.0   to 5.3   (5.1 to 5.5)   (6.7 to 7.4)    0.04   0.0032   0.04   0.03    to 0.20   —   0.11   —    10¹² to 10¹⁴   to 10⁰   49x10⁻⁰   58x10⁻⁰    8.7 to 9.6   to 12   —   —    6.5 to 8.9   to 19   —   —    to 1100 to 1300   —   to 160   170 to 240 >  26 to 30   6.87   to 14   (15 to 16)    —   —   (5000 to 9000)   —	Alumina (Al <sub>2</sub> O <sub>3</sub> point glass (LS-0110)   Kovar 42alloy   Tungsten (W)    3.6 to 3.9	Alumina (AlzOs 90% to 95%) point glass (LS-0110) Kovar 42alloy Iungsten (W) Molybde- point glass (LS-0110) to 5.3 (5.1 to 5.5) (6.7 to 7.4) to 4.5 (3.7 to 5.3)  0.04 0.0032 0.04 0.03 0.423 0.342  to 0.20 — 0.11 — 0.033 0.072  10¹² to 10¹⁴ to 10⁰ 49x10⁻⁶ 58x10⁻⁶ 5.5x10⁻⁶ 5.6x10⁻⁶  8.7 to 9.6 to 12 — — — —  6.5 to 8.9 to 19 — — — —  to 1100 to 1300 — to 160 170 to 240 > 490  — 26 to 30 6.87 to 14 (15 to 16) to 37 to 35  — (5000 to 9000) — (1300 to 47000) —	Alumina (AlzO <sub>3</sub> 90% to 95%) point glass (LS-0110) Rovar 42alloy lungsten (W) Molybde-num (Mo) Al 3.6 to 3.9 4.8 8.4 8.2 19.1 10.2 2.69  to 6.5 to 7.0 to 5.3 (5.1 to 5.5) (6.7 to 7.4) to 4.5 (3.7 to 5.3) (23 to 5.5) 0.04 0.03 0.423 0.342 0.569  to 0.20 — 0.11 — 0.033 0.072 0.21  10¹² to 10¹⁴ to 10³ 49x10⁻⁶ 58x10⁻⁶ 5.5x10⁻⁶ 5.6x10⁻⁶ 2.75x10⁻⁶ 8.7 to 9.6 to 12 — — — — — — — — 10 1100 to 1300 — to 160 170 to 240> 250 to 490 — — 26 to 30 6.87 to 14 (15 to 16) 12 to 37 to 35 7.17  — (5000 to 9000) — (1300 to 47000) — to 1020	All Multina (AlaCos 90% to 95%) reliting (ILS-0110) rolling also (ILS-0110) rolling (ILS-0110) rolling (ILS-0110) rolling rolling (ILS-0110) rolling r	Alumina (Al-U)	Alumina (Alco)   Point rights   Po	Alumina (AlcOs) 90% to 95%)         melting (CS-0110) (CS-0110)         Kovar (A2alloy 1ungsten (W))         Molybde-num (Mo)         Al         Au         Ag         Cu         Epoxy resin           3.6 to 3.9         4.8         8.4         8.2         19.1         10.2         2.69         19.3         10.5         8.93         to 1.8           1 to 6.5 to 7.0         1 to 5.3         (5.1 to 5.5)         (6.7 to 7.4)         10.4.5         (3.7 to 5.3)         (23 to 29)         10.14         to 19         10.16.7         to 18           0.04         0.0032         0.04         0.03         0.423         0.342         0.569         0.762         1.022         0.963         10.1.4x 10.3           10 0.20         —         0.11         —         0.033         0.072         0.21         0.030         0.056         to 0.091         —           10 12 to 10 10         10 10 9         49x10 4         58x10 5         5.5x10 5         5.6x10 6         2.75x10 6         2.4x10 5         1.62x10 6         1.72x10 5         10 10 5           8.7 to 9.6         to 19         —         —         —         —         —         —         —         —         —         —         10 4.3           10 1100 to	Alumina (AlC) general poly (US-0110)   Kovar   42alloy   Lungsten   Molybdo-num (Mo)   Al   Au   Ag   Cu   Polys (eutectic)    3.6 to 3.9   4.8   8.4   8.2   19.1   10.2   2.69   19.3   10.5   8.93   to 1.8   8.45    10 6.5 to 7.0   10 5.3   (5.1 to 5.5)   (6.7 to 5.5)   (7.4)   to 4.5   (3.7 to 2.9)   10 14   to 19   to 16.7   to 18   24.5    0.04   0.0032   0.04   0.03   0.423   0.342   0.569   0.762   1.022   0.963   to 1.4x   0.122    10 0.20   —   0.11   —   0.033   0.072   0.21   0.030   0.056   to 0.091   —   to 0.042    10 12 to 10 14   to 10   49x10   58x10   5.5x10   5.6x10   5.6x10   2.75x10   2.4x10   1.62x10   1.72x10   10 1   10 10    8.7 to 9.6   to 19   —   —   —   —   —   —   —   —   —

# **How Package Dimensions Are Indicated**

## 1.5 How Package Dimensions Are Indicated

This section will use representative DIP and FLAT (SOP) packages to explain the manner in which dimensions are indicated in the package outline dimension diagrams in this data book.

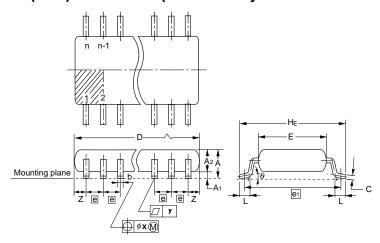
### 1.5.1 DIP dimensions (in accordance with EIAJ IC-74-3)



Dimension name	Symbol	Explanation
Mounting height	Α	Height from the mounting surface to the top of the package
Standoff height	<b>A</b> 1	Distance between the mounting surface and the bottom of the package
Height of body	<b>A</b> 2	Height (thickness) of the package body
Pin width	b <sub>1</sub>	Width of the portion of the pin inserted into the mounting hole in the printed circuit board, etc.
Maximum pin width	b, B1	Maximum width of the pin
Pin thickness	С	Thickness of the pin
Package length	D	The longest dimension of the body of the package parallel to the mounting surface and excluding the pins; also include resin burrs
Package width	E	The width of the body of the package, excluding the pins
Pin linear spacing	е	Linear spacing between the centers of the pins; also called the "lead pitch"
Pin linear spacing	e <sub>1</sub>	Width between the rows of the pins; also called the "row spacing"
Pin length	L	Length from the mounting surface to the tip of the pin
Pin angle	θ	Angle of spread between the pin and a line perpendicular to the mounting surface
Overhang	Z	Distance from the center position of an end pin to the end of the body of the package

# **How Package Dimensions Are Indicated**

### 1.5.2 FLAT (SOP) dimensions (in conformity with EIAJ IC-74-2)



Symbol	Explanation
Α	Height from the mounting surface to the top of the package
<b>A</b> 1	Distance between the mounting surface and the bottom of the package
<b>A</b> 2	Thickness of the package (height of the body)
b	Width of the pin
С	Thickness of the pin
D	The longest dimension of the body of the package parallel to the
	mounting surface and excluding the pins; also include resin burrs
E	The width of the body of the package, excluding the pins
е	Linear spacing between the centers of the pins; also called the "lead pitch"
	Distance between the centers of the pads where the package is mounted;
<b>e</b> ₁	in the case of flat packages, there are generally four standard values:
	TYPEI :225mil (5.72mm)
	TYPEII :300mil (7.62mm)
	TYPEIII :375mil (9.53mm)
	TYPEIV :450mil (11.43mm)
	TYPEV :525mil (13.34mm)
HE	TYPE VI :600mil (15.24mm)
L	Distance from the tip of one pin to the tip of the pin on the opposite side
	of the package
θ	Length of the flat portion of the pin that comes into contact with the
	mounting pad
Z	Angle formed by the mounting surface and the flat portion of the pin
	Distance from the center position of an end pin to the end of the body
	of the package
$\bigoplus \phi \times M$	Shows the tolerance for the center position of the pin in the package
	outline diagram
	Shows the uniformity of the pin bottoms in the package outline diagram
	A A1 A2 b c D E e  Φ Δ

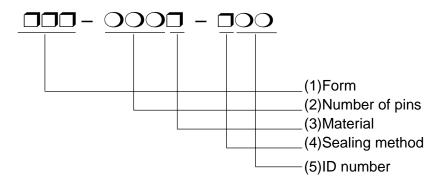
The information provided above is a simplified explanation. If you have inquiries concerning dimensions, confirm the "dimension name" shown in the preceding tables.

#### 1.6 Package Codes

#### 1.6.1 Fujitsu Code Labeling

Distinctions among package forms, number of pins, material, sealing method, etc., as well as classification between packages and modules are shown in the package code as follows.

Packages (excluding TCPs)



(1) Form: Indicates the form of the package. (three letters)

DIP: Indicates a DIP-type package (including SH, SK, and SL).

PGA: Indicates a PGA-type package

FPT: Indicates a flat-type package

LCC: Indicates an LCC-, QFJ-, or SOJ-type package

CRD: Indicates a card.

(2) Number of pins: Indicates the number of pins.

(3) Material: Indicates the package material. (one letter)

P: Plastic

C: Ceramic

(4) Sealing method: Indicates the package sealing method. (one letter)

M: Plastic mold

A: Metal seal

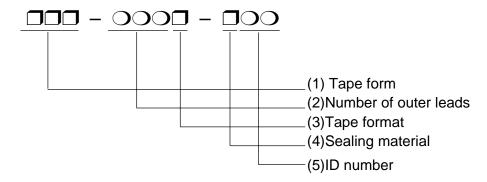
F: Frit seal

C: Cerdip

(5) ID number: An ID number within the form. (two digits)

# **Package Codes**

Packages (TCP)

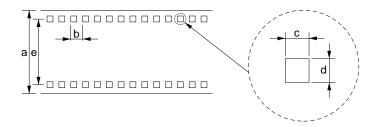


(1) Tape form: Indicates the tape form of the package. (three letters)

DTP: TCP with leads on two sides

QTP: TCP with leads on four sides

- (2) Number of outer leads: Indicates the number of outer leads that are actually used.
- (3) Tape format: Indicates the tape format. (a letter from A to F)



(Dimensions in mm)

Symbol						
Letter in code	Name	а	b	С	d	е
А	35 mm superwide	34.975	4.750	1.420	1.420	31.820
В	48 mm superwide	48.175	4.750	1.420	1.420	44.860
С	70 mm superwide	66.800	4.750	1.420	1.420	66.800
D	35 mm wide	34.975	4.750	1.981	1.981	28.977
Е	48 mm wide	48.175	4.750	1.981	1.981	42.177
F	70 mm wide	69.950	4.750	1.981	1.981	63.949

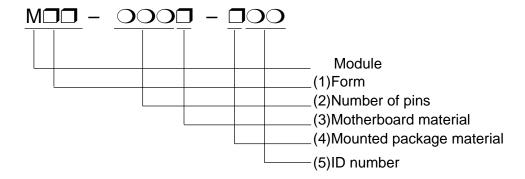
(4) Sealing method: Indicates the package sealing method. (one letter)

M: Resin sealed

B: Not sealed

(5) ID number: An ID number within the form. (two digits)

#### Modules



(1) Form: Indicates the form of the module. (three letters)

DP: DIP type

QP: QFP type

TP: DIP type with 100-mil (2.54 mm) row spacing

SS: Socket mounted type

- (2) Number of pins: Indicates the number of pins. (two or three digits)
- (3) Motherboard material: Indicates the motherboard material. (one letter)

P: Plastic

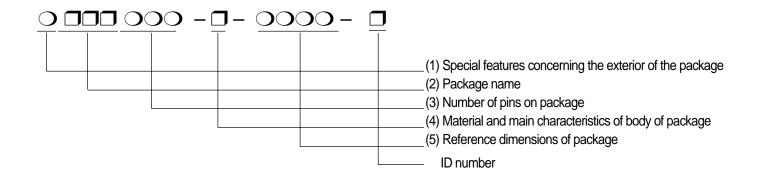
C: Ceramic

- (4) Mounted package material: Indicates the material of the package that is mounted. (one letter)
  - P: Plastic
  - C: Ceramic
- (5) ID number: An ID number within the form. (two digits)

## **Package Codes**

#### 1.6.2 EIAJ code labeling

Section 1.6.1 explained the codes used by Fujitsu. This section explains the EIAJ codes.



- (1) Special features concerning the exterior of the package
  - \*: Standard package
  - S: Standard package with compressed lead pitch
  - H: Package with heat sink
  - W: Package with transparent window
  - A: Piggyback package
  - T: Package with a mounting height of 1.27mm (0.050 inches) or less
- (2) Package name

Indicates either SIP, ZIP, DIP, PGA, SOP, SOL, SOJ, QFP, QFJ, or QFN.

(3) Number of pins on package

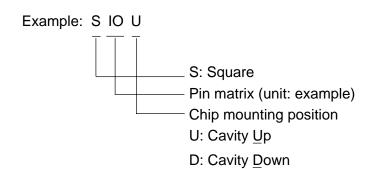
Basically indicates the total number of pins present. If the number of pins is 1000 or more, four digits are used.

- (4) Material and main characteristics of body of package
  - C: Airtight ceramic package sealed with metal
  - G: Airtight ceramic package sealed with glass
  - P: Package formed of resin
  - R: Package formed from plastic and glass compound substrate
  - X: Package not covered by any of the other designations

#### (5) Package reference dimensions

DIP: Package pin row spacing (unit: mil)

PAG: Refer to the following example:



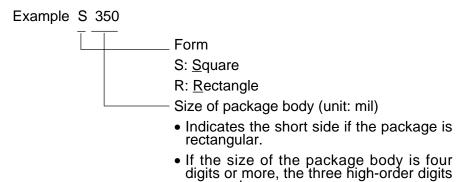
Cavity up: The chip mounting position is on the top of the package Cavity down: The chip mounting position is on the bottom of the package

SOP: Spacing between centers of mounting pads (unit: mil)

SOP/SOJ: Width of package body (unit: mil)

QFP: Size of package body (unit: mm)

QFJ, QFN: Refer to the following example:



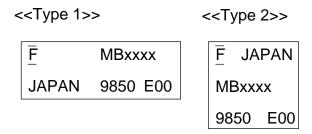
are used.

#### 1.7 Marking

Marking includes Fujitsu's standard marking and customer-specified marking. Section 1.7.1 shows the format for standard marking; if customer-specified marking is desired, the customer should establish the marking specifications while observing the restrictions shown in section 1.7.2 Note that in the case of customer-specified marking, the Engineering Samples (ES) will bear the standard marking, and the Commercial Samples (CS) will bear the customer-specified marking.

If a format other than those shown in this data book is desired, consult with the Fujitsu sales office beforehand.

#### 1.7.1 Standard marking



Note: The <<Type 1>> and <<Type 2>> formats are the basic formats; there are other simpler formats based on the lot number and control number for cases where space is limited, etc.

However, the lot number indication in the case of hybrid ICs is as follows.

IX 03 (Example)

—Manufacturing serial number (count of lots for this month)

—Code for month of manufacture: 1 to 9: January to September;

X: October; Y: November; Z: December

—Code for year of manufacture: Last digit of year

E01 (Example) .. Fujitsu's control number

# **Marking**

#### 1.7.2 Customer-specified marking

If needed for custom ICs, etc., marking can be specified as indicated below.

#### Marking format

(1) One line for the customer product name (the customer part number) can be added to Fujitsu's standard marking format.



(2) Fujitsu's mark can be replaced with the customer's company mark.

If the customer's company mark is to be required, a camara-ready copy must be submitted.

If marking other than that described above is desired, or if the above format is not feasible due to space limitations, etc., special consultation will be necessary.

Note that the lot number and control number are administrative numbers required by Fujitsu's specifications, and cannot be omitted.

#### 1.8 Future Trends in Packages

#### 1.8.1 Diversification

Semiconductor packages can be broadly classified into two types: pin inserted types and surface mounted types. The main package format has changed from DIP to types such as SOP, QFP, and PGA. In addition, a package is now expected to provide the following features:

- High-density mounting in order to permit lighter and smaller designs as more equipment becomes portable
- Multiple I/O pins, required as devices are integrated on larger scales and more functions are offered
- Faster speed
- Lower cost

Given the balance between mounting technology and the design standards for the reference printed circuit board that serves as the mounting platform, progress in the area of surface mounting and leadless packages (except for vertical packages) should be attainable.

Development is already progressing on representative types such as BGAs and CSPs. The features of each of these types and their future direction of development are described below:

- SOPs are mainly suited for packages with up to 100 pins. There are versions in which
  the pitch is even smaller or the package profile is even lower, such as TSOPs and
  UTSOPs, and the trend is towards CSPs. One variation is the SVP, as progress in
  utilizing all three dimensions is made in order to permit high-density mounting of
  memory.
- QFP normally have from 50 to 300 pins.Packages for an even smaller pitch are in progress and being deployed into QTPs and TPQFPs using tape carriers.
- PGAs are a package type suited for ICs with a large number of pins (200 to 500 pins).
   SPGAs offer an even narrow pitch, and BGAs are being developed for the future.

#### 1.8.2 Future formats

In the future, due to the demand for high-density mounting, surface mounted packages will grow in number, while the demand for higher speeds will drive the growth of leadless packages.

Cost requirements will cause growth in plastic packages, while the characteristics of ceramic packages will make them required for applications that demand high reliability, for devices that operate at high speeds and consume a lot of power, and for large chips.

With these trends in mind, Fujitsu's own package development efforts will continue to emphasize mounting efficiency while paying attention to the need for compatibility with the JEDEC\*1 standards, the EIAJ\*2 standards, and packages from other manufacturers.

- \*1:Joint Electron Device Engineering Council
- \*2:Electronic Industries Association of JAPAN

## **Future Trends in Packages**

#### 1.8.3 Custom packages

In addition to the increasingly important diversity of product types, there is also a growing trend towards diversity among semiconductor types and mounting methods. As a manufacturer of ASICs, it is important for Fujitsu to be able to quickly grasp market trends and make strategic contributions to customer product differentiation efforts.

At Fujitsu, in addition to promoting new standard packages in order to meet market demand for smaller and thinner packages, through joint development of CSPs and BGAs, we are also striving to supply "user-friendly" custom packages that satisfy the needs of a single customer. We make every effort to meet with customers and discuss in detail their desires concerning the form of the package, the dimensions, the number of leads, the exterior processing, etc., and then we strive to meet those needs quickly and flexibly.

#### 1.8.4 Modules

Recently, modules intended for higher densities and more advanced functions are becoming increasingly important for the following types of applications:

- Although there is a trend towards combining multiple ICs and peripheral components into a single LSI in order to raise mounting density and permit more sophisticated functions, when the characteristics of the devices make it difficult to do so, a module is used to create a circuit block.
- High-density modules, such as PC cards, are used to increase mounting densities.

In light of these needs, and given our background in a variety of device families and small package series, Fujitsu is devoting tremendous effort to the design and supply of modules that are suited for COB mounted (including multi-layered wiring boards) and surface mounted packages.

#### 1.8.5 Multi-chip Modules

Fujitsu's hybrid ICs, with analog and digital components mounted together and featuring the formation of high-precision resistors, have contributed to the reduced size of systems, and now we have developed a surface mounted package for these hybrid ICs. Fig. 1 shows the internal structure of the MBH10000 Multi-chip Modules, which not only retains the strengths of earlier hybrid ICs and offers increased integration, but also permits surface mounting.

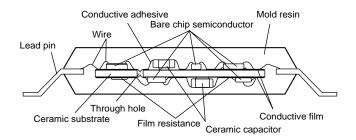


Fig. 1 Structural Diagram of Multi-chip Modules

#### 2.1 Overview

There are two basic methods for mounting packages. One is the flow soldering method, and the other is the reflow soldering method.

The flow soldering method, which is widely used for lead inserted type packages, uses a jettype solder bath to mount packages on printed circuit boards.

As electronic devices become smaller and lighter, IC packages are also expected to become smaller and thinner. As a result, in recent years there has been rapid growth in surface mounted packages, and surface mounting technology based on the reflow soldering method has garnered much attention.

One point that is important is that the flow soldering method used with lead insertion packages does not subject the package to much thermal stress, while in the reflow soldering method used with surface mounted packages, the package as a whole is heated, so that there is a great deal of thermal stress placed on the package, which must be noted during mounting.

This chapter will provide an overview of the mounting methods, the level of package moisture absorption, and the proper handling of packages, all in order to permit surface mounted packages to be mounted in a proper manner that preserves their reliability.

#### 2.2 Mounting Methods

#### 2.2.1 Lead inserted type

There are two methods for mounting lead inserted type packages on a printed circuit board: one method where the solder is applied directly to the printed circuit board, and another method where the package is mounted in a socket on the board.

When applying solder directly to the board, the leads are inserted into the mounting holes in the printed circuit board first, and the flow soldering method (wave soldering method) is used with jet solder. This is the most popular and widely used method for mounting packages on a printed circuit board.

However, during the soldering process, heat in excess of the normal maximum rating for the storage temperature is applied to the leads. As a result, quality assurance concerning heat resistance during soldering limits the soldering process to the levels shown below; do not exceed these levels during soldering work.

- 1) Solder temperature and immersion time 260 °C (500 °F), 10 seconds or less
- 2) Lead immersion positionUp to a distance of at least 1 to 1.5 mm from the main body of the package
- 3) When mounting an element using the solder flow method, ensure that the element itself is not immersed in the solder.
- 4) When using flux, avoid chlorine based fluxes; instead, use a resin-based flux.

Note, however, that if the module leads are exposed to the solder for a long period of time, solder on the module board may melt and previously mounted ICs may become detached. Also be careful to prevent any solder from coming into direct contact with the packages mounted on the module.

When using socket mounting, in some cases when the surface treatment of the socket pins is different from the surface metal of the IC leads, problems due to poor contact may arise. Therefore, a check of the surface treatment of the socket contacts and of the surface treatment of the IC leads is recommended.

#### 2.2.2 Surface mounted type

Compared to the lead inserted type, surface mounted packages have finer, thinner leads, which means that the leads are more easily bent. In addition, as packages come to have more and more pins, the lead pitch is becoming narrower, making handling more difficult. When the pitch of an IC is narrow, problems such as open pins caused by bent leads or short circuits caused by solder bridges occur easily; therefore, suitable mounting technology becomes a necessity.

Surface mounted packages include ceramic LCCs with no leads, flat packages with gull-wing leads or straight leads, packages with J-leaded, and ball-grid array packages(BGA); aside from the LCCs, the packages can be either plastic or ceramic. In the case of surface mounted packages, the solder reflow method is recommended as the mounting method for either type of package.

Fig. 1 illustrates the basic process for mounting.

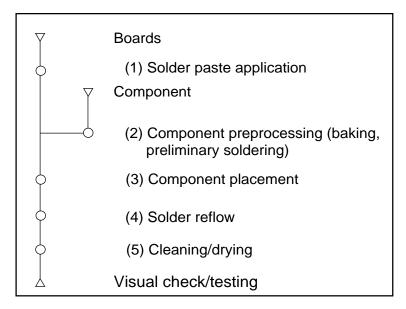


Fig. 1 Flow Chart of Basic Mounting Process

There are a variety of methods for soldering surface mounted packages onto a printed circuit board. Some of these methods are described below.

The mounting methods can be broadly classified into two types: partial heating methods and the total heating methods. The partial heating methods are desirable from a reliability standpoint since the thermal stress is small, but from the standpoint of mass production such methods are somewhat more difficult to implement.

## (1) Partial heating methods

Soldering method	Advantages	Disadvantages
Manual method  Soldering iron	Less stress placed on IC package     Bent leads can be repaired     Low equipment/ facility cost	Limited suitability for mass production     Danger of electrostatic damage
Block heater method  Pulse current  Heater	<ul> <li>Less stress placed on IC package</li> <li>Bent leads can be repaired</li> <li>No problem if the leads are raised a little</li> <li>Faster than the manual method</li> </ul>	Limited suitability for mass production     Danger of electrostatic damage
Laser method  Laser	Less stress     placed on IC     package	Limited suitability for mass production     Problems arise if leads are raised slightly
Hot air method  Hot air	Less stress placed on IC package     Low operating costs	Very low suitability for mass production

### (2) Total heating methods

Soldering method	Advantages	Disadvantages
Full dip method  Melted solder	Highly suited for mass production     Existing techniques and facilities can be used     Low operating costs	Places the most stress on package
Infrared reflow method  Infrared heater	<ul> <li>Highly suited for mass production</li> <li>Low operating costs</li> </ul>	Places comparatively large amount of stress on package
Vapor phase reflow method  Saturated steam Inert liquid (florinate) Heater	<ul> <li>Highly suited for mass production</li> <li>Places comparatively little stress on package</li> <li>Uniformity of temperature distribution is excellent</li> </ul>	Operating costs are high
Hot air heating method (used with far infrared heater  Far infrared heater  Forced convection	Places     comparatively     little stress on     package     Highly suited     for mass     production	Oxidation due to surrounding air may occur
Underside heating method  Conveyor belt  Heater	High temperatures are not applied directly to the package	Cannot be used with double-sided boards

#### 2.2.3 Precautions on mounting

Points of consideration concerning mounting work are explained below.

#### (1) Boards

Packages can be mounted on a variety of boards, including resin boards made of materials such as paper phenol or glass epoxy, ceramic boards, and flexible printed circuit boards, and when selecting the board material it is essential to give due consideration to factors such as matching the thermal expansion coefficients of the components to be mounted, electrical and mechanical characteristics, heat dissipation characteristics, the total reliability level, and cost. In addition, the reliability and production yield in terms of the wiring pattern on the component mounting surface also become important factors.

Figs. 2, 3, and 4 show examples of design criteria for surface patterns. In the design stage, consideration should be given to ease of mounting, reliability of the connections, pattern spacing, and the possibility of solder bridge formation.

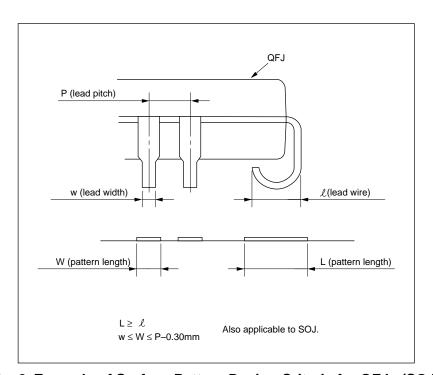


Fig. 2 Example of Surface Pattern Design Criteria for QFJs (SOJs)

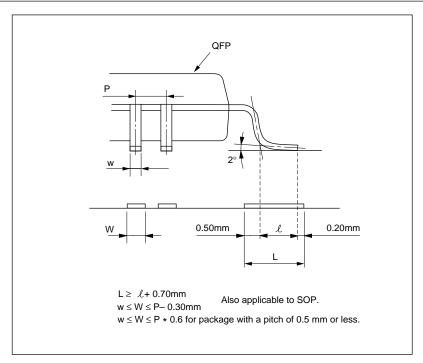


Fig. 3 Example of Surface Pattern Design Criteria for SOPs and QFPs

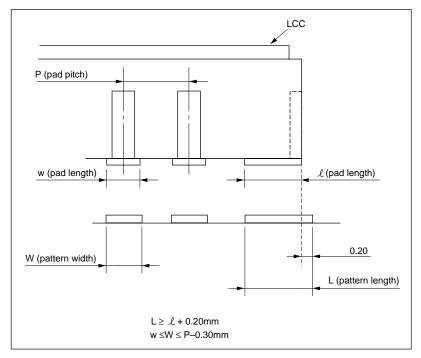


Fig. 4 Example of Surface Pattern Design Criteria for LCCs

#### (2) Applying solder paste

There are two methods for supplying the solder paste: by printing and by dispenser.

When done by printing, a stainless steel screen mask is used to apply the solder paste. When mounting packages with a narrow pitch, how the solder paste printing process is

performed has a major effect on the production yield after the reflow process, so careful attention must be paid to the selection of the equipment and to the printing conditions.

Careful attention must also be paid to the selection of the solder paste and the printing mask.

If the board surface is not flat and some of the solder is to be applied after a portion of the components have already been mounted, the remaining solder paste can be applied by using a dispenser.

#### (3) Solder paste

The solder paste is a mixture of solder powder (normally #250 to 325) mixed with flux.

The merits of using solder paste include:

- It is easy to control the amount of solder used.
- It is possible to use the viscosity of the paste to temporarily hold components in place.
- There are no impurities from a solder bath, etc.
- It is well suited for automation and mass production.

The most common type of solder is lead-tin eutectic type, but when soldering boards or components that use silver-lead for conductors, a eutectic type solder with a silver content of about 2% or 3% is used.

Until now, most solder pastes have used a resin-type flux, but recently measures to eliminate fluorocarbons and regulations on organic solvents have spurred the development of pastes that require no cleaning process or that use water-soluble flux.

Key points to consider in the selection of flux include:

- a) Selection based on catalog values
  - Size and shape of the solder powder
  - Solder composition
  - Amount of flux and chlorine included

- b) Evaluation criteria for actual trials
  - Good patterning characteristics (deposits well)
  - No change in the viscosity and uniformity of the mixture of solder powder and flux over time
  - Continuous printing possible
  - Very little dripping or formation of solder balls when melted
  - Easy to clean, with little flux residue, no-discoloration or staining

Although the cost of the solder paste is also important, the total manufacturing cost in terms of production yield, etc., must be taken into consideration when selecting a paste, not just the cost of the paste itself.

Before beginning mass production, a thorough study should be conducted and then those materials that best fit the conditions under which they will be used should be selected.

Solder paste is normally applied through a printing process, using a screen mask about 150  $\mu$ m thick.

#### (4) Component preprocessing

a) Preliminary soldering of ceramic packages

Either an alloy with a low melting point or glass with a low melting point is used to seal LCC packages. The melting point of the alloy is about 280°C (536°F), so work must be performed at temperatures below that level. While the glass sealant will maintain its air-tight integrity at higher temperatures, in the interest of maintaining device reliability the same precautions as for alloys should be observed.

When using the solder dip method, regardless of the type of sealing material, the package must not be immersed in the solder bath to the point where the sealant is also immersed. For the conditions for solder dipping, refer to Table 1.

Table 1 Recommended Conditions for Preliminary Soldering

Process	Conditions	
Preliminary heating	100 °C (212 °F) to 150°C (302 °F)	
Solder dip temperature	260°C max. (500°F max.), 10 seconds max.	
Cooling	Natural cooling until temperature drops below 100°C (212 °F)	

#### b) Baking plastic SMD packages

Unlike ceramic packages, plastic packages absorb moisture when exposed to atmosphere. Although this does not present a reliability problem during storage, if a plastic package that has absorbed moisture is soldered by the reflow method, the package may crack. Although it depends on the package type and the reflow method, it is important to note that some packages must undergo a baking process before the reflow process. (For details, refer to section 2.3, "Surface Mounted Plastic Package Reliability.")

#### (5) Component placement

Equipment that positions surface mounted package components is available from a variety of manufacturers in worldwide. When selecting such equipment, it is necessary to consider the number of components it will handle and the manner in which the components are packaged (in containers, trays, or on tape).

Because the leads on flat packages extend outwards, they are easily bent. Because repair is difficult once the leads are bent, great care must be taken when handling the packages.

#### (6) Full solder dip (wave soldering method)

When using the full solder dip method for mounting, observe the following conditions.

(Contact a Fujitsu sales representative for details on those packages and products for which full solder dipping is available.)

Solder bath temperature: 260°C max. (500°F max.)

Time: Less than 5 seconds

#### (7) Solder reflow

The typical reflow methods are: a) infrared reflow; b) vapor phase reflow; and c) hot air reflow. General descriptions of

each of these methods are provided below. Note that the use of full solder dipping should be avoided.

#### Infrared reflow a)

This reflow method uses radiant heat from an infrared heater.

#### Advantages

- · Processing capability is high.
- Temperature profile can be controlled comparatively well.
- Operating cost is low.
- Equipment is inexpensive.

- Disadvantages Temperature differences can arise due to differences in radiation absorption rates on the board.
  - Caution is required, since the flux is easily blackened.
  - Reflow in a normal oxidizing atmosphere.

#### b) Vapor phase reflow

This reflow method uses the latent heat of vaporization of an inert liquid.

- Advantages Uniform temperature distribution.
  - Reflow in an inert atmosphere.
  - No fear of overheating. (Heat is not applied above the boiling point of the inert liquid.)

Disadvantages • Temperature profile is limited.

- · Operating cost is high.
- Processing capability decreases somewhat.
- Attention must be paid to ventilation.
- Equipment is expensive.

#### c) Hot air reflow

This reflow method uses convective thermal propagation with heat-saturated air.

There are two different types of methods: the far infrared combination type and the hot air circulation type.

Advantages

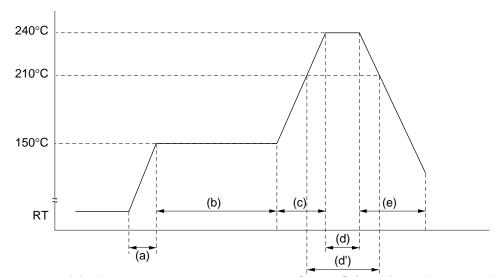
- Temperature profile can be controlled comparatively well.
- Temperature distribution can be made relatively uniform.
- Operating cost is low.

Disadvantages • Reflow in a normal oxidizing atmosphere.

- Processing capability decreases somewhat.
- Note on temperature profiles

No problems should arise if the temperature profile is equivalent to that of infrared reflow.

• Temperature profile for infrared reflow scheme



- (a) Temperature increase gradient 1°C to 4°C /sec (33.8°F to 39.2°F /sec)
- (b) Preliminary heating Temperature:150 ± 10°C (302 ± 50°F):60 sec or more
- (c) Temperature increase gradient 1°C to 4°C /sec (33.8°F to 39.2°F /sec)
- (c) Temperature increase gradient 1 0 to 4 075cc (55.5 1 to 55.2 1 75cc

(d) Actual heating Temperature: 230°C (446°F) max.

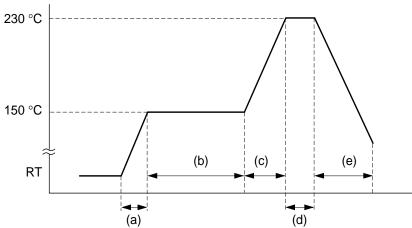
Time: 10 sec or less

(Temperature of the top of the package

body)

- (d') Temperature: 210°C (410°F) Time: 40 sec or less
- (e) Natural cooling or forced cooling

• Temperature profile for infrared reflow scheme



- (a) Temperature increase gradient 1°C to 4°C /sec (33.8°F to 39.2°F /sec)
- (b) Preliminary heating

Temperature:  $150 \pm 10^{\circ}$ C ( $302 \pm 50^{\circ}$ F): 60 sec or more

- (c) Temperature increase gradient 1°C to 4°C /sec (33.8°F to 39.2°F /sec)
- (d) Actual heating

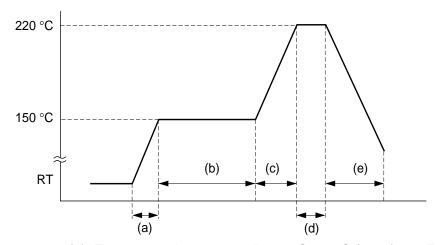
Temperature: 230°C (446°F) max.

Time: 10 sec or less

(Temperature of the top of the package

body)

- (e) Natural cooling or forced cooling
- Temperature profile for infrared reflow scheme



- (a) Temperature increase gradient 1°C to 4°C /sec (33.8°F to 39.2°F /sec)
- (b) Preliminary heating

Temperature:150  $\pm$  10°C (302  $\pm$  50°F):60 sec or more

- (c) Temperature increase gradient 1°C to 4°C /sec (33.8°F to 39.2°F /sec)
- (d) Actual heating

Temperature: 225°C (437°F) max.

220°C (428°F) up

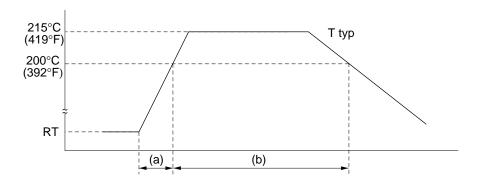
Time: 10 sec or less

(Temperature of the top of the package

body)

(e) Natural cooling or forced cooling

• Temperature profile for vapor phase reflow scheme (using florinate or an equivalent)



- (a) Temperature increase gradient 1°C to 8°C /sec. (33.8°F to 46.4°F /sec)
- (b) Temperature: 200°C to (Typ: 215°C) (392°F to (Typ: 419°F)) Time: 30 to 60 sec.

#### (8) Manual soldering (partial heating method)

This method uses a soldering iron; soldering is done with the IC fixed in place by flux or adhesive.

Conditions Temperature: 350°C max. (662°F max.)

Time: 3 seconds max./pin

#### (9) Cleaning

After soldering, clean away any flux residue.

If any flux left on the printed circuit board begins to absorb moisture, it can have a negative impact on reliability due to degradation of the insulation resistance or corrosion of the leads due to the chlorine component of the flux; therefore, cleaning is recommended. Refer to Table 2 for details on the cleaning requirements.

The following cautions should be observed during cleaning:

- a) Do not touch printed surfaces until the cleaning fluid dries.
- b) When solder paste was used for mounting, solder balls may have formed, depending on the paste type, paste quality, mounting conditions, etc.; therefore, pay attention to the need to clean away any solder ball residue as well.

**Table 2 Plastic Package Cleaning Requirements** 

Frequency	27 to 29kHz	
Ultrasonic wave output	15w/s or less	
Solvent	Water-based cleaning solvent, alcohol-based	
	cleaning solvent, etc.	
Cleaning time	Up to 30 seconds (one time)	
Cautions	The packages must not resonate.	
	The packages and printed circuit board must	
	not come into direct contact with the vibration	
	source.	
	Do not touch or brush printed surfaces while	
	cleaning is in progress or while there is	
	cleaning solvent on a package.	
	• When using solvents, observe public	
	environmental standards and safety	
	standards.	

Note: Cleaning ceramic packages

Do not use ultrasonic cleaning to clean ceramic packages after mounting. Instead, use hot water, boiling water, steam, etc., for cleaning. Also, caution should be exercised in regards to the volatility of the cleaning fluids, and performing the work in sealed equipment is recommended.

#### (8) Miscellaneous (reworking a package)

If, after mounting, a package must be reworked, use a hot jet or other method to apply localized heat in order to remove the package in question, and then mount a proper package in its place in the same manner. In this instance, the preliminary soldering method and the solder paste (applied with a dispenser) method can be used individually or together. In either case, keep the points described in item 4, "Component preprocessing," in mind. From the standpoint of device reliability, such replacements should be kept to a minimum.

#### 2.3 Surface Mounted Plastic Package Reliability

The heat stress that surface mounted plastic packages are subjected to when they are mounted adversely affects their humidity resistance characteristics. This section describes the humidity resistance characteristics of surface mounted plastic packages.

#### 2.3.1 Features of surface mounted packages

Compared with conventional lead inserted types, surface mounted packages offer the following advantages and disadvantages.

#### (1) Advantages

- Higher mounting densities are possible, making thinner and lighter devices possible.
- · Packages can have more pins.
- Surface mounted packages offer benefits from the standpoint of electrical characteristics.
- Because through holes are not needed, costs are lower.
- Surface mounted packages are suited for automated assembly lines.

#### (2) Disadvantages

- Surface mounted packages are vulnerable to thermal stress during mounting,
   which can result in cracked packages or poor humidity resistance characteristics.
- Because the external leads are thin, they are easily bent.
- Because the pitch is very small, solder bridges form easily.

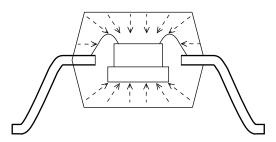
# 2.3.2 Mechanism behind degradation of humidity resistance characteristics due to thermal stress during mounting

For plastic packages, high thermal stress may cause deterioration of the IC Packages.

The moisture resistance of packages is deteriorated by thermal stress in the following phases:

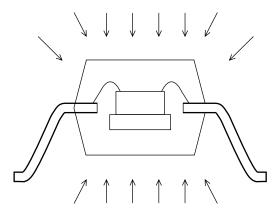
#### (1) Moisture absorption

Plastic packages absorb moisture in the air. The thinner the package, the sooner the moisture absorbed to the center.



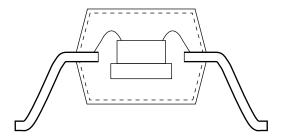
#### (2) Thermal stress during mounting

The mounting temperature and time depend on the mounting method. In particular, the overall heating method causes higher thermal stress on the package than the partial heating method.



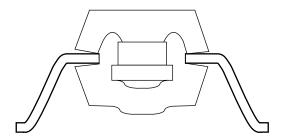
#### (3) Temperature increase in package

The increasing temperature causes evaporation of moisture absorbed in phase (1), and deterioration of resin strength and mismatch between the lead frame and resin of the package due to the different thermal expansion coefficients.



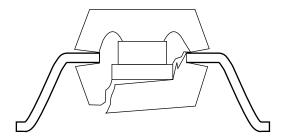
#### (4) Resin interface exfoliation

The stress generated in phase (3), causes exfoliation of the package resin interface. (The water pressure increases to 46 atm at 260°C (500°F).)



#### (5) Package cracking

If the above-mentioned stress is high, package cracking and bonding wire breaking may occur.



#### 2.3.3 Measures to improve humidity resistance characteristics

In response to the mechanisms described above that contribute to the degradation of a package's humidity resistance characteristics, Fujitsu is taking the following measures in order to improve reliability.

- (1) Improvement of mold resins
  - Fujitsu is striving to improve the sealing power of resins, reduce the stress that they are subjected to, and to increase their purity.
- (2) Improvement of the lead frame
  - It is essential to eliminate the boundary surface separations that form due to thermal expansion of the lead frame and the resin when thermal stress is applied during the mounting process.
- (3) Improvement of packaging materials for shipment
  - Since one of the mechanisms described was the absorption of moisture by plastic ICs which in turn lead to a degradation of humidity resistance characteristics, Fujitsu packages ICs in an aluminum-laminate pouch that is highly impermeable to moisture, and with silica gel placed inside the pouch.

#### 2.3.4 Mount ranking

Surface mounted plastic packages come in a variety of sizes and thicknesses, and also vary in their ability to withstand the thermal stress of the mounting process. Therefore, Fujitsu specifies (for each individual product) the number of days that can elapse between the time a shipment of ICs is unpacked until the time when they are mounted. This specification is called the "mount ranking." The number of days allowed differs according to the product name, the package, and the mounting conditions. Table 3 shows classified mount ranking. Even for the same packages, the mounting rank may vary depending on the device type. For detailed information about each mounting rank, contact Fujitsu.

Table 3 Classification of Mounting Ranks (Mounting by Overall Heating)

		Overall dipping	IR*1	IR x 2 times	VPS*2
	А	Free (within 2 years after production)			
	В	Within 7 days after PB*3	Within 25 days	Within 14 days *4	Within 25 days
			Within n days	n ≥ 10 Within 7 days	
	Cn	Impossible	Within n days	10 > n ≥ 7 Within 3 days	Within n days
			Within n days	7 > n >0 Impossible	
Mountig rank	Dn	Impossible	Impossible	Impossible	Within n days
				n ≥ 10 Within 7 days	
	Fn	Impossible	Within n days	10 > n ≥ 7 Within 3 days	Within n days
				7 > n >0 Impossible	
	Jn	Impossible	Within n days	Impossible	Within n days

n: Number of days (for control of moisture absorption)

\*1: Infrared reflow

Peak temperature A, B, Cn: 240°C (464°F) Max. within 10 min.

Fn: 230°C (446°F) Max. within 10 min.

Jn: 225°C (437°F) Max. (conforms with JEDEC)

- \*2: Vapor phase soldering
- \*3: Overall dip is possible depending on products or packages.

  Mounting by overall dipping requires baking at 125°C (257°F) for 24 H.
- \*4: Double reflow is not supported for some products or packages. (QFJ22, etc.)
- Mounting by partial heating
   Mounting by partial heating is possible at any rank (A to D).

**Table 4 List of mounting ranks** 

Mountig rank	Mounting method	Allowable period from unpacking to mountig*1	Action after elapse of allowable period and 2 years after production
А	Overall dipping Infrared reflow Vapor phase reflow	Within 2 years after production	Baking (at 125° C (257° F) for 24 H) after storage of 2 years from production
В	(Overall dipping )*2 Infrared reflow Vapor phase reflow	– 25 days (14 days)	Mount within 30 days after baking (at 125° C (257° F) for 24 H)
C25	Infrared reflow Vapor phase reflow	25 days (7 days)	Mount within 30 days after baking (at 125° C (257° F) for 24 H)
C10	Infrared reflow Vapor phase reflow	10 days (7 days)	Mount within 10 days after baking (at 125° C (257° F) for 24 H)
C7	Infrared reflow Vapor phase reflow	7 days (3 days)	Mount within 7 days after baking (at 125° C (257° F) for 24 H)
C4	Infrared reflow Vapor phase reflow	4 days (Impossible)	Mount within 4 days after baking (at 125° C (257° F) for 24 H)
C3	Infrared reflow Vapor phase reflow	3 days (Impossible)	Mount within 3 days after baking (at 125° C (257° F) for 24 H)
C2	Infrared reflow Vapor phase reflow	2 days (Impossible)	Mount within 2 days after baking (at 125° C (257° F) for 24 H)
C1	Infrared reflow Vapor phase reflow	1 days (Impossible)	Mount within 1 days after baking (at 125° C (257° F) for 24 H)
D3	Vapor phase reflow	3 days	Mount within 3 days after baking (at 125° C (257° F) for 24 H)
D2	Vapor phase reflow	2 days	Mount within 2 days after baking (at 125° C (257° F) for 24 H)
D1	Vapor phase reflow	1 days	Mount within 1 days after baking (at 125° C (257° F) for 24 H)
F3	Infrared reflow Vapor phase reflow	1 days	Mount within 1 days after baking (at 125° C (257° F) for 24 H)
J3	Infrared reflow Vapor phase reflow	1 days	Mount within 1 days after baking (at 125° C (257° F) for 24 H)

\*1: Parenthesized numbers indicate allowable periods for mounting by two-pass reflow. [Example] Mounting rank B:

Period from unpacking to mounting at the first reflow =  $\alpha$  days

Period from the first reflow to mounting to the second reflow =  $\beta$  days  $\alpha$  +  $\beta$  = 14 days

For mounting by two-pass reflow, pre-baking should be performed after 14 days from unpacking.

\*2: Mounting by overall dipping requires pre-baking (PB) at 125°C (257°F) for 24 H. For the products and packages not supported, contact your local dealer or representative (such as some product of QFP44 or 48 pins or QFJ or low-profile packages).

#### 2.3.5 Storage and drying processing

Surface mounted plastic packages should be stored while still packed in the materials that they were shipped in from Fujitsu. If you have any questions, contact Fujitsu.

#### 2.3.6 Reliability data

Because surface mounted plastic packages are mounted by total heating methods, they are easily affected by thermal stress during the mounting process, with the result that packages sometimes crack or their humidity resistance characteristics are adversely affected.

In addition to normal reliability evaluations, Fujitsu subjects SMDs to temperature cycle tests and PTHS tests after preprocessing the packages for solder heat resistance, all in order to evaluate reliability versus the stresses encountered during the mounting process.

Tables 5 to 9 show examples of the results of these evaluations.

Table 5 Reliability Testing Results (SOJ-42P MB81V16160A)

Test item	Test conditions	Test results
High temperature storage test	150 <sup>-</sup> C (302 <sup>-</sup> F) 1000H	0/55
High temperature continuous operation test* (AC operation)	125 <sup>-</sup> C (257 <sup>-</sup> F) 1000H	0/105
High temperature continuous operation test* (DC operation)	150 <sup>-</sup> C (302 <sup>-</sup> F) 1000H	0/55
High humidity continuous operation test* (AC operation)	85 <sup>-</sup> C (185 <sup>-</sup> F)/85% 1000H	0/55
Low temperature continuous operation test (AC operation)	– 55⁻C (– 67⁻F) 1000H	0/55
Temperature cycle*	- 65 <sup>-</sup> C to 150 <sup>-</sup> C (- 85 <sup>-</sup> F to 302 <sup>-</sup> F) 100C	0/55
Thermal shock	0-C to 100-C (32-F to 212-F) 100C	0/55
PTHS*	121 <sup>-</sup> C (250 <sup>-</sup> F)/100% 168H	0/55
PTHB	121 <sup>-</sup> C (250 <sup>-</sup> F)/100% 96H	0/25

<sup>\*</sup> Preprocessing requirements: Drying 125°C (257°F), 24 H + humidity absorption precess 85°C (185°F)/ 85%, 48 H + Infrared reflow 245°C (473°F) Max.

Table 6 Reliability Testing Results (SON-40P MBM29LV004)

Test item	Test conditions	Test results
High temperature storage test	150 <sup>-</sup> C (302 <sup>-</sup> F) 1000H	0/25
High temperature continuous operation test* (AC operation)	125 <sup>-</sup> C (257 <sup>-</sup> F) 1000H	0/55
High humidity continuous operation test* (AC operation)	85 <sup>-</sup> C (185 <sup>-</sup> F)/85% 1000H	0/25
Temperature cycle*	<ul><li>65⁻C to 150⁻C</li><li>(− 85⁻F to 302⁻F)</li><li>100C</li></ul>	0/105
Thermal shock	0 <sup>-</sup> C to 100 <sup>-</sup> C (32 <sup>-</sup> F to 212 <sup>-</sup> F) 100C	0/55
PTHS*	121 <sup>-</sup> C (250 <sup>-</sup> F)/100% 168H	0/55
PTHB	121 <sup>-</sup> C (250 <sup>-</sup> F)/100% 96H	0/25

<sup>\*</sup> Preprocessing requirements: Drying 125°C (257°F), 24 H + humidity absorption precess 85°C (185°F)/85%, 6 H +Infrared reflow 245°C (473°F) Max.

Table 7 Reliability Testing Results (SSOP-20P MB15A60)

Test item	Test conditions	Test results
High temperature storage test	150 <sup>-</sup> C (302 <sup>-</sup> F) 1000H	0/25
High temperature continuous operation test* (AC operation)	150 <sup>-</sup> C (302 <sup>-</sup> F) 1000H	0/55
High humidity continuous operation test* (AC operation)	85 <sup>-</sup> C (185 <sup>-</sup> F)/85% 1000H	0/25
Low temperature continuous operation test (AC operation)	– 55⁻C (– 67⁻F) 1000H	0/25
Temperature cycle*	- 65 <sup>-</sup> C to 150 <sup>-</sup> C (- 85 <sup>-</sup> F to 302 <sup>-</sup> F) 100C	0/55
Thermal shock	0 <sup>-</sup> C to 100 <sup>-</sup> C (32 <sup>-</sup> F to 212 <sup>-</sup> F) 100C	0/25
PTHS*	121 <sup>-</sup> C (250 <sup>-</sup> F)/100% 168H	0/55
PTHB	121 <sup>-</sup> C (250 <sup>-</sup> F)/100% 96H	0/25

<sup>\*</sup> Preprocessing requirements: Drying 125°C (257°F), 24 H + humidity absorption precess 85°C (185°F)/85%, 24 H + Infrared reflow 245°C (473°F) Max.

Table 8 Reliability Testing Results (QFP-120P MB90000A Serise)

Test item	Test conditions	Test results
High temperature storage test	150 <sup>-</sup> C (302 <sup>-</sup> F) 1000H	0/55
High temperature continuous operation test* (AC operation)	150 <sup>-</sup> C (302 <sup>-</sup> F) 1000H	0/105
High humidity continuous operation test* (AC operation)	85 <sup>-</sup> C (185 <sup>-</sup> F)/85% 1000H	0/55
Low temperature continuous operation test (AC operation)	– 55⁻C (– 67⁻F) 1000H	0/25
Temperature cycle*	- 65 <sup>-</sup> C to 150 <sup>-</sup> C (- 85 <sup>-</sup> F to 302 <sup>-</sup> F) 100C	0/105
Thermal shock	0-C to 100-C (32-F to 212-F) 100C	0/55
PTHS*	121 <sup>-</sup> C (250 <sup>-</sup> F)/100% 168H	0/55
PTHB	121 <sup>-</sup> C (250 <sup>-</sup> F)/100% 96H	0/25

<sup>\*</sup> Preprocessing requirements: Drying 125°C (257°F), 24 H + humidity absorption precess 30°C (86°F)/85%, 96 H + Infrared reflow 245°C (473°F) Max.

Table 9 Reliability Testing Results (QFP-256P MBCE46194)

Test item	Test conditions	Test results
High temperature storage test	150 <sup>-</sup> C (302 <sup>-</sup> F) 1000H	0/25
High temperature continuous operation test* (AC operation)	100 <sup>-</sup> C (212 <sup>-</sup> F) 1000H	0/55
High humidity continuous operation test* (AC operation)	85 <sup>-</sup> C (185 <sup>-</sup> F)/85% 1000H	0/25
Low temperature continuous operation test (AC operation)	– 55⁻C (– 67⁻F) 1000H	0/25
Temperature cycle*	- 65 <sup>-</sup> C to 150 <sup>-</sup> C (- 85 <sup>-</sup> F to 302 <sup>-</sup> F) 100C	0/55
Thermal shock	0-C to 100-C (32-F to 212-F) 100C	0/25
PTHS*	121 <sup>-</sup> C (250 <sup>-</sup> F)/100% 168H	0/55
PTHB	121 <sup>-</sup> C (250 <sup>-</sup> F)/100% 96H	0/25

<sup>\*</sup> Preprocessing requirements: Drying 125°C (257°F), 24 H + humidity absorption precess 30°C (86°F)/85%, 96 H + Infrared reflow 245°C (473°F) Max.

Table 10 Reliability Testing Results (BCC-16P MB15E07SL)

Test item	Test conditions	Test results
High temperature storage test	150° C (302° F) 1000H	0/25
High temperature continuous operation test* (AC operation)	150° C (302° F) /1005D 1000H	0/55
High humidity continuous operation test* (AC operation)	85° C (185° F)/85% 1000H	0/25
Low temperature continuous operation test (AC operation)	– 55° C (– 67° F) 1000H	0/25
Temperature cycle*	1010C 200c	0/55
Thermal shock	1011A 200c	0/25
PTHS*	121° C (250° F)/2.03 ×10 <sup>5</sup> Pa 168H	0/55
PTHB	121° C (250° F)/2.03 ×10 <sup>5</sup> Pa 96H	0/25

<sup>\*</sup> Preprocessing requirements: Drying 125°C (257°F), 24 H + humidity absorption precess 85°C (185°F)/85%, 48 H + Infrared reflow 245°C (473°F) Max.

Table 11 Reliability Testing Results (FBGA-48P MBM29LV800A)

Test item	Test conditions	Test results
High temperature storage test	150° C (302° F) 1000H	0/25
High humidity continuous operation test* (DC operation)	85° C (185° F)/85% 1000H	0/55
Temperature cycle*	1010C 200c	0/105
Thermal shock	1011A 200c	0/55
PTHS*	121° C (250° F)/85% 168H	0/55
PTHB	121° C (250° F)/85% 96H	0/25

<sup>\*</sup> Preprocessing requirements: Drying 125°C (257°F), 24 H + humidity absorption precess 85°C (185°F)/85%, 24 H + Infrared reflow 245°C (473°F) Max.

Table 12 Reliability Testing Results (FBGA-168P MBCU62000)

Test item	Test conditions	Test results
High temperature storage test	150° C (302° F) 1000H	0/25
High humidity continuous operation test* (AC operation)	85° C (185° F)/85% 1000H	0/25
Temperature cycle*	1010C 200c	0/55
Thermal shock	1011A 200c	0/25
PTHS*	121° C (250° F)/85% 168H	0/55
РТНВ	121° C (250° F)/85% 96H	0/25

<sup>\*</sup> Preprocessing requirements: Drying 125°C (257°F), 24 H + humidity absorption precess 85°C (185°F)/85%, 24 H + Infrared reflow 235°C (455°F) Max.

Table 13 Reliability Testing Results (BGA-416P CG51 Series)

Test item	Test conditions	Test results
High temperature	150° C (302° F)	0/25
storage test	1000H	0/20
High temperature	125° C (257° F)	
continuous operation	/1005D	0/55
test* (AC operation)	1000H	
High humidity		
continuous operation	85° C (185° F)/85%	0/25
test*	1000H	0/25
(AC operation)		
Tomporoturo ovolo*	1010B	0/55
Temperature cycle*	500c	0/33
Thermal shock	1011A	0/25
I Heimai Shock	200c	0/25
PTHS*	121° C (250° F)/85%	0/55
FIIIS	168H	0/33
	121° C (250° F)/85%	
PTHB	96H	0/25
	Vcc=3.8V	

<sup>\*</sup> Preprocessing requirements: Drying 125°C (257°F), 24 H + humidity absorption precess 30°C (86°F)/80%, 96 H + Infrared reflow 225°C (437°F) Max. (3times)

Table 14 Reliability Testing Results (BGA-416P CS60ALE TEST CHIP)

Test item	Test conditions	Test results
High temperature storage test	150° C (302° F) 1000H	0/25
High temperature continuous operation test* (AC operation)	125° C (257° F) /1005D 1000H	0/55
High humidity continuous operation test* (AC operation)	85° C (185° F)/85% 1000H	0/25
Low temperature continuous operation test (AC operation)	– 55° C (– 67° F) 1000H	0/25
Temperature cycle*	1010B 500c	0/55
Thermal shock	1011A 200c	0/25
PTHS*	121° C (250° F)/85% 168H	0/55
PTHB	121° C (250° F)/85% 96H Vcc=3.8V	0/25

<sup>\*</sup> Preprocessing requirements: Drying 125°C (257°F), 24 H + humidity absorption precess 30°C (86°F)/80%, 96 H + Infrared reflow 225°C (437°F) Max.

Table 15 Reliability Testing Results (BGA-416P CS60ALE Custum CHIP)

Test item	Test conditions	Test results
High temperature storage test	150° C (302° F) 1000H	0/25
High temperature continuous operation test* (AC operation)	125° C (257° F) /1005D 1000H	0/55
High humidity continuous operation test* (AC operation)	85° C (185° F)/85% 1000H	0/25
Low temperature continuous operation test (AC operation)	– 55° C (– 67° F) 1000H	0/25
Temperature cycle*	1010B 500c	0/55
Thermal shock	1011A 200c	0/25
PTHS*	121° C (250° F)/85% 168H	0/55
PTHB	121° C (250° F)/85% 96H VDD1,2=3.8V	0/25

<sup>\*</sup> Preprocessing requirements: Drying 125°C (257°F), 24 H + humidity absorption precess 30°C (86°F)/80%, 96 H + Infrared reflow 225°C (437°F) Max.

Table 16 Reliability Testing Results (TSOP-86P MB811643242A)

Test item	Test conditions	Test results
High temperature	125° C (257° F)	
continuous operation	/1005D	0/55
test* (AC operation)	1000H	
High humidity continuous operation test*	85° C (185° F)/85% 1000H	0/25
(AC operation)		
Temperature cycle*	1010C 200c	0/105
Thermal shock	1011A 200c	0/55
PTHS*	121° C (250° F)/2 atm 168H	0/55
PTHB	121° C (250° F)/2 atm 96H	0/25

<sup>\*</sup> Preprocessing requirements: Drying 125°C (257°F), 24 H + humidity absorption precess 85°C (185°F)/85%, 24 H + Infrared reflow 235°C (455°F) Max.

#### 2.4 Storage

Products should be stored while still packed in the materials that they were shipped in from Fujitsu.

The recommended condition for the storage area is as belows;

Room Temperature; 5 to 30°C (41 to 86°F)

Room Humidity; 40 to 70%

- Do not store the products where they will be exposed to corrosive gases or in dusty locations.
- Because sudden temperature changes can cause moisture to condense on the products, store the products in an area where the temperature remains fairly constant.
- Note that if products are stored for an extended period of time, the solderability of the lead pins may decline, rust may form, or the electrical characteristics may deteriorate.

## **Package Forms and Number of Pins**

### 3.1 Package Forms and Number of Pins

The following tables show the correspondence between the forms and number of pins for each package.

Lead inserted types

																	Nun	nbei	r of	pins	;														
Form	Package	8	9	14	16	17	18	19	20	22	24	28	32	36	40	42					80	88	107	121	135	149	179	208	211	256	299	321	361	401	441
Standard	DIP	•		•	•		•		•		_	•	•	•	•	_	•																		
	SH- DIP										•	•			•	•	•	•	•																
Small	SK- DIP									•	•	•	•																						
	SL- DIP										• 0	•	0																						
Matrix	PGA																		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

● : Plastic

O : Ceramic

# **Package Forms and Number of Pins**

#### Surface mounted types

Form	Pack-																			Nu	mb	er o	of p	oins	;																	
1 01111	age	8	14	16	18										48	50	54	56	64	68	80	86	100	104	120	124	144	160	164	168	176	196	208	216	232	240	256	260	296	304	320	368
	SOP		•					•		•	•	•	•	•																												
	SOL	•																																								
	SSOP	•		•		•	•		•		•							•																								
	TSOP						•			•			•	•	•	•	•	•				•																				
Flat	CSOP														•																											
	QFP							0		0				•	•				•	0	•		•	•		0		•		•			•				•	)		•		0
	LQFP									•					•				•		•		•		•		•				•		•	•			•					
	TQFP																		•		•		•		•		•				•											
	FPT (round)			0			0																																			

Form	Pack-																				N	lur	nbe	er c	of p	ins	3																		
FOIIII	age	16	26	28	32	36	40	42	2 4	44	46	48	57	61	64	68	69	7	7 8	14 1	00	112	120	144	168	176	192	2 22	4 24	40 2	256	272	288	320	352	410	6 42	0 56	50 5	76	672	720	1196	134	1600
	SON						•				•																																		
	всс	•			•							•																																	
Lead- less chip carrier	QFN	0			0	0			(	Э		0			0																														
	PCLP											•								(	•			•																					
	LCC				0																																								
Leaded chip	QFJ			•	•					• )						•			•	•																									
carrier	SOJ		0	0				•	•																																				
	BGA											•																		•	•				•	•	•	•	•		•	•			
Matrix	FBGA											•	•	•			•	•				•	•	•	•	•	•	•	•	•		•	•	•									•	•	•
	T-BGA																													•	•	•													

● : Plastic O : Ceramic

# **Package Forms and Number of Pins**

Modules, Cards Package Lineup

Mount type	Form	Package					Numbe	r of pins				
Mount type	Folili	rackage	72	48	60	64	68	80	100	144	168	200
		SIMM	•									
Modules	SMD	MZIP	•									
iviodules		DIMM								•	•	•
	Piggy back	Flat		0		0		0	0			
Cardss	_	Socket			•		•					

• : Plastic O : Ceramic

### 3.2 Package Lineup

### 3.2.1 Lead inserted types

#### Plastic and Ceramic DIPs

Number of pins	Form	Construction	Package code	Lead pitch mm/mil	Row spacing mm/mil	Weight g/package	Features
8		M	DIP-8P-M01	2.54/100	7.62/300	0.45	
		М	DIP-14P-M02	2.54/100	7.62/300	0.94	( = M01) Index is different
14	Stan- dard	С	DIP-14C-C01	2.54/100	7.62/300	_	Pin length: 134mil, package width: 248mil
	ga. c	O	DIP-14C-C04	2.54/100	7.62/300	_	Pin length: 134mil, package width: 268mil
			DIP-16P-M03	2.54/100	7.62/300	1.07	Package width: 260mil
		M	DIP-16P-M04	2.54/100	7.62/300	1.06	Package width: 244mil
16	Stan-		DIP-16P-M05	2.54/100	7.62/300	1.06	Package width: 283mil
10	dard		DIP-16C-C01	2.54/100	7.62/300		Package width: 248mil
		С	DIP-16C-C02	2.54/100	7.62/300		Package width: 268mil
			DIP-16C-C04	2.54/100	7.62/300		Package width: 288mil
			DIP-18P-M02	2.54/100	7.62/300	1.16	Package width: 244mil
18	Stan-	M	DIP-18P-M03	2.54/100	7.62/300	1.15	Package width: 260mil
10	dard		DIP-18P-M04	2.54/100	7.62/300	1.31	Package width: 283mil
		С	DIP-18C-C01	2.54/100	7.62/300		
			DIP-20P-M01	2.54/100	7.62/300	1.22	Package width: 260mil
		M	DIP-20P-M02	2.54/100	7.62/300		Package width: 244mil
	Stan-		DIP-20P-M03	2.54/100	7.62/300	1.53	Package width: 283mil
20	dard	Α	DIP-20C-A01	2.54/100	7.62/300	_	Package width: 288mil, mounted height: 177mil
		С	DIP-20C-C01	2.54/100	7.62/300		Package width: 275mil
		O	DIP-20C-C02	2.54/100	10.16/400		Package width: 380mil
	Stan-	М	DIP-22P-M03	2.54/100	10.16/400	1.94	
22	dard	С	DIP-22C-C02	2.54/100	10.16/400		
	SK	М	DIP-22P-M04	2.54/100	7.62/300	1.41	
	JIX	С	DIP-22C-C03	2.54/100	7.62/300		
		М	DIP-24P-M02	2.54/100	15.24/600	3.40	
24	Stan- dard	А	DIP-24C-A04	2.54/100	15.24/600	_	Package width: 588mil, mounted height: 177mil, maximum pin width: 50mil
		Α	DIP-24C-A06	2.54/100	15.24/600	_	Package width: 590mil, mounted height: 200mil, maximum pin width: 40mil

(continued)

#### (continued)

Number of pins	Form	Construction	Package code	Lead pitch mm/mil	Row spacing mm/mil	Weight g/package	Features
	Stan-	А	DIP-24C-A07	2.54/100	15.24/600	_	Package width: 595mil, mounted height: 177mil, maximum pin width: 40mil
	dard	С	DIP-24C-C01	2.54/100	15.24/600	_	Package width: 520mil
		C	DIP-24C-C03	2.54/100	15.24/600	_	Package width: 577mil
24	SH	М	DIP-24P-M05	1.778/70	7.62/300	1.15	
	SK	М	DIP-24P-M03	2.54/100	7.62/300	1.55	
	SK	С	DIP-24C-C04	2.54/100	7.62/300		
		М	DIP-24P-M04	2.54/100	10.16/400	2.35	
	SL	А	DIP-24C-A05	2.54/100	10.16/400	_	
		С	DIP-24C-C05	2.54/100	10.16/400	_	
		М	DIP-28P-M05	2.54/100	15.24/600	_	( = M02) Index is different
	Stan-	۸	DIP-28C-A01	2.54/100	15.24/600	_	Mounted height: 177mil
	dard	Α	DIP-28C-A07	2.54/100	15.24/600	_	Mounted height: 200mil
20		С	DIP-28C-C02	2.54/100	15.24/600	_	
28	SH	М	DIP-28P-M03	1.778/70	10.16/400	1.87	
	SK	М	DIP-28P-M04	2.54/100	7.62/300	2.10	
	CI	М	DIP-28P-M06	2.54/100	10.16/400	_	
	SL	Α	DIP-28C-A10	2.54/100	10.16/400	_	
	Stan- dard	М	DIP-32P-M01	2.54/100	15.24/600	4.60	
32	SK	М	DIP-32P-M02	2.54/100	7.62/300	2.45	Package length: 1600mil
	SL	Α	DIP-32C-A02	2.54/100	10.16/400	_	
36	Stan- dard	М	DIP-36P-M01	2.54/100	15.24/600	5.36	Package length: 2063mil
		М	DIP-40P-M02	2.54/100	15.24/600		Package length: 1510mil
40	Stan- dard	Δ.	DIP-40C-A04	2.54/100	15.24/600		With window(DIA: 355mil)
40	uaru	Α	DIP-40C-A05	2.54/100	15.24/600	_	Purple body
	SH	М	DIP-40P-M03	1.778/70	15.24/600	4.44	
		М	DIP-42P-M03	2.54/100	15.24/600	_	( = M01) Index is different
	Stan-		DIP-42C-A01	2.54/100	15.24/600	_	
42	dard	Α	DIP-42C-A03	2.54/100	15.24/600	_	( = A01) With window (DIA : 335mil)
	SH	М	DIP-42P-M02	1.778/70	15.24/600	4.47	
	Stan-	М	DIP-48P-M02	2.54/100	15.24/600	7.52	
48	dard	Α	DIP-48C-A01	2.54/100	15.24/600	_	
	SH	М	DIP-48P-M01	1.778/70	15.24/600	5.00	

(continued)

M : Plastic mold F : Frit seal (ceramic)

A : Metal seal (ceramic)
C : Cerdip(ceramic)

#### (continued)

Number of pins	Form	Construction	Package code	Lead pitch mm/mil	Row spacing mm/mil	Weight g/package	Features
52	SH	М	DIP-52P-M01	1.778/70	15.24/600	5.55	
64		М	DIP-64P-M01	1.778/70	19.05/750	9.03	
	SH	A DIP 64C A06 1 778/70 10 05/750 (= A02) With					
				( = A02) With window (DIA : 350mil)			

 $\begin{tabular}{lll} M: Plastic mold & A: Metal seal (ceramic) \\ F: Frit seal (ceramic) & C: Cerdip(ceramic) \\ \end{tabular}$ 

#### Ceramic PGAs

Number of pins	Construction	Package code	Lead pitch mm/mil	Pin matrix	Features			
64	Α	PGA-64C-A02	2.54/100	10				
68	Α	PGA-68C-A01	2.54/100	11				
80	А	PGA-80C-A01	2.54/100	12	3 fins			
		PGA-88C-A01	2.54/100	12				
88	А	PGA-88C-A02	2.54/100	12	( = A01) Cap is different			
00		PGA-88C-A05	2.54/100	12	2 fins			
		PGA-88C-A06	2.54/100	12	3 fins			
107	Α	PGA-107C-A01	2.54/100	12				
107		PGA-107C-A02	2.54/100	12	( = A01) Cap is different			
121	Α	PGA-121C-A01	2.54/100	13	2 fins			
121		PGA-121C-A02	2.54/100	13	3 fins			
	А	PGA-135C-A01	2.54/100	14	Pin length: 134mil			
		PGA-135C-A02	2.54/100	14	Pin length: 130mil			
135		PGA-135C-A05	2.54/100	14	2 fins			
		PGA-135C-A06	2.54/100	14	( = A01) Cap is different			
	С	PGA-135C-C02	2.54/100	14				
	А	PGA-149C-A02	2.54/100	15	6 fins			
149		PGA-149C-A03	2.54/100	15	3 fins			
149		PGA-149C-A06	2.54/100	15	2 fins			
		PGA-149C-A09	2.54/100	15				
	А	PGA-179C-A02	2.54/100	15	Pin length: 189mil			
179		PGA-179C-A03	2.54/100	15	Pin length: 134mil			
179		PGA-179C-A04	2.54/100	18	Pin fins			
		PGA-179C-A06	2.54/100	16	2 fins			

(continued)

 $\begin{tabular}{ll} M: Plastic mold & A: Metal seal (ceramic) \\ F: Frit seal (ceramic) & C: Cerdip(ceramic) \end{tabular}$ 

### (continued)

Number of pins	Construction	Package code	Lead pitch mm/mil	Pin matrix	Features
		PGA-208C-A02	2.54/100	17	
	A	PGA-208C-A03	2.54/100	17	Pin fins
208		PGA-208C-A04	2.54/100	17	6 fins
200		PGA-208C-A05	2.54/100	17	Screw stud
		PGA-208C-A06	2.54/100	17	3 fins
		PGA-208C-A07	2.54/100	17	2 fins
211	Α	PGA-211C-A01	2.54/100	17	
	A	PGA-256C-A01	1.27/50	19	
		PGA-256C-A02	2.54/100	19	Pin length: 134mil
256		PGA-256C-A03	2.54/100	19	Pin length: 130mil
230		PGA-256C-A04	2.54/100	19	3 fins, pin length: 134mil
		PGA-256C-A05	2.54/100	19	2 fins
		PGA-256C-A07	2.54/100	19	3 fins, pin length: 187mil
299	Α	PGA-299C-A01	2.54/100	20	
299	_ ^	PGA-299C-A02	2.54/100	20	3 fins
321	Α	PGA-321C-A02	2.54/100	33	Interstitial
361	Α	PGA-361C-A01	1.27/50	35	Interstitial
401	А	PGA-401C-A02	1.27/50	37	Interstitial
401	^	PGA-401C-A04	1.27/50	30	
		PGA-441C-A01	1.27/50	27	6 fins
441		PGA-441C-A03	1.27/50	27	17 fins
		PGA-441C-A06	1.27/50	27	

M : Plastic mold

A: Metal seal (ceramic)

C : Cerdip(ceramic)

### 3.2.2 Surface mounted types

#### Plastic and Ceramic SOPs

1 lastic and Gerannic 501 5									
Number of pins	Form	Construction	Package code	Lead pitch mm/mil	Package width x package length mm/mil	Lead shape	Weight g/package	Features	
8	SOP	М	FPT-8P-M01	1.27/50	7.62/300 (II)	Gullwing	0.10		
	301	С	FPT-8C-A01	1.27/50	_	Gullwing	_		
	SSOP	М	FPT-8P-M03	0.80mm	_	Gullwing	0.04		
	SOL	М	FPT-8P-M02	1.27/50	_	Gullwing	0.06		
		N 4	FPT-14P-M03	1.27/50	5.72/225 (I)	Gullwing	_		
14	000	М	FPT-14P-M04	1.27/50	7.62/300 (II)	Gullwing	0.20		
14	SOP	-	FPT-14C-A01	1.27/50	_	Straight	_		
		С	FPT-14C-A02	1.27/50	_	Straight			
			FPT-16P-M03	1.27/50	9.53/375 (III)	Gullwing	0.50		
		М	FPT-16P-M04	1.27/50	5.72/225 (I)	Gullwing			
16	SOP		FPT-16P-M06	1.27/50	7.62/300 (II)	Gullwing	0.20		
10		С	FPT-16C-C01	1.27/50	_	Straight	_		
			FPT-16C-C02	1.27/50	_	Gullwing			
	SSOP	М	FPT-16P-M05	0.65mm	_	Gullwing	0.07		
18	SOP	С	FPT-18C-C01	50/1.27	_	Straight	_		
	SOP	М	FPT-20P-M01	50/1.27	7.62/300 (II)	Gullwing	_		
			FPT-20P-M02	50/1.27	9.53/375 (III)	Gullwing	0.50	Mounted height: 3.10mm max.	
20			FPT-20P-M05	50/1.27	9.53/375 (III)	Gullwing	0.50	Mounted height: 2.80mm max.	
	SSOP	OP M	FPT-20P-M03	0.65mm	_	Gullwing	0.09	Mounted height: 1.45mm max.	
		141	FPT-20P-M04	0.65mm	_	Gullwing	0.08	Mounted height: 1.20mm max.	
	SOP	М	FPT-24P-M01	1.27/50	7.62/300 (II)	Gullwing	0.39		
24			FPT-24P-M02	1.27/50	9.53/375 (III)	Gullwing	0.58		
		А	FPT-24C-A01	1.27/50	_	Straight	_		
			FPT-24C-A02	1.27/50	_	Straight	_		
		F	FPT-24C-F01	1.27/50	_	Straight	_		
		С	FPT-24C-C04	0.76/30	_	Straight	_		
	SSOP	М	FPT-24P-M03	0.65mm	_	Gullwing	0.12		
		ļ						(continued)	

(continued)

#### (continued)

Number	Form	Construction	Package code	Lead pitch	Package width x package length	Lead shape	Weight	Features
of pins			Ü	mm/mil	mm/mil	,	g/package	
28		М	FPT-28P-M01	1.27/50	9.53/375 (III)	Gullwing	0.67	
		IVI	FPT-28P-M17	1.27/50	_	Gullwing	0.82	
	SOP	F	FPT-28C-F01	1.27/50	_	Straight	—	
			FPT-28C-C02	1.27/50	_	Gullwing	_	
		С	FPT-28C-C03	1.27/50	_	Straight	_	
			FPT-28C-C07	30mil	_	Straight		
30	SSOP	М	FPT-30P-M02	0.65mm	_	Gullwing	0.16	
	SOP	M	FPT-32P-M02	1.27/50	11.43/450(IV)	Gullwing	0.80	
32	301		FPT-32P-M03	1.27/50	13.34/525 (V)	Gullwing	1.08	Width: 14.40mm
32	TSOP	М	FPT-32P-M24	0.50mm	_	Gullwing	0.38	Normal bend type
	1301	IVI	FPT-32P-M25	0.50mm	_	Gullwing	0.38	Reverse bend type
	SOP	Α	FPT-34C-A01	1.00m	_	Straight	—	
34	SSOP	M	FPT-34P-M01	1.00mm	_	Gullwing	0.70	Normal bend type
J <del>4</del>			FPT-34P-M02	1.00mm	_	Gullwing	_	Reverse bend type
			FPT-34P-M03	0.65mm	_	Gullwing	0.19	
38	SOP	М	FPT-38P-M02	1.00mm	_	Gullwing	1.63	
	SSOP	М	FPT-40P-M01	0.80mm	_	Gullwing	0.76	
			FPT-40P-M04	0.80mm	_	Gullwing	0.71	Normal bend type
40			FPT-40P-M05	0.80mm	_	Gullwing		Reverse bend type
	TSOP	М	FPT-40P-M06	0.50mm	_	Gullwing		Normal bend type
			FPT-40P-M07	0.50mm	_	Gullwing		Reverse bend type
	SOP	М	FPT-44P-M16	0.80mm	_	Gullwing	_	
44	TSOP	М	FPT-44P-M07	0.80mm	_	Gullwing	0.49	Normal bend type
7-7			FPT-44P-M08	0.80mm	_	Gullwing		Reverse bend type
			FPT-44P-M18	0.80mm	_	Gullwing		
48	TSOP	М	FPT-48P-M19	0.50mm	_	Gullwing	0.50	Normal bend type
			FPT-48P-M20	0.50mm	_	Gullwing	0.50	Reverse bend type
	CSOP	М	LCC-48P-M03	0.40mm	$10.0 \times 9.50$			
50	TSOP	M	FPT-50P-M05	0.80mm	_	Gullwing		
			FPT-50P-M06	0.80mm	_	Gullwing	0.60	
	TSOP	М	FPT-54P-M01	0.80mm	_	Gullwing	0.77	
		. • • •	FPT-54P-M02	0.80mm	10.16/400	Gullwing	0.63	
56	SSOP	М	FPT-56P-M03	0.80mm		Gullwing		

(continued)

M : Plastic mold A : Metal seal (ceramic)
F : Frit seal (ceramic) C : Cerdip(ceramic)

#### (continued)

Number of pins	Form	Construction	Package code	Lead pitch mm/mil	Package width x package length mm/mil	Lead shape	Weight g/package	Features
			FPT-56P-M01	0.50mm	_	Gullwing	_	
56	TSOP	М	FPT-56P-M02	0.50mm	_	Gullwing	_	
			FPT-56P-M04	0.40mm	_	Gullwing	_	
86	TSOP	М	FPT-86P-M01	0.50mm	10.16/400	Gullwing	0.63	

 $\begin{tabular}{lll} M: Plastic mold & A: Metal seal (ceramic) \\ F: Frit seal (ceramic) & C: Cerdip(ceramic) \\ \end{tabular}$ 

#### Plastic SONs

Number of pins	Form	Construction	Package code	Lead pitch mm	Package width x package length mm	Lead shape	Weight g/package	Features
40	SON	М	LCC-40P-M02	0.50	_	_		Mounted height: 0.75mm max.
46	SON	М	LCC-46P-M02	0.50	_	_		Mounted height: 0.75mm max.

 $\begin{tabular}{ll} M: Plastic mold & A: Metal seal (ceramic) \\ F: Frit seal (ceramic) & C: Cerdip(ceramic) \end{tabular}$ 

#### Plastic BCCs

Number of pins	Form	Construction	Package code	Lead pitch mm	Package width x package length mm	Lead shape	Weight g/package	Features
16	всс	M	LCC-16P-M02	0.65 0.80	3.40 × 4.55	_	0.02	Bump contact
	10 BCC W		LCC-16P-M03	0.65 0.80	4.20 × 4.55	_	0.02	Bump contact
32	всс	М	LCC-32P-M08	_	_	_	_	
52	200	141	LCC-32P-M09					
48	BCC	М	LCC-48P-M02	_	_	_	_	

M : Plastic mold A : Metal seal (ceramic)
F : Frit seal (ceramic) C : Cerdip(ceramic)

#### Plastic and Ceramic QFPs

		<b>.</b>		1 labile and	Ceramic QFPs		1	+
Number of pins	Form	Construction	Package code	Lead pitch mm	Package width x package length mm	Lead shape	Weight g/package	Features
16	FPT	Α	FPT-16C-A01	_	_	Straight	_	Round package
24	FPT	Α	FPT-24C-A03			Straight		Round package
		Α	FPT-28C-A06			Straight		Round package
28	QFP	А	FPT-28C-A07	1.27	11.40 × 11.40	Gullwing		Length of flat portion of pin: 0.80mm
			FPT-28C-A08	1.27	11.40 × 11.40	Gullwing		Length of flat portion of pin: 0.48mm
32	LQFP	М	FPT-32P-M21	1.27		Gullwing	0.17	
02	QFP	С	FPT-32C-C02	1.27	_	Gullwing		
44	QFP	М	FPT-44P-M11	0.80	10×10	Gullwing	0.42	Length of flat portion of pin: 1.40mm
			FPT-48P-M13	0.80	10×10	Gullwing	0.42	Length of flat portion of pin: 1.80mm
	QFP	М	FPT-48P-M15	0.80	12 × 12	Gullwing	0.75	Length of flat portion of pin: 0.85mm
48			FPT-48P-M16	0.80	12 × 12	Gullwing	0.75	Length of flat portion of pin: 1.80mm
		Α	FPT-48C-A01	0.80	12 × 12	Gullwing		
	LQFP	М	FPT-48P-M05	0.50	7×7	Gullwing	0.17	Length of flat portion of pin: 0.50mm
			FPT-64P-M06	1.00	14 × 20	Gullwing	1.68	Length of flat portion of pin: 1.20mm
	QFP	М	FPT-64P-M07	1.00	14 × 20	Gullwing	1.67	Length of flat portion of pin: 1.70mm
	QFP	A	FPT-64P-M10	1.00	14 × 20	Gullwing	1.68	Length of flat portion of pin: 0.80mm
64			FPT-64C-A01	1.00	14 × 20	Gullwing		
		,,	FPT-64C-A02	1.00	14 × 20	Gullwing		With window(DIA: 370mil)
	LQFP	M	FPT-64P-M03	0.50	10×10	Gullwing	0.32	Length of flat portion of pin: 0.50mm
			FPT-64P-M09	0.65	12 × 12	Gullwing	0.64	
	TQFP	М	FPT-64P-M04	0.50	10×10	Gullwing	0.25	Mounted height: 1.27mm max.
68	QFP	Α	FPT-68C-A01	1.00	19.61 × 19.61	Gullwing		5 fins
			FPT-68C-A02	1.00	19.61 × 19.61	Gullwing		
			FPT-80P-M06	0.80	14 × 20	Gullwing	1.70	Length of flat portion of pin: 0.80mm
	QFP	М	FPT-80P-M07	0.80	14 × 20	Gullwing	1.70	Length of flat portion of pin: 1.20mm
80			FPT-80P-M09	0.80	24 × 24	Gullwing		For multichip module
		Α	FPT-80C-A01	0.80	14 × 20	Gullwing	_	
		_ ^	FPT-80C-A02	0.80	14 × 20	Gullwing	_	With window(DIA : 335mil)
	LQFP	М	FPT-80P-M05	0.50	12 × 12	Gullwing	_	Length of flat portion of pin: 0.50mm

### (continued)

Number of pins	Form	Construction	Package code	Lead pitch mm	Package width x package length mm	Lead shape	Weight g/package	Features
80	LQFP	М	FPT-80P-M11	0.65	14×14	Gullwing	0.62	
00	TQFP	М	FPT-80P-M15	0.50	12 × 12	Gullwing	0.4	
		М	FPT-100P-M06	0.65	14 × 20	Gullwing	1.68	(= M01) Different tie bar position
	QFP	101	FPT-100P-M07	0.65	14 × 20	Gullwing	1.68	(= M01) Different tie bar position
			FPT-100C-A01	0.65	14 × 20	Gullwing		
100		А	FPT-100C-A02	0.65	14 × 20	Gullwing	_	(DIA: 350mil) With window
.00	LQFP	М	FPT-100P-M05	0.50	14×14	Gullwing	0.65	Length of flat portion of pin: 0.50mm
		С	FPT-100C-C01	0.50	14 × 14	Gullwing		
			FPT-100P-M09	0.50	14×14	Gullwing	0.46	
	TQFP	М	FPT-100P-M18	0.40	12 × 12	Gullwing	0.4	
			FPT-100P-M19	0.65	14 × 20	Gullwing	0.7	
104	QFP	М	FPT-104P-M01	0.80	28 × 28	Gullwing		For multichip module
		М	FPT-120P-M03	0.80	28 × 28	Gullwing	5.37	Length of flat portion of pin: 0.80mm
	QFP		FPT-120P-M04	0.80	28 × 28	Gullwing	5.37	Length of flat portion of pin: 1.20mm
	QFP		FPT-120P-M13	0.50	20 × 20	Gullwing		
120		Α	FPT-120C-A01	0.80	27.81 × 27.81	Gullwing		
		С	FPT-120C-C01	0.80	28 × 28	Gullwing		
	LQFP	М	FPT-120P-M05	0.40	14×14	Gullwing	0.62	
	LQII	IVI	FPT-120P-M21	0.50	16 × 16	Gullwing	0.88	
	TQFP	М	FPT-120P-M17	0.40	14×14	Gullwing	_	Mounted height: 1.27mm max.
124	QFP	Α	FPT-124C-A03	0.51/0.76	20.32 × 20.32	Gullwing		
			FPT-144P-M01	0.65	28 × 28	Gullwing	_	Length of flat portion of pin: 0.80mm
	055	М	FPT-144P-M02	0.65	28 × 28	Gullwing		Length of flat portion of pin: 1.20mm
	QFP		FPT-144P-M03	0.50	20 × 20	Gullwing	_	Mounted height: 3.85mm max.
144		С	FPT-144C-C01	0.65	28 × 28	Gullwing	_	
			FPT-144C-C02	0.50	20.20 × 20.20	Gullwing	_	
	LQFP	М	FPT-144P-M08	0.50	20 × 20	Gullwing	_	Length of flat portion of pin: 0.50mm
			FPT-144P-M12	0.40	16 × 16	Gullwing	0.88	
	TQFP	М	FPT-144P-M10	0.50	20 × 20	Gullwing		

### (continued)

Number of pins	Form	Construction	Package code	Lead pitch mm	Package width x package length mm	Lead shape	Weight g/package	Features
			FPT-160P-M03	0.65	28 × 28	Gullwing	5.34	Length of flat portion of pin: 0.80mm
		M	FPT-160P-M04	0.65	28 × 28	Gullwing	5.34	Length of flat portion of pin: 1.20mm
160	QFP		FPT-160P-M11	0.65	28 × 28	Gullwing		Heat spreader built in
100	α	Α	FPT-160C-A01	0.65	27.81 × 27.81	Gullwing		
			FPT-160C-C01	0.65	28 × 28	Gullwing		
		С	FPT-160C-C02	0.65	28 × 28	Gullwing		
			FPT-160C-C04	0.65	28 × 28	Gullwing		
		А	FPT-164C-A03	0.51/0.76/1.02	27.94 × 27.94	Gullwing	_	Length of flat portion of pin: 1.02mm
164	QFP	ζ	FPT-164C-A05	0.51/0.76	27.94 × 27.94	Gullwing	_	Length of flat portion of pin: 1.02mm
		С	FPT-164C-C01	0.65	25.60 × 23.37	Gullwing		
168	QFP	М	FPT-168P-M01	0.80	40 × 40	Gullwing		For multichip module
	QFP	М	FPT-176P-M01	0.50	24 × 24	Gullwing	3.80	Length of flat portion of pin: 0.50mm
		С	FPT-176C-C01	0.50	23.20 × 23.20	Gullwing		
176	LQFP	М	FPT-176P-M02	0.50	24 × 24	Gullwing		Length of flat portion of pin: 0.50mm
	LQII	IVI	FPT-176P-M03	0.40	20 × 20	Gullwing	_	Length of flat portion of pin: 0.50mm
	TQFP	М	FPT-176P-M06	0.40	20 × 20	Gullwing		
196	QFP	С	FPT-196C-C01	0.65	28 × 40	Gullwing		
		М	FPT-208P-M01	0.50	28 × 28	Gullwing	5.25	Length of flat portion of pin: 0.50mm
			FPT-208P-M04	0.50	28 × 28	Gullwing		Heat spreader built in
	QFP	Α	FPT-208C-A01	0.50	28 × 28	Gullwing		
208	QII		FPT-208C-C02	0.50	27.20 × 27.20	Gullwing		
		С	FPT-208C-C03	0.50	27.20 × 27.20	Gullwing	_	Base ceramic: Aluminium nitride
			FPT-208C-C04	0.50	27.20 × 27.20	Gullwing		( = C03) With pin fins
	LQFP	М	FPT-208P-M06	0.50	28 × 28	Gullwing	3.0	
216	LQFP	М	FPT-216P-M01	0.40	24 × 24	Gullwing	_	
232	QFP	М	FPT-232P-M01	0.65	40 × 40	Gullwing	12.47	Length of flat portion of pin: 0.80mm
		С	FPT-232C-C01	0.65	40 × 40	Gullwing	_	
240	QFP	М	FPT-240P-M02	0.50	32 × 32	Gullwing	7.80	Length of flat portion of pin: 0.50mm

(continued)

M : Plastic mold C : Cerdip(ceramic)

A: Metal seal (ceramic)

### (continued)

Number of pins	Form	Construction	Package code	Lead pitch mm	Package width x package length mm	Lead shape	Weight g/package	Features
		М	FPT-240P-M03	0.50	32 × 32	Gullwing	_	Heat spreader built in
240	QFP	А	FPT-240C-A03	0.51/0.63/0.76	37.34 × 37.34	Gullwing	_	7 fins
240	QFF	A	FPT-240C-A05	0.51/0.63/0.76	37.34 × 37.34	Gullwing	_	
		С	FPT-240C-C01	0.50	31.20 × 31.20	Gullwing	_	
256	QFP	М	FPT-256P-M04	0.50	40 × 40	Gullwing	_	For multichip module
230	QFP	IVI	FPT-256P-M06	0.40	28 × 28	Gullwing	_	
		М	FPT-256P-M09	0.40	28 × 28	Gullwing	_	Heat spreader built in
		Α	FPT-256C-A07	0.50	36 × 36	Gullwing	_	3 fins
	QFP		FPT-256C-C01	0.50	27.20×39.20	Gullwing	_	
256	ζ	С	FPT-256C-C02	0.50	27.20×27.20	Gullwing	_	Base ceramic: Aluminium nitride
			FPT-256C-C03	0.50	27.20×27.20	Gullwing	_	Pin fins
	LQFP	М	FPT-256P-M11	0.40	28 × 28	Gullwing	_	
			FPT-260C-A02	0.51/0.76	41.15 × 41.15	Gullwing	_	No fins
260	QFP	Α	FPT-260C-A03	0.51/0.76	41.15 × 41.15	Gullwing	_	12 fins
			FPT-260C-A04	0.51/0.76	41.15 × 41.15	Gullwing	_	5 fins
296	QFP	М	FPT-296P-M01	0.40	32 × 32	Gullwing	_	
		М	FPT-304P-M02	0.50	40 × 40	Gullwing	_	Heat spreader built in
304	QFP		FPT-304C-C01	0.50	39.20 × 39.20	Gullwing	_	
	<b>.</b> .	С	FPT-304C-C02	0.50	39.20 × 39.20	Gullwing	_	Base ceramic: Aluminium nitride
320	QFP	Α	FPT-320C-A01	0.40	36 × 36	Gullwing	_	8 fins
		Α	FPT-368C-A01	0.40	40 × 40	Gullwing	_	8 fins
368	QFP	С	FPT-368C-C01	0.40	39.20 × 39.20	Gullwing	_	Base ceramic: Aluminium nitride

M : Plastic mold F : Frit seal (ceramic)

A : Metal seal (ceramic)
C : Cerdip(ceramic)

### Ceramic QFNs (LCCs)

Number of pins	Construction	Package code	Lead pitch mm/mil	Package width × package length mm/mil	Features
16	F	LCC-16C-F01	1.27/50	$6.35 \times 6.35/250 \times 250$	Body thickness: 100mil
	А	LCC-32C-A01	1.27/50	11.45 × 13.97/450 × 550	Body thickness: 130mil
32	^	LCC-32C-A06	1.016/40	10.67 × 10.67/420 × 420	Body thickness: 130mil
	F	LCC-32C-F01	1.27/50	11.45 × 13.97/450 × 450	Body thickness: 130mil
36	F	LCC-36C-F01	1.27/50	13.97 × 13.97/550 × 550	Body thickness: 130mil
44	F	LCC-44C-F01	1.27/50	16.51 × 16.51/650 × 650	Body thickness: 130mil
48	Α	LCC-48C-A01	1.016/40	14.22 × 14.22/560 × 560	Body thickness: 100mil
64	Α	LCC-64C-A01	1.016/40	18.29 × 18.29/720 × 720	Body thickness: 100mil

#### Plastic and Ceramic QFJs

Number of pins	Construction	Package code	Lead pitch mm/mil	Package width × package length mm/mil	Weight g/package	Features
28	М	LCC-28P-M03	1.27/50	453 × 453	1.00	
32	М	LCC-32P-M02	1.27/50	450 × 550	0.96	
32	С	QFJ-32C-C01	1.27/50	450 × 550	_	
44	М	LCC-44P-M02	1.27/50	653 × 653	2.05	
44	С	QFJ-44C-C01	1.27/50	650 × 650	_	
68	М	LCC-68P-M02	1.27/50	953 × 953	4.25	
84	М	LCC-84P-M02	1.27/50	1153 × 1153	6.20	

 $\begin{tabular}{ll} M: Plastic mold & A: Metal seal (ceramic) \\ F: Frit seal (ceramic) & C: Cerdip(ceramic) \end{tabular}$ 

P : Resin seal

#### Plastic and Ceramic SOJs

Number of pins	Construction	Package code	Lead pitch mm/mil	Package width mm/mil	Weight g/package	Features
26	Α	LCC-26C-A01	1.27/50	8.03/316	_	
28	Α	LCC-28C-A04	1.27/50	10.98/432	_	
42	М	LCC-42P-M01	1.27/50	10.16/400	_	

#### Plastic BGAs

Number of pins	Construction	Package code	Lead pitch mm/mil	Pin matrix	Features
48	М	BGA-48P-M06	1.0	8×6	
40	IVI	BGA-48P-M10	1.0	6×8	
256	М	BGA-256P-M01	1.27/50	20	
230	IVI	BGA-256P-M02	1.27/50	20	
		BGA-352P-M01	1.27/50	26	
		BGA-352P-M02	1.27/50	26	
352	М	BGA-352P-M03	1.27/50	26	
332	IVI	BGA-352P-M04	1.27	26	
		BGA-352P-M05	1.27	26	
		BGA-352P-M08		_	
416	М	BGA-416P-M02	1.27/50	30	
		BGA-420P-M01	1.27/50	26	
420	М	BGA-420P-M02	1.27/50	26	
		BGA-420P-M03	1.27	26	
560	М	BGA-560P-M01		_	
576	М	BGA-576P-M01	1.27	30	
672	М	BGA-672P-M01	1.27	34	
720	М	BGA-720P-M01	_	_	
1196	М	BGA-1196C-M01	_	_	
1344	М	BGA-1344C-M01	_	_	
1600	М	BGA-1600C-M01	_	_	

### Plastic and Ceramic F-BGAs

Number of pins	Construction	Package code	Lead pitch mm	Pin matrix	Features
		BGA-48P-M11	0.80	6 × 8	
48	М	BGA-48P-M12	0.80	6×9	
		BGA-48P-M13	0.80	8×9	
57	М	BGA-57P-M01			
61	М	BGA-61P-M02			
69	М	BGA-69P-M02			
77	М	BGA-77P-M01			
112	М	BGA-112P-M01	0.80	11 × 11 (4 row)	
120	М	BGA-120P-M01	0.80	13 × 13 (3 row)	
144	М	BGA-144P-M01	0.80	14 × 14 (a part is 4 row)	
144	IVI	BGA-144P-M02	0.80	13 × 13 (4 row)	
168	М	BGA-168P-M01	0.80	$14 \times 14$ (a part is 5 row)	
176	М	BGA-176P-M02	0.80	14 × 14 (5 row)	
192	М	BGA-192P-M01	0.80	16 × 16 (4 row)	
192	IVI	BGA-192P-M02	0.50	19 × 19 (3 row)	
224	М	BGA-224P-M03	0.80	18 × 18 (4 row)	
240	М	BGA-240P-M01	0.50	19 × 19 (4 row)	
272	М	BGA-272P-M02	0.80	21 × 21 (4 row)	
288	М	BGA-288P-M02	0.75	22 × 22 (4 row)	
320	М	BGA-320P-M01	0.80	21 × 21 (5 row)	

#### T-BGA Plastic

Number of pins	Construction	Package code	Lead pitch mm/mil	Pin matrix	Features
256	М	BGA-256P-M04	1.27/50	20	Body thickness: 50mil
272	М	BGA-272P-M01	1.27/50	21	Body thickness: 50mil

### LGA Plastic

Number of pins	Construction	Package code	Land pitch mm	Land matrix	Features
48	М	LGA-48P-M01	1.00	6 × 8	
1	IVI	LGA-48P-M02	1.00	6×8	

### TCP Plastic

Number of pins	Form	Construction	Package code	Lead pitch mm	Lead pitch mm/mil	Package width x package length mm/mil	Features
44	DTP	М	DTP-44A-M01	0.80	1.27/50	10.16 × 18.41	
160	QTP	М	QTP-160A-M01	0.30	1.27/50	14.00 × 14.00	
208	QTP	М	QTP-208E-M01	0.30	1.27/50	20.00 × 20.00	
256	QTP	М	QTP-256E-M01	0.30	1.27/50	24.00 × 24.00	
400	QTP	М	QTP-400F-M01	0.30	1.27/50	$28.00 \times 28.00$	

### 3.2.3 SMD Module

### SIMM (SIPs, socket-mounted type)

Number of pins	Construction	Package code	Lead pitch mm/mil	Features
		MSS-72P-P11	1.27/50	With 8 SOJs and 4 PLCCs mounted; module thickness: 200mil max.
		MSS-72P-P12	1.27/50	
		MSS-72P-P23	1.27/50	With 8 SOJs and 4 PLCCs mounted; module thickness: 200mil max.
		MSS-72P-P24	1.27/50	With 16 SOJs and 8 PLCCs mounted; module thickness: 350mil max.
		MSS-72P-P27	1.27/50	With 16 SOJs mounted; module thickness: 350mil max.
		MSS-72P-P29	1.27/50	With 8 SOJs mounted; module thickness: 200mil max.
		MSS-72P-P39	1.27/50	With 16 SOJs mounted; module thickness: 350mil max.
		MSS-72P-P43	1.27/50	With 8 TSOPs mounted; module thickness: 160mil max.
		MSS-72P-P48	1.27/50	With 8 SOJs mounted; module thickness: 200mil max.
		MSS-72P-P49	1.27/50	With 16 SOJs mounted; module thickness: 350mil max.
		MSS-72P-P62	1.27/50	With 3 SOJs mounted; module thickness: 200mil max.
72	М	MSS-72P-P74	1.27/50	With 8 TSOPs mounted; module thickness: 160mil max.
		MSS-72P-P75	1.27/50	With 9 SOJs mounted; module thickness: 160mil max.
		MSS-72P-P76	1.27/50	With 18 SOJs mounted; module thickness: 350mil max.
		MSS-72P-P77	1.27/50	With 8 SOJs mounted; module thickness: 200mil max.
		MSS-72P-P78	1.27/50	With 16 SOJs mounted; module thickness: 350mil max.
		MSS-72P-P79	1.27/50	With 8 SOJs mounted; module thickness: 200mil max.
		MSS-72P-P80	1.27/50	With 16 SOJs mounted; module thickness: 350mil max.
		MSS-72P-P81	1.27/50	With 12 SOJs mounted; module thickness: 350mil max.
		MSS-72P-P82	1.27/50	With 12 SOJs mounted; module thickness: 350mil max.
		MSS-72P-P84	1.27/50	With 18 TSOPs + 2 logic mounted; module thickness: 160mil max.
		MSS-72P-P85	1.27/50	With 4 SOJs mounted; module thickness: 350mil max.
		MSS-72P-P86	1.27/50	With 2 SOJs mounted; module thickness: 200mil max.
		MSS-72P-P87	1.27/50	

#### MZIP

Number of pins	Construction	Package code	Lead pitch mm/mil	Features
72	М	MZP-72P-P01	1.27/50	With 20 SOJs mounted; module thickness: 350mil max.

#### DIMM

Number of pins	Construction	Package code	Lead pitch mm/mil	Features
		MDS-144P-P01	1.00/39	4 TSOPs + 1 logic mounted; module thickness: 150mil max.
		MDS-144P-P02	1.00/39	8 TSOPs + 1 logic mounted; module thickness: 150mil max.
		MDS-144P-P03	1.00/39	4 TSOPs + 1 logic mounted; module thickness: 150mil max.
		MDS-144P-P04	1.00/39	8 TSOPs + 1 logic mounted; module thickness: 150mil max.
144	М	MDS-144P-P05	1.00/39	16 TSOPs + 1 logic mounted; module thickness: 150mil max.
144	IVI	MDS-144P-P06	1.00/39	4 TSOPs + 1 logic mounted; module thickness: 150mil max.
		MDS-144P-P07	1.00/39	8 TSOPs + 1 logic mounted; module thickness: 150mil max.
		MDS-144P-P08	0.80	
		MDS-144P-P09	0.80	
		MDS-144P-P12	0.80	
		MDS-168P-P04	1.27/50	18 TSOPs + 3 logic mounted; module thickness:157mil max.
		MDS-168P-P05	1.27/50	16 TSOPs + 3 logic mounted; module thickness:157mil max.
		MDS-168P-P06	1.27/50	8 SOPs + 3 logic mounted; module thickness:350mil max.
		MDS-168P-P07	1.27/50	8 TSOPs + 1 logic mounted; module thickness:157mil max.
		MDS-168P-P08	1.27/50	4 TSOPs + 1 logic mounted; module thickness:110mil max.
		MDS-168P-P09	1.27/50	16 TSOPs + 1 logic mounted; module thickness:157mil max.
		MDS-168P-P10	1.27/50	8 SOJs + 1 logic mounted; module thickness:200mil max.
		MDS-168P-P11	1.27/50	16 SOJs + 1 logic mounted; module thickness:350mil max.
		MDS-168P-P12	1.27/50	4 SOJs + 1 logic mounted; module thickness:200mil max.
		MDS-168P-P13	1.27/50	8 TSOPs + 1 logic mounted; module thickness:157mil max.
168	М	MDS-168P-P14	1.27/50	16 TSOPs + 1 logic mounted; module thickness:157mil max.
		MDS-168P-P16	1.27	
		MDS-168P-P17	1.27	
		MDS-168P-P18	1.27	
		MDS-168P-P19	1.27	
		MDS-168P-P20	1.27	
		MDS-168P-P21	1.27	
		MDS-168P-P22	1.27	
		MDS-168P-P23	1.27	
		MDS-168P-P24	1.27	
		MDS-168P-P25	1.27	

M : Plastic mold

### (continued)

Number of pins	Construction	Package code	Lead pitch mm/mil	Features
		MDS-168P-P26	1.27	
		MDS-168P-P27	1.27	
168	М	MDS-168P-P28	1.27	
100		MDS-168P-P29	1.27	
		MDS-168P-P36	1.27	
		MDS-168P-P37	1.27	
		MDS-168P-P38	1.27	
168	М	MDS-168P-P39	1.27	
100		MDS-168P-P40	1.27	
		MDS-168P-P41	1.27	
		MDS-200P-P05	1.27/50	
200	М	MDS-200P-P06	1.27/50	
200		MDS-200P-P07	1.27/50	18 TSOPs + 3 logic mounted; module thickness: 160mil max.
		MDS-200P-P08	1.27/50	9 TSOPs + 3 logic mounted; module thickness: 110mil max.

M : Plastic mold

## 3.2.4 Piggy Back

### MQFP

Number of pins	Construction	Package Code	Lead pitch mm/mil	Features
48	С	MQP-48C-P01	0.80/31.5	
64	С	MQP-64C-P01	1.00/39	
		MQP-80C-P01	0.80/31.5	
80	С	MQP-80C-P02	0.80/31.5	
		MQP-80C-C01	0.80/31.5	
100	С	MQP-100C-P01	0.65/25.6	
100		MQP-100C-P02	0.50/19.7	

C : Cerdip(ceramic)

### 3.2.5 Cards

#### Cards

Number of pins	Package code	Lead pitch mm/mil	Features
60	CRD-60P-M01	1.0/39.37	Miniature card
	CRD-60P-M02	1.0/39.37	Miniature card
	CRD-68P-M02	1.27/50	PC Card (TYPE I)
	CRD-68P-M04	1.27/50	PC Card (TYPE I)
	CRD-68P-M05	1.27/50	PC Card (TYPE I)
	CRD-68P-M14	1.27/50	PC Card (TYPE II)
68	CRD-68P-M16	1.27/50	PC Card (TYPE II)
08	CRD-68P-M17	1.27/50	PC Card (TYPE I)
	CRD-68P-M19	1.27/50	PC Card (TYPE II)
	CRD-68P-M24	1.27/50	PC Card (TYPE II)
	CRD-68P-M28	1.27/50	PC Card (TYPE II)
	CRD-68P-M29	1.27/50	PC Card (TYPE II)

## **Package Index**

rackage index	
FPT-48P-M13	MDS-144P-P08
FPT-48P-M15	MDS-144P-P09
FPT-48P-M16	MDS-144P-P12
FPT-48P-M19	MDS-168P-P04
FPT-48P-M20	MDS-168P-P05
FPT-50P-M05	MDS-168P-P06
FPT-50P-M06	MDS-168P-P07
FPT-54P-M01	MDS-168P-P08
FPT-54P-M02	MDS-168P-P09
FPT-56P-M01	MDS-168P-P10
FPT-56P-M02	MDS-168P-P11
FPT-56P-M03	MDS-168P-P12
FPT-56P-M04	MDS-168P-P13
FPT-64C-A01	MDS-168P-P14
FPT-64C-A02	MDS-168P-P16
FPT-64P-M03	MDS-168P-P17
FPT-64P-M04	MDS-168P-P18
FPT-64P-M06	MDS-168P-P19
FPT-64P-M07	MDS-168P-P20
FPT-64P-M09	MDS-168P-P21
FPT-64P-M10	MDS-168P-P22
FPT-68C-A01	MDS-168P-P23
FPT-68C-A02	MDS-168P-P24
FPT-80C-A01	MDS-168P-P25
FPT-80C-A02	MDS-168P-P26
FPT-80P-M05	MDS-168P-P27
FPT-80P-M06	MDS-168P-P28
FPT-80P-M07	MDS-168P-P29
FPT-80P-M09	MDS-168P-P36
FPT-80P-M11	MDS-168P-P37
FPT-80P-M15	MDS-168P-P38
FPT-86P-M01	MDS-168P-P39
FPT-8C-A01	MDS-168P-P40
FPT-8P-M01	MDS-168P-P41
FPT-8P-M02	MDS-200P-P05
FPT-8P-M03	MDS-200P-P06
LCC-16C-F01	MDS-200P-P07
LCC-16P-M02	MDS-200P-P08
LCC-16P-M03	MQP-100C-P01
LCC-26C-A01	MQP-100C-P02
LCC-28C-A04	MQP-48C-P01
LCC-28P-M03	MQP-64C-P01
LCC-32C-A01	MQP-80C-C01
LCC-32C-A06	MQP-80C-P01
LCC-32C-F01	MQP-80C-P02
LCC-32P-M02	MSS-72P-P11
LCC-32P-M08	MSS-72P-P12
LCC-32P-M09	MSS-72P-P23
LCC-36C-F01	MSS-72P-P24
LCC-40P-M02	MSS-72P-P27
LCC-42P-M01	MSS-72P-P29
LCC-44C-F01	MSS-72P-P39
LCC-44P-M02	MSS-72P-P43
LCC-46P-M02	MSS-72P-P48
LCC-48C-A01	MSS-72P-P49
LCC-48P-M02	MSS-72P-P62
LCC-48P-M03	MSS-72P-P74
LCC-64C-A01	MSS-72P-P75
LCC-68P-M02	MSS-72P-P76
LCC-84P-M02	MSS-72P-P77
LGA-48P-M01	MSS-72P-P78
LGA-48P-M02	MSS-72P-P79
MDS-144P-P01	MSS-72P-P80
MDS-144P-P02	MSS-72P-P81
MDS-144P-P03	MSS-72P-P82
MDS-144P-P04	MSS-72P-P84
MDS-144P-P05	MSS-72P-P85
MDS-144P-P06	MSS-72P-P86
MDS-144P-P07	MSS-72P-P87

MZP-72P-P01 PGA-107C-A01 PGA-107C-A02 PGA-121C-A01 PGA-121C-A02 PGA-135C-A01 PGA-135C-A02 PGA-135C-A05 PGA-135C-A06 PGA-135C-C02 PGA-149C-A02 PGA-149C-A03 PGA-149C-A06 PGA-149C-A09 PGA-179C-A02 PGA-179C-A03 PGA-179C-A04 PGA-179C-A06 PGA-208C-A02 PGA-208C-A03 PGA-208C-A04 PGA-208C-A05 PGA-208C-A06 PGA-208C-A07 PGA-211C-A01 PGA-256C-A01 PGA-256C-A02 PGA-256C-A03 PGA-256C-A04 PGA-256C-A05 PGA-256C-A07 PGA-299C-A01 PGA-299C-A02 PGA-321C-A02 PGA-361C-A01 PGA-401C-A02 PGA-401C-A04 PGA-441C-A01 PGA-441C-A03 PGA-441C-A06 PGA-64C-A02 PGA-68C-A01 PGA-80C-A01 PGA-88C-A01 PGA-88C-A02 PGA-88C-A05 PGA-88C-A06 QFJ-32C-C01 QFJ-44C-C01 QTP-160A-M01 QTP-208E-M01 QTP-256E-M01 QTP-400F-M01

# **Package Outline Diagrams**

QFJ

DIP (Standard DIP, SH-DIP, SK-DIP, SL-DIP)

Plastic Plastic Ceramic Ceramic

SIP SOJ

Plastic Plastic

ZIP (Standard ZIP, SZIP)

Ceramic

Plastic BGA

**PGA** Plastic

Plastic T-BGA
Ceramic Plastic

SOP (SOP, SOL, SSOP, TSOP) TCP

Plastic Plastic
Ceramic SMD MODULE

SON SIMM
Plastic MZIP
BCC DIMM

Plastic PIGGY BACK

QFP (QFP, LQFP, TQFP, TPQ) MQFP

Plastic CARD

Ceramic

**Plastic** 

MINIATURE CARD

QFN (LCC) PC CARD
Ceramic DRAM CARD

PCLP

#### 5.1 Sockets

The sockets listed in section 5.2, "List of test sockets," are available as test sockets. For details, on functions, performance, price and delivery, contact the respective socket manufacturers directly. Fujitsu also has its own sockets under development. If a socket not listed in the charts is required, contact the sales department indicated on the endpaper of this data book.

#### Contact information

Company	Address of headquarters	Telephone	Name
Yamaichi Electric Mfg. Co., Ltd.	3-28-7, Nakamagome, Ota-ku, Tokyo, 143	+81-3-3778-6102	Mr. Oguchi
Enplas Corp.	Kojima Bldg 9th Floor, 2-15-1, Dote-machi, Ohmiya-shi, Saitama, 330	+81-48-643-7676	Mr. Watanabe
Fuso Trading *1	TBR Bldg, 2-10-2, Nagatacho, Chiyoda-ku, Tokyo, 100	+81-3-3581-9056	Mr. Itoo
Showa Densen Trading *2	Tomozuna Bldg, 2-17-22, Mita, Minato-ku, Tokyo, 108	+81-3-5440-4710	Mr. Kuroda
Unitechno	Maekawa Shibaura Bldg2, 2-13-9, Shiba-ura, Minato-ku, Tokyo, 108	+81-3-5476-5661	Mr. Yamazaki
Macnica *3	Shirayama Hi-tech Park1-22-2, Shirayama, Midori-ku, Yokohama, 226	-81-45-939-6116	Mr. Suzuki

<sup>\*1:</sup> Fuso Trading is the Japanese agent for Wells Co. of the U.S.

<sup>\*2:</sup> Showa Densen Trading is a representative for Texas Instruments, Japan.

<sup>\*3:</sup> Macnica is an agent for Sumitomo 3M.

#### 5.2 List of Test Sockets

### Plastic SOPs

Package		So	cket
Number of pins	Package code	Model number	Manufacturer
8	EDT OD MO4	IC51-902-2.KS-8571	Yamaichi Electric Mfg.
	FPT-8P-M01	FP-16 (20M) -1.27-06A	Enplas
	FPT-8P-M02	FP-8 (16H) -1.27-06	Enplas
14	EDT 4 4D M00	IC51-0162-658	Yamaichi Electric Mfg.
	FPT-14P-M03	FP-14 (16H) -1.27-05	Enplas
	EDT 4 4D MO4	IC51-902-2.KS-8571	Yamaichi Electric Mfg.
	FPT-14P-M04	FP-14 (20) -1.27-06	Enplas
	EDT 40D MOO	IC51-347.KS-5755	Yamaichi Electric Mfg.
	FPT-16P-M03	FP-16 (28Z) -1.27-10	Enplas
	EDT 4CD MO4	IC51-0162-658	Yamaichi Electric Mfg.
4.0	FPT-16P-M04	FP-16-1.27-05	Enplas
16	EDT 4CD MOS	IC51-0162-911	Yamaichi Electric Mfg.
	FPT-16P-M05	FP-16-0.65-01A	Enplas
	FPT-16P-M06	IC51-902-2.KS-8571	Yamaichi Electric Mfg.
		FP-16 (20) -1.27-06	Enplas
	EDT 20D MO4	IC51-793.KS-7536	Yamaichi Electric Mfg.
	FPT-20P-M01	FP-20-1.27-06	Enplas
	FPT-20P-M02	IC51-0202-347-2	Yamaichi Electric Mfg.
		FP-20-1.27-03	Enplas
20	EDT OOD MOO	IC51-0202-912	Yamaichi Electric Mfg.
20	FPT-20P-M03	FP-20-0.65-01A	Enplas
	FPT-20P-M04	IC51-0202-912	Yamaichi Electric Mfg.
	FF 1-20F-W04	FP-20-0.65-01A	Enplas
	FPT-20P-M05	IC51-0202-347-2	Yamaichi Electric Mfg.
	FF 1-20F-W05	FP-20-1.27-03	Enplas
	FPT-24P-M01	IC51-0242-793	Yamaichi Electric Mfg.
	FPT-24P-M02	IC51-371.KS-7842	Yamaichi Electric Mfg.
24	FF 1-24F-W02	FP-24 (28S) -1.27-08	Enplas
	FPT-24P-M03	IC51-0242-913	Yamaichi Electric Mfg.
	1 1 1-24F -WU3	FP-24-0.65-01A	Enplas
	FPT-28P-M01	IC51-0302-904	Yamaichi Electric Mfg.
28	1 F 1-20F-IVIU I	FP-28-1.27-10	Enplas
20	FPT-28P-M17	IC51-474.KS-94455	Yamaichi Electric Mfg.
	1 F 1-20F-W17	FP-28-1.27-22	Enplas

#### (continued)

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
30	EDT 20D MO2	IC51-0302-914	Yamaichi Electric Mfg.
	FPT-30P-M02	FP-30-0.65-01A	Enplas
	EDT 22D MO2	IC51-0322-937	Yamaichi Electric Mfg.
20	FPT-32P-M02	OTS-32-1.27-06	Enplas
32	EDT OOD MOO	IC51-0322-667-2	Yamaichi Electric Mfg.
	FPT-32P-M03	OTS-32-1.27-12	Enplas
		IC51-0322-1207	Yamaichi Electric Mfg.
	FPT-32P-M24	OTS-32-0.5-02	Enplas
20		648-0322211-A41	Wells (Fuso Trading)
32		IC51-0322-1207	Yamaichi Electric Mfg.
	FPT-32P-M25	OTS-32-0.5-02	Enplas
		648-0322211-A41	Wells (Fuso Trading)
0.4	FPT-34P-M01	IC51-0342-741	Yamaichi Electric Mfg.
34	FPT-34P-M02	IC51-0342-741	Yamaichi Electric Mfg.
	FPT-40P-M01	IC51-0402-708	Yamaichi Electric Mfg.
	FPT-40P-M04	IC51-0402-708	Yamaichi Electric Mfg.
	FPT-40P-M05	IC51-0402-708	Yamaichi Electric Mfg.
40	FPT-40P-M06	IC51-0402-1174	Yamaichi Electric Mfg.
		648-0402211-A41	Wells (Fuso Trading)
	EDT 40D 1407	IC51-0402-1174	Yamaichi Electric Mfg.
	FPT-40P-M07	648-0402211-A41	Wells (Fuso Trading)
		IC235-0402-005	Yamaichi Electric Mfg.
	FPT-44P-M07	FP-40 (44) -0.8-03	Enplas
		674C1444011FA11	Wells (Fuso Trading)
		IC235-0402-005	Yamaichi Electric Mfg.
44	FPT-44P-M08	FP-40 (44) -0.8-03	Enplas
		674C1444011FA11	Wells (Fuso Trading)
	FPT-44P-M16	IC51-0442-1315	Yamaichi Electric Mfg.
	EDT 44D 1440	IC297-0442-003P	Yamaichi Electric Mfg.
	FPT-44P-M18	674C1442211FA13	Wells (Fuso Trading)
	EDT 40D 1440	IC189-0482-077P	Yamaichi Electric Mfg.
40	FPT-48P-M19	648-0482211-A41	Wells (Fuso Trading)
48	EDT 40D MOO	IC189-0482-077P	Yamaichi Electric Mfg.
	FPT-48P-M20	648-0482211-A41	Wells (Fuso Trading)
	FPT-50P-M05	674C1502211FA11	Wells (Fuso Trading)
50	FPT-50P-M06	674C1504411FA11	Wells (Fuso Trading)

### (continued)

	Package	Socket	
Number of pins	Package code	Model number	Manufacturer
	FPT-54P-M01	IC235-0542-218P-2	Yamaichi Electric Mfg.
54	FPT-54P-M02	IC297-0542-004P	Yamaichi Electric Mfg.
		674C1542211FA13	Wells (Fuso Trading)
	FPT-56P-M01	IC354-0562-010P	Yamaichi Electric Mfg.
56	FPT-56P-M02	IC354-0562-010P	Yamaichi Electric Mfg.
30	FPT-56P-M03	CSP056-070	Japan TI (Showa Densen Trading)
	FPT-56P-M04	TS4-056040-019	Unitechno
86	FPT-86P-M01	IC297-0862-005P	Yamaichi Electric Mfg.
00		674C2862211-A11	Wells (Fuso Trading)

### Ceramic SOPs

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
8	FPT-8C-A01	IC51-793.KS-8194	Yamaichi Electric Mfg.
16	FPT-16C-C02	IC51-902.KS-8571	Yamaichi Electric Mfg.

### Plastic QFPs

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
32	FPT-32P-M21	FPQ-32-0.8-01	Enplas
44	FPT-44P-M11	IC51-0444-228	Yamaichi Electric Mfg.
44	FF 1-44F-WITT	FPQ-44-0.8-14A	Enplas
	FPT-48P-M05	IC51-0484-806	Yamaichi Electric Mfg.
	FP1-48P-IVIU5	FPQ-48-0.5-06	Enplas
48	FPT-48P-M13	FPQ-48-0.8-01	Enplas
40	FPT-14P-M15	FPQ-48-0.8-05	Enplas
	FPT-48P-M16	IC51-0484-630	Yamaichi Electric Mfg.
		FPQ-48-0.8-05	Enplas
	FPT-64P-M03	QP1-064050-108	Unitechno
		IC51-0644-807	Yamaichi Electric Mfg.
64		FPQ-64-0.5-01	Enplas
04		QP1-064050-108	Unitechno
	FPT-64P-M04	IC51-0644-807	Yamaichi Electric Mfg.
		FPQ-64-0.5-01	Enplas

#### (continued)

Package		kage Socket	
Number of pins	Package code	Model number	Manufacturer
		680H642111	Wells (Fuso Trading)
	FPT-64P-M06	IC53-108.KS-7507	Yamaichi Electric Mfg.
		FPQ-64-1.0-08A	Enplas
	EDT 64D MOZ	IC53-108.KS-7507	Yamaichi Electric Mfg.
C.4	FPT-64P-M07	FPQ-64-1.0-08A	Enplas
64	EDT 64D MOO	IC51-0644-1602	Yamaichi Electric Mfg.
	FPT-64P-M09	FPQ-64-0.65-02	Enplas
		680H642111	Wells (Fuso Trading)
	FPT-64P-M10	IC53-108.KS-7507	Yamaichi Electric Mfg.
		FPQ-64-1.0-08A	Enplas
		IC234-0804-026P	Yamaichi Electric Mfg.
		IC51-0804-808	Yamaichi Electric Mfg.
	FPT-80P-M05	OTQ-80-0.5-02	Enplas
		QP1-080050-147	Unitechno
80		FPQ-80-0.5-04	Enplas
	FPT-80P-M06	IC201-0804-005	Yamaichi Electric Mfg.
		IC51-0804-819-4	Yamaichi Electric Mfg.
		FPQ-80-0.8-13A	Enplas
		QP1-080080-007	Unitechno
		IC51-0804-819-4	Yamaichi Electric Mfg.
	FPT-80P-M07	FPQ-80-0.8-13A	Enplas
		QP1-080080-007	Unitechno
80		IC218-0804-007P	Yamaichi Electric Mfg.
80	FPT-80P-M11	IC51-1311.KS-12951	Enplas
		FPQ-80-0.65-09A	Unitechno
	FPT-80P-M15	QP1-080050-147	Unitechno
	T F 1-00F-WITO	IC234-0804-026P	Yamaichi Electric Mfg.
		IC234-1004-023P	Yamaichi Electric Mfg.
	FPT-100P-M05	IC51-1004-809	Yamaichi Electric Mfg.
	T F I- TOUF -IVIUS	FPQ-100-0.5-10A	Enplas
100		QP1-100050-146	Unitechno
100		IC234-1004-009P	Yamaichi Electric Mfg.
	FPT-100P-M06	IC51-1004-814-4	Yamaichi Electric Mfg.
	T I TOUT -IVIUU	FPQ-100-0.65-11A	Enplas
		QP1-100065-004	Unitechno

### (continued)

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
		IC51-1004-814-4	Yamaichi Electric Mfg.
	FPT-100P-M07	FPQ-100-0.65-11A	Enplas
		QP1-100065-004	Unitechno
		IC51-1004-809	Yamaichi Electric Mfg.
	FPT-100P-M09	FPQ-100-0.5-10A	Enplas
100		QP1-100050-146	Unitechno
	FPT-100P-M18	IC234-045P.AC-10654	Yamaichi Electric Mfg.
		680-1001111-002	Wells (Fuso Trading)
	FPT-100P-M19	CQF100-151	Japan TI (Showa Densen Trading)
	FF1-100F-W119	QP1-100065-003	Unitechno
		IC51-1004-814-7	Yamaichi Electric Mfg.
	FPT-120P-M03	IC234-1204-065P	Yamaichi Electric Mfg.
		IC51-844.KS-7692	Yamaichi Electric Mfg.
		FPQ-120-0.8-03A	Enplas
		QP1-120080-177	Unitechno
		IC234-1204-065P	Yamaichi Electric Mfg.
120	FPT-120P-M04	IC51-844.KS-7692	Yamaichi Electric Mfg.
	1 F 1-120F-W04	FPQ-120-0.8-03A	Enplas
		QP1-120080-177	Unitechno
	FPT-120P-M13	QP1-144050-044-1	Unitechno
	FPT-120P-M21	IC234-1204-058P	Yamaichi Electric Mfg.
		QP1-120050-272	Unitechno
		IC51-1444-1014	Yamaichi Electric Mfg.
	FPT-144P-M01	FPQ-144-0.65-02A	Enplas
		QP1-144065-178	Unitechno
		IC51-1444-1014	Yamaichi Electric Mfg.
144	FPT-144P-M02	FPQ-144-0.65-02A	Enplas
		QP1-144065-178	Unitechno
	FPT-144P-M03	QP1-1440050-044	Unitechno
	EDT 1//D M/OO	IC51-1444-1354-7	Yamaichi Electric Mfg.
	FPT-144P-M08	FPQ-144-0.5-03	Enplas
	FPT-144P-M12	IC234-1444-053P	Yamaichi Electric Mfg.

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
		IC234-1604-064P	Yamaichi Electric Mfg.
	FPT-160P-M03	IC51-845.KS-7764	Yamaichi Electric Mfg.
	FP 1- 100P-10103	FPQ-160-0.65-06A	Enplas
		QP1-160065-165	Unitechno
160		IC234-1604-064P	Yamaichi Electric Mfg.
	FPT-160P-M04	IC51-845.KS-7764	Yamaichi Electric Mfg.
	FF 1-100F-W04	FPQ-160-0.65-06A	Enplas
		QP1-160065-165	Unitechno
	FPT-160P-M11	QP1-160065-010	Unitechno
176	FPT-176P-M02	IC234-1764-002P	Yamaichi Electric Mfg.
	FPT-208P-M01	QP4-208050-024	Unitechno
		IC51-2084-1052-9	Yamaichi Electric Mfg.
		FPQ-208-0.5-06	Enplas
208		QP1-208050-014	Unitechno
	FPT-208P-M04	QP4-208050-024	Unitechno
	FF 1-200F-1V104	QP1-208050-182	Unitechno
	FPT-208P-M06	IC201-001.AC-08598	Yamaichi Electric Mfg.
216	FPT-216P-M01	IC234-2164-050P	Yamaichi Electric Mfg.
240	FPT-240P-M03	EXC97072CQF240	Japan TI (Showa Densen Trading)
304	FPT-304P-M02	IC201-004.AC-07901	Yamaichi Electric Mfg.

### Ceramic QFPs

Package			Socket
Number of pins	Package code	Model number	Manufacturer
16	FPT-16C-A01	FP-16-0.0-02	Enplas
48	FPT-48C-A01	FPQ-48-0.8-03	Enplas
	EDT 040 A04	IC51-820-2.KS-9376	Yamaichi Electric Mfg.
0.4	FPT-64C-A01	OTQ-64-1.0-03	Enplas
64	EDT 040 A00	IC51-820-2.KS-9177	Yamaichi Electric Mfg.
	FPT-64C-A02	OTQ-64-1.0-03	Enplas
		IC51-0804-819-4	Yamaichi Electric Mfg.
	FPT-80C-A01	FPQ-80-0.8-13A	Enplas
80		QP1-080080-007	Unitechno
	EDT 00C 402	FPQ-80-0.8-13A	Enplas
	FPT-80C-A02	QP1-080080-007	Unitechno
		IC51-1004-814-4	Yamaichi Electric Mfg.
	FPT-100C-A01	FPQ-100-0.65-10D	Enplas
100		QP1-100065-004	Unitechno
100	FPT-100C-A02	FPQ-100-0.65-10D	Enplas
	FPT-100C-C01	FPQ-100-0.5-10A	Enplas
		QP1-100050-146	Unitechno
		IC51-844.KS-7692	Yamaichi Electric Mfg.
	FPT-120C-A01	FPQ-120-0.8-03A	Enplas
120		QP1-120080-177	Unitechno
120		IC51-844.KS-7692	Yamaichi Electric Mfg.
	FPT-120C-C01	FPQ-120-0.8-03A	Enplas
		QP1-120080-177	Unitechno
144	FPT-144C-C01	FPQ-144-0.65-02A	Enplas
144	FF 1-144C-C01	QP1-144065-178	Unitechno
	FPT-160C-A01	FPQ-160-0.65-06A	Enplas
	FF1-100C-A01	QP1-160065-165	Unitechno
	FPT-160C-C01	FPQ-160-0.65-06A	Enplas
160	1 F 1-100C-C01	QP1-160065-165	Unitechno
100	FPT-160C-C02	FPQ-160-0.65-06A	Enplas
	1 F 1-100C-C02	QP1-160065-165	Unitechno
	EDT-160C C04	FPQ-160-0.65-06A	Enplas
	FPT-160C-C04	QP1-160065-165	Unitechno

### Ceramic LCCs

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
16	LCC-16C-F01	IC53-0164-083	Yamaichi Electric Mfg.
	LCC-32C-A01	IC51-0324-1259	Yamaichi Electric Mfg.
	LCC-32C-A01	LCC-32-1.27- (03)	Enplas
32	LCC-32C-A06	IC53-0324-098	Yamaichi Electric Mfg.
	LCC-32C-F01	IC53-0324-065	Yamaichi Electric Mfg.
	LCC-32C-FUT	LCC-32-1.27- (03)	Enplas
36	LCC-36C-F01	IC53-0364-296	Yamaichi Electric Mfg.
44	LCC-44C-F01	IC53-0444-306	Yamaichi Electric Mfg.
44		LCC-44-1.27- (01)	Enplas
48	LCC-48C-A01	IC53-0484-100	Yamaichi Electric Mfg.

### Plastic QFJs (PLCCs), SOJs

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
		IC51-0284-399	Yamaichi Electric Mfg.
28	LCC-28P-M03	PLCC-28-1.27-30	Enplas
		647X1281912	Wells (Fuso Trading)
		IC51-0324-453	Yamaichi Electric Mfg.
32	LCC-32P-M02	PLCC-32-1.27-30	Enplas
		647X0321912	Wells (Fuso Trading)
42	LCC-42P-M01	IC100-4204-G	Yamaichi Electric Mfg.
42		IC255-4204	Yamaichi Electric Mfg.
		IC51-0444-400	Yamaichi Electric Mfg.
44	LCC-44P-M02	PLCC-44-1.27-30	Enplas
		647X1441912	Wells (Fuso Trading)
	LCC-68P-M02	IC51-0684-390-2	Yamaichi Electric Mfg.
68		PLCC-68-1.27-30	Enplas
		647X1681912	Wells (Fuso Trading)
	LCC-84P-M02	IC51-0844-401-2	Yamaichi Electric Mfg.
84		PLCC-84-1.27-30	Enplas
		647X1841912	Wells (Fuso Trading)

### Ceramic QFJs, SOJs

	Package	Socket	
Number of pins	Package code	Model number	Manufacturer
26	LCC-26C-A01	IC100-2603-20-G	Yamaichi Electric Mfg.
20	LCC-20C-AUT	IC107-2603-20-G	Yamaichi Electric Mfg.
	QFJ-32C-C01	IC51-0324-453	Yamaichi Electric Mfg.
32		PLCC-32-1.27-30	Enplas
		647X0321912	Wells (Fuso Trading)
	QFJ-44C-C01	IC51-0444-400	Yamaichi Electric Mfg.
44		PLCC-44-1.27-30	Enplas
		647X1441912	Wells (Fuso Trading)

#### Modules

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
	MSS-30P-P03	IC-176-4	Yamaichi Electric Mfg.
	MSS-30P-P04	IC-176-4	Yamaichi Electric Mfg.
	MSS-30P-P11	IC-176-4	Yamaichi Electric Mfg.
	MSS-30P-P12	IC-176-4	Yamaichi Electric Mfg.
	MSS-72P-P11	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P12	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P16	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P17	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P18	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P19	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P23	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P24	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P27	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P29	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P39	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P43	IC-176-2	Yamaichi Electric Mfg.
MOO	MSS-72P-P44	IC-176-2	Yamaichi Electric Mfg.
MSS	MSS-72P-P48	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P49	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P62	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P70	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P74	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P75	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P76	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P77	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P78	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P79	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P80	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P81	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P82	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P84	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P85	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P86	IC-176-2	Yamaichi Electric Mfg.
	MSS-72P-P87	IC-176-2	Yamaichi Electric Mfg.
MDC	MDS-144P-P01	IC-497-1-2	Yamaichi Electric Mfg.
MDS	MDS-144P-P02	IC-497-1-2	Yamaichi Electric Mfg.

### (continued)

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
	MDS-144P-P03	IC-497-1-2	Yamaichi Electric Mfg.
	MDS-144P-P04	IC-497-1-2	Yamaichi Electric Mfg.
	MDS-144P-P05	IC-497-1-2	Yamaichi Electric Mfg.
	MDS-144P-P06	IC-497-1-2	Yamaichi Electric Mfg.
	MDS-144P-P07	IC-497-1-2	Yamaichi Electric Mfg.
	MDS-144P-P08	IC-497-1-2	Yamaichi Electric Mfg.
	MDS-144P-P09	IC-497-1-2	Yamaichi Electric Mfg.
	MDS-144P-P11	IC-497-1-2	Yamaichi Electric Mfg.
	MDS-144P-P12	IC-497-1-2	Yamaichi Electric Mfg.
	MDS-168P-P04	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P05	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P06	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P07	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P08	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P09	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P10	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P11	IC-438-3-2	Yamaichi Electric Mfg.
MDS	MDS-168P-P12	IC-438-3-2	Yamaichi Electric Mfg.
MDS	MDS-168P-P13	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P14	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P16	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P17	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P18	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P19	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P20	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P21	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P22	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P23	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P24	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P25	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P26	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P27	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P28	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P29	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P36	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P37	IC-438-3-2	Yamaichi Electric Mfg.

### (continued)

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
MDS	MDS-168P-P38	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P39	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P40	IC-438-3-2	Yamaichi Electric Mfg.
	MDS-168P-P41	IC-438-3-2	Yamaichi Electric Mfg.

## Piggyback devices

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
	MQP-48C-P01	IC53-0484-107	Yamaichi Electric Mfg.
MQP	MQP-64C-P01	IC53-0644-237	Yamaichi Electric Mfg.
	MQP-80C-P01	IC53-0804-160	Yamaichi Electric Mfg.
IVIQE	MQP-80C-P02	IC53-0804-160	Yamaichi Electric Mfg.
	MQP-80C-C01	IC53-0804-160	Yamaichi Electric Mfg.
	MQP-100C-P01	IC53-1004-374	Yamaichi Electric Mfg.

#### **BGAs**

Package			Socket
Number of pins	Package code	Model number	Manufacturer
48	BGA-48P-M06	654048228010812	Wells (Fuso Trading)
40	BGA-48P-M10	654048231010812	Wells (Fuso Trading)
		CBG256-011	Japan TI (Showa Densen Trading)
	BGA-256P-M01	NP276-37206.AC-02310	Yamaichi Electric Mfg.
256		BGA-256 (441) -1.27-08	Enplas
256		CBG256-011	Japan TI (Showa Densen Trading)
	BGA-256P-M02	NP276-40009.AC-06524	Yamaichi Electric Mfg.
		BGA-292 (441) -1.27-08	Enplas
		CBG352-014	Japan TI (Showa Densen Trading)
	DCA 252D MO4	NP276-59608.AC-13306-2	Yamaichi Electric Mfg.
	BGA-352P-M01	2352K-9228-01-1401	Sumitomo 3M (Macnica)
		BGA-352 (841) -1.27-19	Enplas
		CBG352-014A	Japan TI (Showa Densen Trading)
	DCA 252D MO2	NP276-59608.AC-04944	Yamaichi Electric Mfg.
	BGA-352P-M02	2352-9228-01-1401	Sumitomo 3M (Macnica)
		BGA-352 (841) -1.27-14	Enplas
		CBG352-014A	Japan TI (Showa Densen Trading)
352	DCA SESD MOS	NP276-59608.AC-11092	Yamaichi Electric Mfg.
	BGA-352P-M03	2352-9228-01-1401	Sumitomo 3M (Macnica)
		BGA-352 (841) -1.27-14	Enplas
		EXC96124-352	Japan TI (Showa Densen Trading)
	BGA-352P-M04	NP276-59608.AC-13306-1	Yamaichi Electric Mfg.
		2352K-9228-01-1401	Sumitomo 3M (Macnica)
	BGA-352P-M05	CBG352-014	Japan TI (Showa Densen Trading)
		NP276-59608.AC-13306-1	Yamaichi Electric Mfg.
		2352K-9228-01-1401	Sumitomo 3M (Macnica)
		BGA-352 (841) -1.27-19	Enplas
416	BGA-416P-M02	NP276-76820.AC-06127	Yamaichi Electric Mfg.
410	DGA-410F-W02	BGA-416 (1089) -1.27-04	Enplas
		NP276-59608.AC-03119	Yamaichi Electric Mfg.
	BGA-420P-M01	2420-9228-01-1401	Sumitomo 3M (Macnica)
420		BGA-420 (841) -1.27-14	Enplas
420		NP276-59608.AC-03035	Yamaichi Electric Mfg.
	BGA-420P-M02	2420-9228-01-1401	Sumitomo 3M (Macnica)
		BGA-484 (841) -1.27-14	Enplas

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
		EXC96144-420	Japan TI (Showa Densen Trading)
420	BGA-420P-M03	NP276-59608.AC-09801	Yamaichi Electric Mfg.
		2420K-9228-01-1401	Sumitomo 3M (Macnica)
		CBG480-014	Japan TI (Showa Densen Trading)
	DCA 400D M04	NP276-59608.AC-03036	Yamaichi Electric Mfg.
	BGA-480P-M01	2480-9228-01-1401	Sumitomo 3M (Macnica)
400		BGA-480 (841) -1.27-14	Enplas
480	BGA-480P-M02	CBG480-014	Japan TI (Showa Densen Trading)
		NP276-59608.AC-11093	Yamaichi Electric Mfg.
		2480-9228-01-1401	Sumitomo 3M (Macnica)
		BGA-544 (841) -1.27-14	Enplas
560	BGA-560P-M01	NP352-56009	Yamaichi Electric Mfg.
F70	BGA-576P-M01	NP276-76820.AC-06128	Yamaichi Electric Mfg.
576		BGA-576 (1089) -1.27-17	Enplas
672	BGA-672P-M01	NP276-97623.AC-06017	Yamaichi Electric Mfg.
		2-0672-08407-000	Sumitomo 3M (Macnica)
700	BGA-720P-M01	NP352-72017	Yamaichi Electric Mfg.
720		IC280-720-106	Yamaichi Electric Mfg.

### **FBGAs**

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
	BGA-48P-M11	654048628010806	Wells (Fuso Trading)
		703-1048-01	Wells (Fuso Trading)
48	BGA-48P-M12	654048631010806	Wells (Fuso Trading)
40	DGA-40F-IVI12	703-1048-02	Wells (Fuso Trading)
	BGA-48P-M13	654048632010806	Wells (Fuso Trading)
	DGA-40P-IVI 13	703-1048-03	Wells (Fuso Trading)
61	BGA-61P-M02	NP351-05673-1	Yamaichi Electric Mfg.
69	BGA-69P-M02	NP351-05672-1	Yamaichi Electric Mfg.
77	BGA-77P-M01	NP351-05671-1	Yamaichi Electric Mfg.
120	BGA-120P-M01	NP351-12114-1.AC-13081	Yamaichi Electric Mfg.
144	BGA-144P-M01	NP291-16803-1.AC-06328	Yamaichi Electric Mfg.
168	BGA-168P-M01	NP291-16803-1	Yamaichi Electric Mfg.
176	BGA-176P-M01	NP291-16803-1.AC-08229	Yamaichi Electric Mfg.
192	BGA-192P-M01	NP351-19219-1	Yamaichi Electric Mfg.
192	BGA-192P-M02	IC274-192267	Yamaichi Electric Mfg.
224	BGA-224P-M02	NP351-22405-1	Yamaichi Electric Mfg.
240	BGA-240P-M01	IC274-240269	Yamaichi Electric Mfg.
256	BGA-256P-M04	BGA-256 (441) -1.27-29	Enplas
272	BGA-272P-M01	BGA-272 (441) -1.27-25	Enplas
288	BGA-288P-M02	NP291-28807-1	Yamaichi Electric Mfg.
320	BGA-320P-M01	NP351-21653-1.AC-14425	Yamaichi Electric Mfg.

### T-BGAs

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
256	BGA-256P-M04	BGA-256 (441) -1.27-29	Enplas
272	BGA-256P-M01	BGA-272 (441) -1.27-25	Enplas

### FC-BGAs

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
1600 BG/	BGA-1600C-M01	NP352-160018	Yamaichi Electric Mfg.
	BGA-1000C-W01	21600-9130-90-1101	Sumitomo 3M (Macnica)

### SONs

Package		Socket	
Number of pins	Package code	Model number	Manufacturer
40	LCC-40P-M02	685-0402211	Wells (Fuso Trading)
46	LCC-46P-M02	IC162-0462-055P	Yamaichi Electric Mfg.

### 6.1 Packing for Shipment

### 6.1.1 Packing form

The packaging used to deliver products consits of tubes, trays, tapes, inner boxes, and an outer box. (See Figures 1 to 7.)

The tubes, trays, and tapes are designed to protect the products from damage. After unpacking the products, however, handle them with care not to let ICs become loose or protrude to prevent them from being damaged.

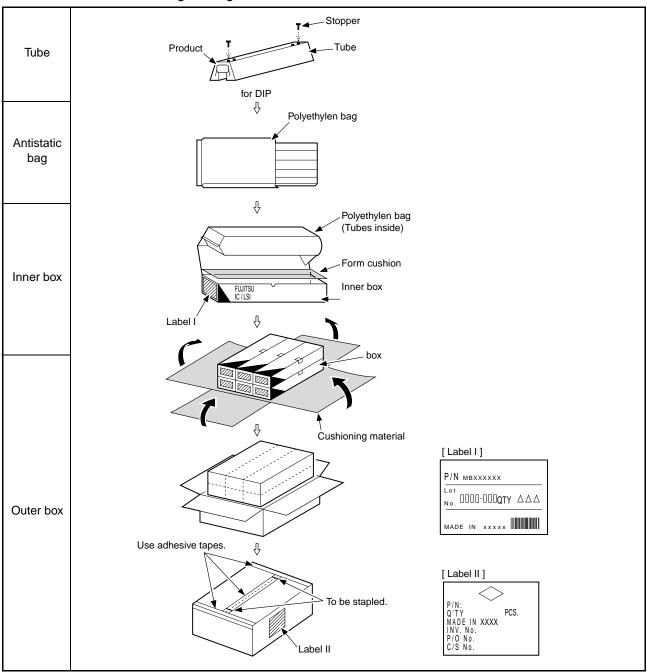


Fig.1 Packing Form for DIP, SIP, ZIP, LCC Tubes

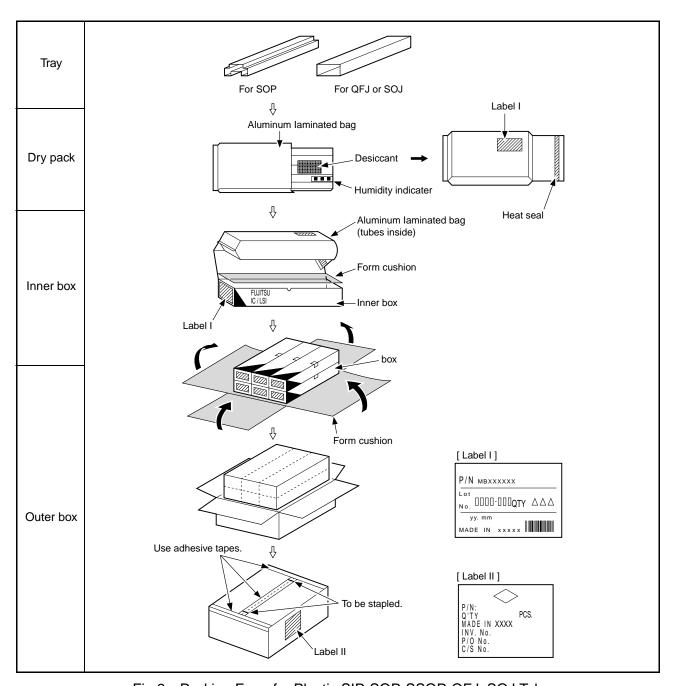


Fig.2 Packing Form for Plastic SIP, SOP, SSOP, QFJ, SOJ Tubes

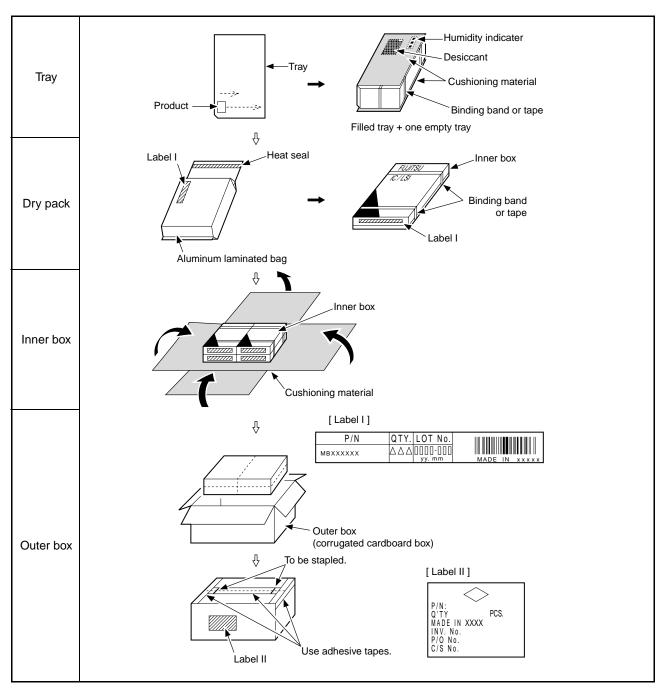


Fig.3 Packing Form for Plastic SOP, SSOP, TSOP, QFP, LQFP, TQFP Trays

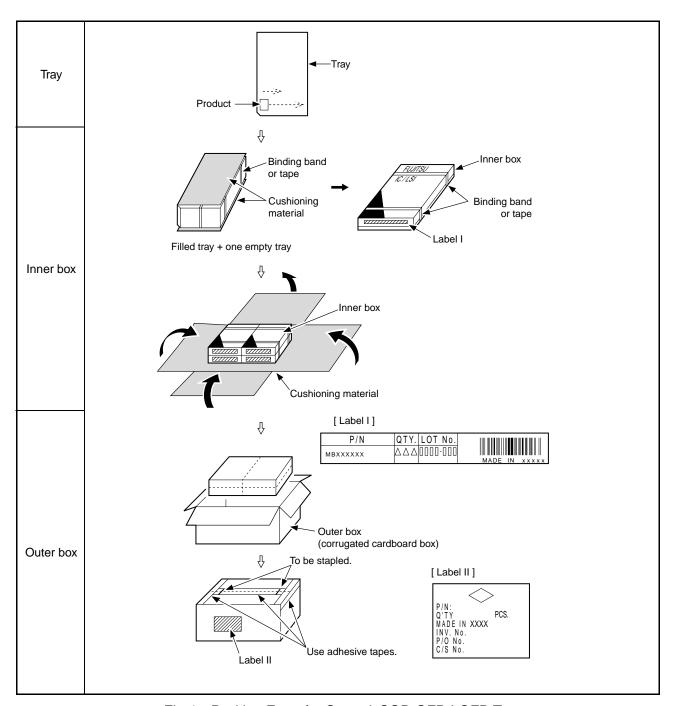


Fig.4 Packing Form for CeramicSOP, QFP, LQFP Trays

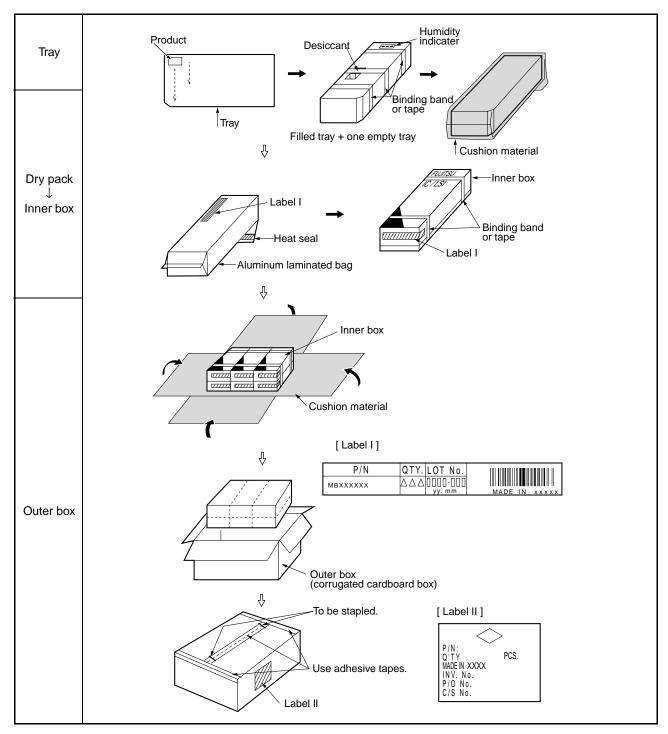


Fig.5 Packing Form for Plastic SOP, TSOP, SON, BCC, QFP, LQFP, TQFP, BGA, T-BGA, FBGA JEDEC Size Trays

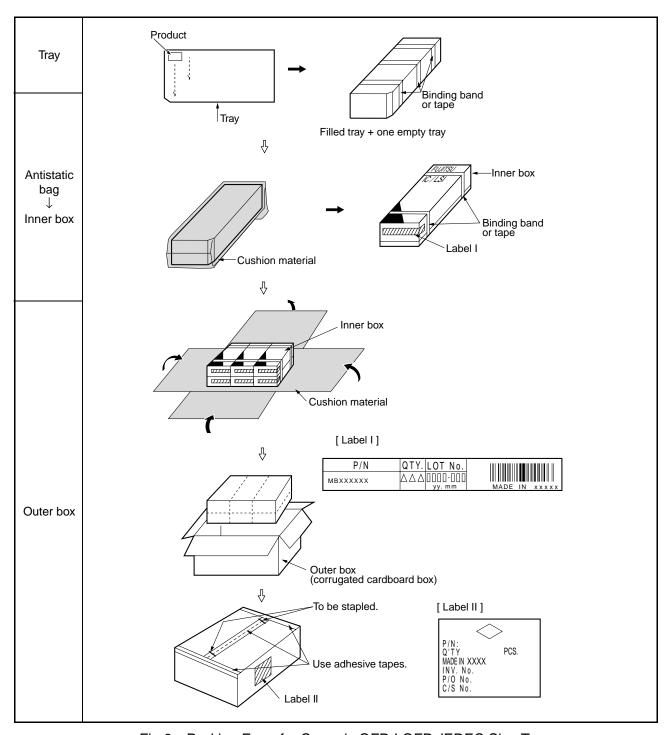


Fig.6 Packing Form for Ceramic QFP, LQFP JEDEC Size Tray

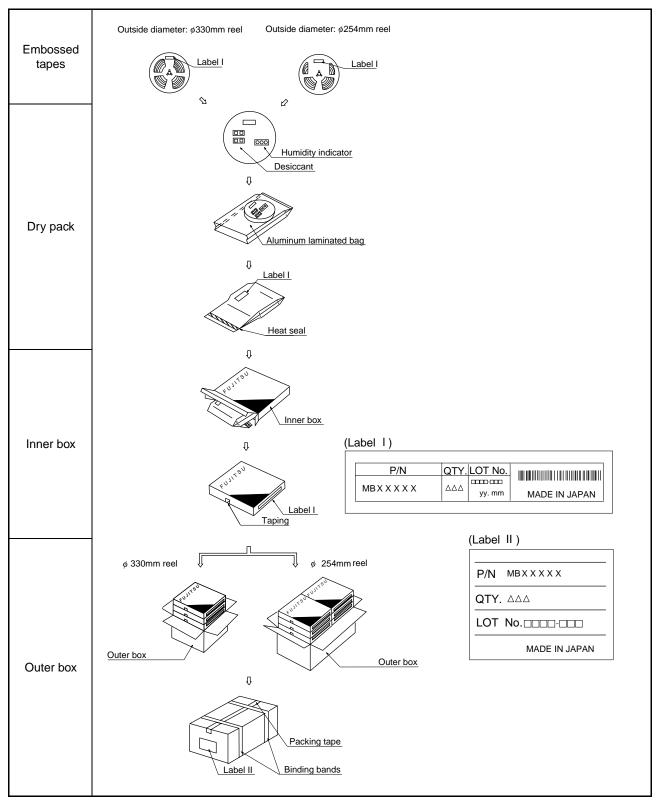


Fig.7 Packing form (Embossed tapes)

### • Embossed tapes

Package code			Remarks		
		ICs/reel	Inner box/outer box	ICs/outer box	(outer diameter of reel used)
	FPT-8P-M01	1500	7	10500	ф 330 mm
	FPT-8P-M02	1500	7	10500	ф 330 mm
	FPT-14P-M04	1500	7	10500	ф 330 mm
	FPT-16P-M03	1500	5	7500	ф 330 mm
	FPT-16P-M04	1500	7	10500	ф 330 mm
S	FPT-16P-M06	1500	7	10500	ф 330 mm
0	FPT-20P-M01	1500	5	7500	ф 330 mm
P	FPT-20P-M02	1500	5	7500	ф 330 mm
	FPT-20P-M05	1500	5	7500	ф 330 mm
	FPT-24P-M01	1500	5	7500	ф 330 mm
	FPT-24P-M02	1500	5	7500	ф 330 mm
	FPT-28P-M01	1000	5	5000	ф 330 mm
	FPT-32P-M03	1000	5	5000	ф 330 mm
	FPT-8P-M03	2000	6	12000	φ 254 mm
	FPT-16P-M05	1500	7	10500	ф 330 mm
	FPT-20P-M03	1500	7	10500	ф 330 mm
S	FPT-20P-M04	1500	7	10500	ф 330 mm
S	FPT-24P-M03	1500	7	10500	φ 330 mm
0	FPT-30P-M02	1500	7	10500	ф 330 mm
P	FPT-34P-M01	1000	5	5000	ф 330 mm
	FPT-34P-M02	1000	5	5000	ф 330 mm
	FPT-34P-M03	1000	6	6000	φ 254 mm
	FPT-40P-M01	1000	5	5000	ф 330 mm
	FPT-32P-M24	1000	5	5000	ф 330 mm
	FPT-32P-M25	1000	5	5000	ф 330 mm
	FPT-40P-M06	1000	5	5000	ф 330 mm
	FPT-40P-M07	1000	5	5000	ф 330 mm
Т	FPT-44P-M07	1000	6	6000	ф 254 mm
S	FPT-44P-M08	1000	6	6000	φ 254 mm
0	FPT-44P-M16	750	4	3000	ф 330 mm
Р	FPT-44P-M18	1000	6	6000	φ 254 mm
	FPT-48P-M19	1000	5	5000	φ 330 mm
	FPT-48P-M20	1000	5	5000	ф 330 mm
	FPT-50P-M05	1000	6	6000	φ 254 mm
	FPT-50P-M06	1000	6	6000	ф 254 mm
S	LCC-40P-M02	1000	6	6000	φ 254mm
N	LCC-46P-M02	1000	6	6000	ф 254mm
В	LCC-16P-M02	2000	6	12000	φ 254mm
С	LCC-16P-M03	2000	6	12000	φ 254mm
С	LCC-48P-M02	2000	6	12000	φ 254mm

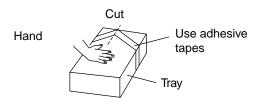
(continued)

### (continued)

			Quantity			
Package code		ICs/reel Inner box/outer box ICs/oute		ICs/outer box	(outer diameter of reel used)	
Q F P	FPT-32P-M21	T-32P-M21 1500 7 10500		10500	φ 330 mm	
L Q	FPT-48P-M05	1500	7	10500	ф 330 mm	
F P	FPT-64P-M03	1500	5	7500	φ 330 mm	
	LCC-28P-M03	800	5	4000	ф 330 mm	
Q	LCC-32P-M02	1000	5	5000	ф 330 mm	
F	LCC-44P-M02	500	5	2500	ф 330 mm	
J	LCC-68P-M02	300	4	1200	ф 330 mm	
	LCC-84P-M02	200	4	800	ф 330 mm	
S O	LCC-28C-A04	1000	5	5000	φ 330 mm	
J	LCC-42P-M01	500	4	2000	ф 330 mm	
	BGA-48P-M01	2000	6	12000	4 25 4 mm	
F	BGA-48P-M11	2000	6	12000	φ 254 mm	
В	BGA-48P-M02	2000	40000	42000	± 05.4 mm	
G A	BGA-48P-M12	2000	6	12000	φ 254 mm	
^	BGA-48P-M03	1500	6	0000	. → 25.4 mm	
	BGA-48P-M13	1500	O	9000	φ 254 mm	

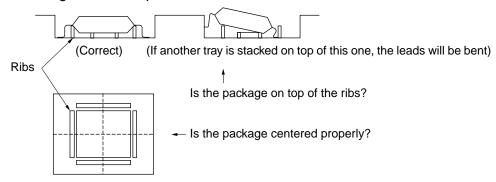
### 6.1.2 Packing handling precautions

- (1)Because corrugated cardboard boxes gradually weaken during storage as a result of moisture, stacking, etc., inventory should be handled on a first-in, first-out basis.
- (2)Although the IC packing materials (such as tubes) are designed to protect against electrostatic damage, full precautions against electrostatic damage must be taken when removing the products from their packing.
- (3)Handle the inner and outer boxes gently. Rough handling can dislodge the tubes stoppers, allowing the products to fall out of the tubes and possibly bend their leads. Rough handling of boxes containing ceramic packages in particular can cause chips, cracks, or leak defects.
- (4)Because the inner and outer boxes are made of corrugated cardboard, do not allow them to become wet. Do not store the boxes outside or in a hot and humid location under any circumstances.
- (5)Do not throw, drop or otherwise roughly handle the outer boxes (corrugated cardboard boxes) containing products under any circumstances.
- (6)QFPs are packed in trays, but it is very easy to accidentally bend their leads when removing them from their original packing and placing them in other packing. Therefore, strictly obsever the following items.
- a) When cutting the binding tape, hold the trays securely in place with one hand.

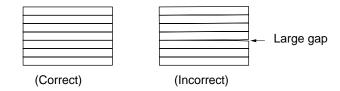


- b) Hold the trays firmly in place while removing the tape so that the trays do not shift position. (If the trays shift, the packed ICs may be dislodged and the leads may bend.)
- c) Check after packing the packages
  - Before stacking the trays containing the IC packages, check the following points.
  - •Make sure that the packages are properly positioned on the ribs.
  - •If any packages are not positioned on the ribs, , check for bent leads and then reposition the packages.

Note: When handling the packages, make sure to take electrostatic countermeasures, such as wearing finger sleeves or a grounded wrist strap. Use air tweezers, and avoid touching the leads if possible.

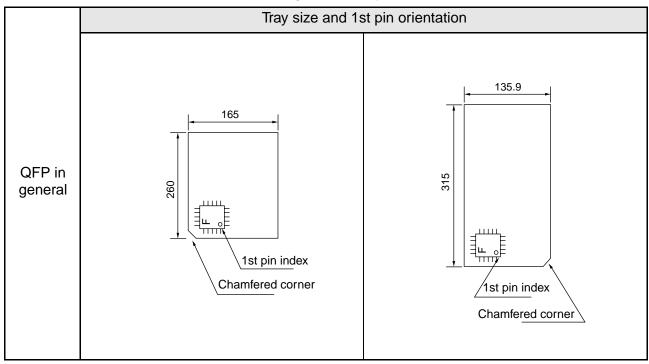


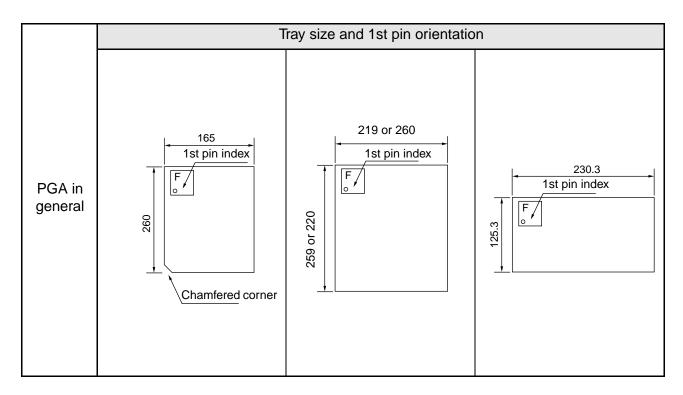
- d) Check after stacking the trays
  - •Make sure that there are no abnormal gaps between the stacked trays.
  - •Handle the stacked trays carefully when using packing tape or packing bands to secure the trays. (Secure the trays only after making sure that there are no gaps.)

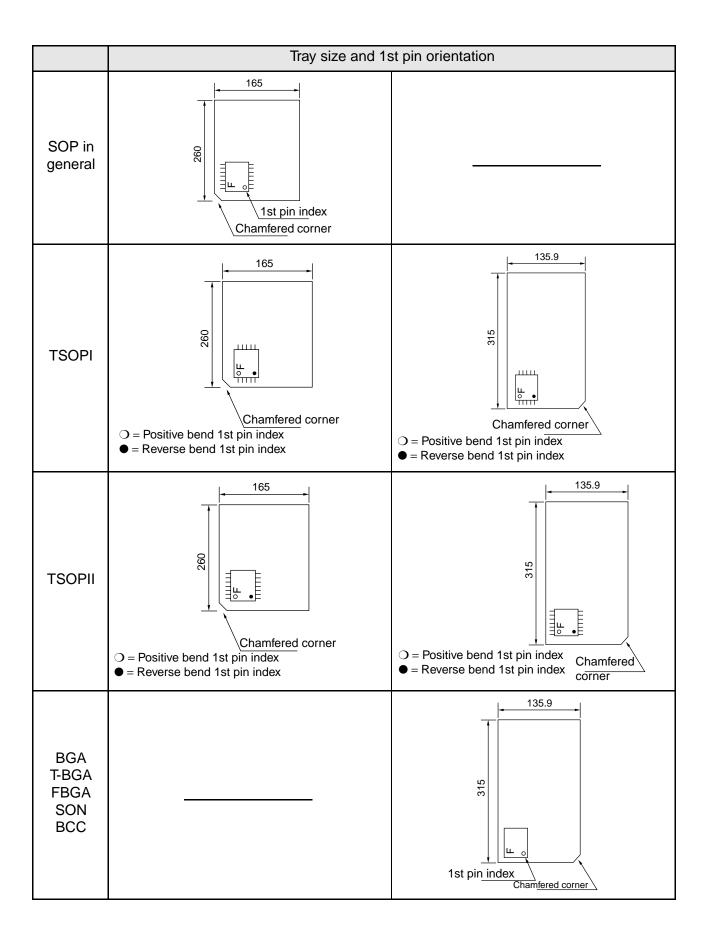


- (7) When removing stoppers from containers, be careful not to let the stopper jump out of the container.
- (8) A container, coated with antistatic, may be sticky.
- (9) Heat resistant tray has a label entitled "HT" or " heat proof temperature" (e.g. 125 °C MAX). The heat resistant tray allows 125 °C heat being applied for 24 hours. It may cause warp on heating or cooling. To minimize the warp, it is be heated or cooled on a flat surface. Avoid the abrupt cooling.

6.1.3 1st pin orientation of a package on the tray



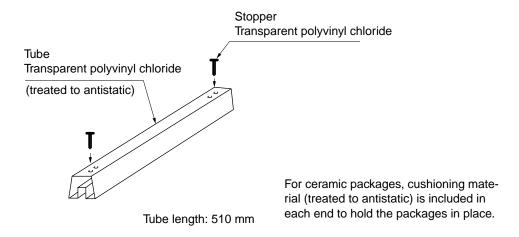




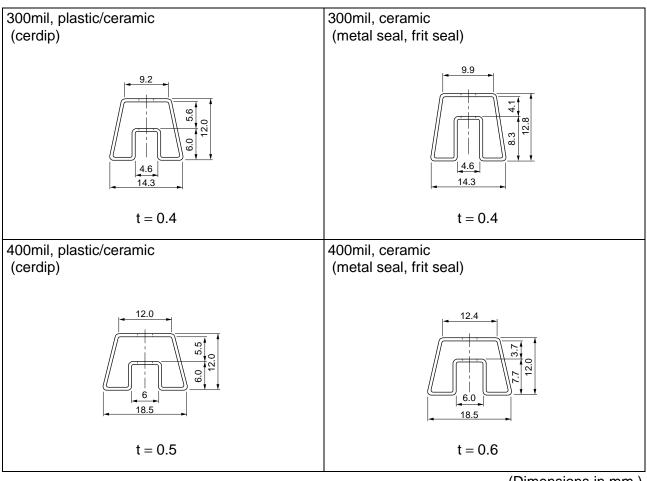
#### **Tube Dimensions** 6.2

### 6.2.1 DIP

Tube/stopper shape



#### **Tube cross-sections**

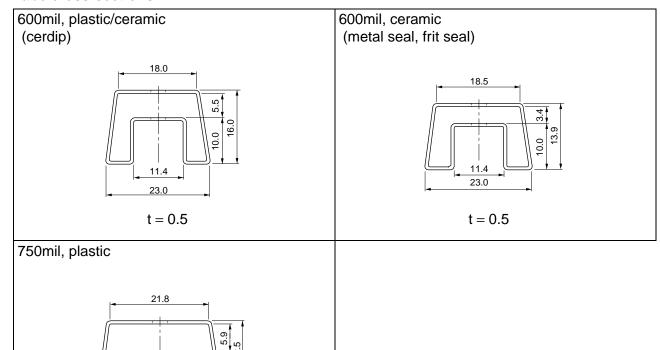


(Dimensions in mm.)

16.0 26.0

t = 0.6

### **Tube cross-sections**



(Dimensions in mm.)

## Maximum quantity per tube (DIP)

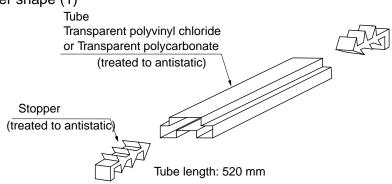
Package form	Package code	Maximum quantity			
i dokago lomi	1 ackage code	pcs/tube	pcs/inner box	pcs/outer box	
DIP, 300mil, plastic/ceramic	DIP-8P-M01	50	2000	8000	
(cerdip)	DIP-14P-M02	25	1000	4000	
	DIP-14C-C01	25	1000	4000	
	DIP-14C-C04	25	1000	4000	
	DIP-16P-M03	25	1000	4000	
	DIP-16P-M04	25	1000	4000	
	DIP-16P-M05	25	1000	4000	
	DIP-16C-C01	25	1000	4000	
	DIP-16C-C02	25	1000	4000	
	DIP-16C-C04	25	1000	4000	
	DIP-18P-M02	22	880	3520	
	DIP-18P-M03	22	880	3520	
	DIP-18P-M04	22	880	3520	
	DIP-18C-C01	21	840	3360	
	DIP-20P-M01	20	800	3200	
	DIP-20P-M02	20	800	3200	
	DIP-20P-M03	20	800	3200	
	DIP-20C-C01	20	800	3200	
	DIP-22P-M04	18	720	2880	
	DIP-22C-C03	17	680	2720	
	DIP-24P-M03	16	640	2560	
	DIP-24P-M05	22	880	3520	
	DIP-24C-C04	15	600	2400	
	DIP-28P-M04	13	520	2080	
	DIP-32P-M02	12	480	1920	
DIP, 300mil, ceramic (metal seal, frit seal)	DIP-20C-A01	20	800	3200	
DIP, 400mil, plastic/ceramic	DIP-20C-C02	20	600	2400	
(cerdip)	DIP-22P-M03	18	540	2160	
	DIP-22C-C02	17	510	2040	
	DIP-24P-M04	16	480	1920	
	DIP-28P-M03	18	540	2160	
	DIP-28P-M06	13	390	1560	
DIP, 400mil, ceramic	DIP-24C-A05	16	480	1920	
(metal seal, frit seal)	DIP-28C-A10	13	390	1560	
	DIP-32C-A02	12	360	1440	
DIP, 600mil, plastic/ceramic	DIP-24P-M02	16	320	1280	
(cerdip)	DIP-24C-C01	15	300	1200	

## Maximum quantity per tube (DIP)

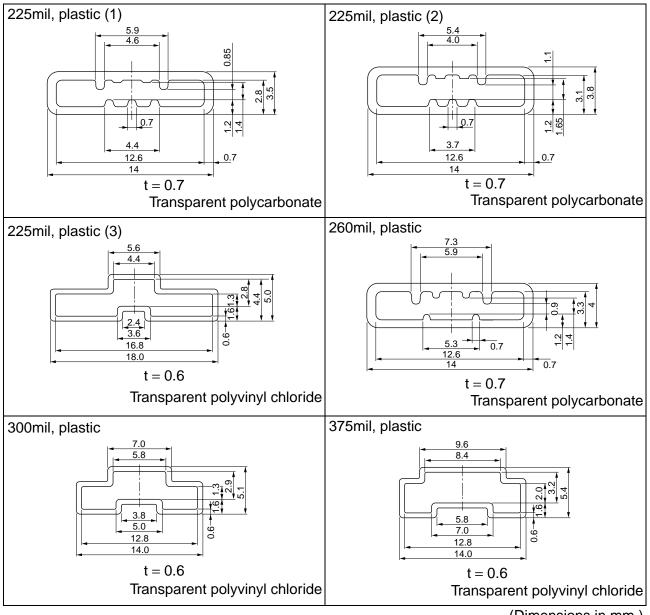
Package form	Package code	Maximum quantity			
l ackage form		pcs/tube	pcs/inner box	pcs/outer box	
DIP, 600mil, plastic/ceramic	DIP-24C-C03	15	300	1200	
(cerdip)	DIP-28C-C02	13	260	1040	
	DIP-32P-M01	12	240	960	
	DIP-36P-M01	10	200	800	
	DIP-40P-M02	9	180	720	
	DIP-40P-M03	12	240	960	
	DIP-42P-M02	12	240	960	
	DIP-42P-M03	9	180	720	
	DIP-48P-M01	11	220	880	
	DIP-48P-M02	8	160	640	
	DIP-52P-M01	10	200	800	
DIP, 600mil, ceramic	DIP-24C-A04	16	320	1280	
(metal seal, frit seal)	DIP-24C-A06	16	320	1280	
	DIP-24C-A07	16	320	1280	
	DIP-28C-A01	13	260	1040	
	DIP-28C-A07	13	260	1040	
	DIP-40C-A04	9	180	720	
	DIP-40C-A05	9	180	720	
	DIP-42C-A01	9	180	720	
	DIP-42C-A03	9	180	720	
	DIP-48C-A01	8	160	640	
DIP, 750mil, plastic	DIP-64P-M01	8	160	640	

#### 6.2.2 SOP and SSOP

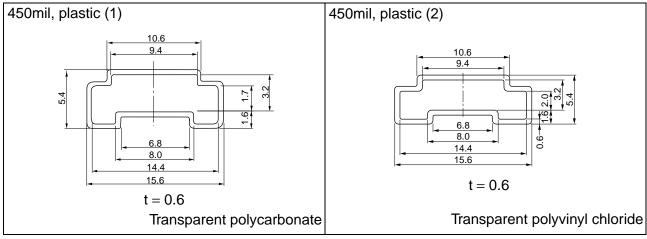
Tube/stopper shape (1)



### **Tube cross-sections**

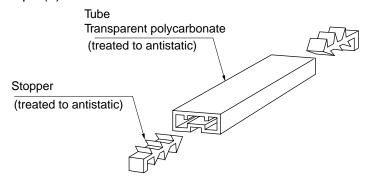


(Dimensions in mm.)



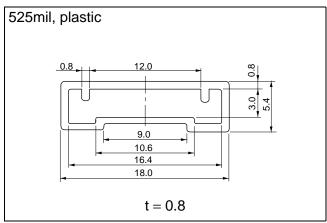
(Dimensions in mm.)

### Tube/stopper shape (2)



Tube length: 510 mm

### **Tube cross-sections**



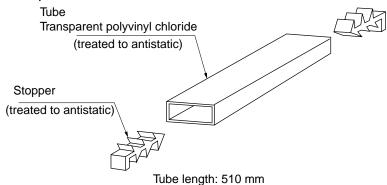
(Dimensions in mm.)

## Maximum quantity per tube (SOP and SSOP)

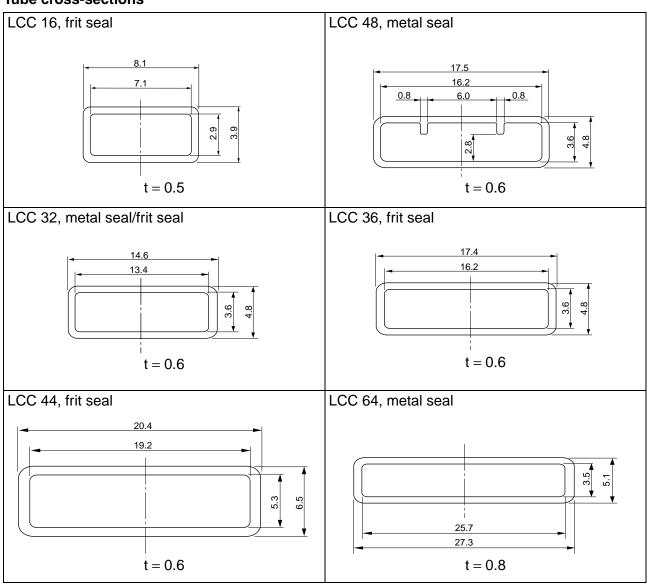
Package form	Package code	Maximum quantity			
r ackage form		pcs/tube	pcs/inner box	pcs/outer box	
SOP/SSOP, 225mil, plastic (1)	FPT-8P-M03	130	10400	41600	
	FPT-16P-M05	95	7600	30400	
	FPT-20P-M03	75	6000	24000	
SOP/SSOP, 225mil, plastic (2)	FPT-8P-M02	95	7600	30400	
SOP/SSOP, 225mil, plastic (3)	FPT-14P-M03	50	4000	16000	
	FPT-16P-M04	48	3840	15360	
SOP/SSOP, 260mil, plastic	FPT-24P-M03	62	4960	19840	
	FPT-30P-M02	50	4000	16000	
SOP/SSOP, 300mil, plastic	FPT-8P-M01	80	6400	25600	
	FPT-14P-M04	50	4000	16000	
	FPT-16P-M06	50	4000	16000	
	FPT-16C-C02	50	4000	16000	
	FPT-20P-M01	40	3200	12800	
	FPT-24P-M01	33	2640	10560	
SOP/SSOP, 375mil, plastic	FPT-16P-M03	39	3120	12480	
	FPT-20P-M02	39	3120	12480	
	FPT-20P-M05	39	3120	12480	
	FPT-24P-M02	33	2640	10560	
	FPT-28P-M01	28	2240	8960	
	FPT-34P-M01	28	2240	8960	
	FPT-34P-M02	28	2240	8960	
SOP/SSOP, 450mil, plastic (1)	FPT-32P-M02	24	1920	7680	
	FPT-40P-M04	28	2240	8960	
	FPT-40P-M05	28	2240	8960	
SOP/SSOP, 450mil, plastic (2)	FPT-40P-M01	28	2240	8960	
SOP/SSOP, 525mil, plastic	FPT-32P-M03	23	1150	4600	

6.2.3 LCC

Tube/stopper shape



### **Tube cross-sections**

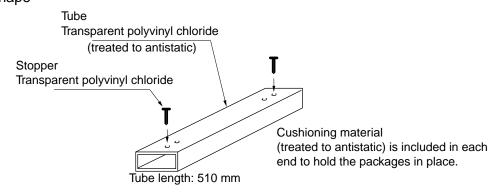


(Dimensions in mm.)

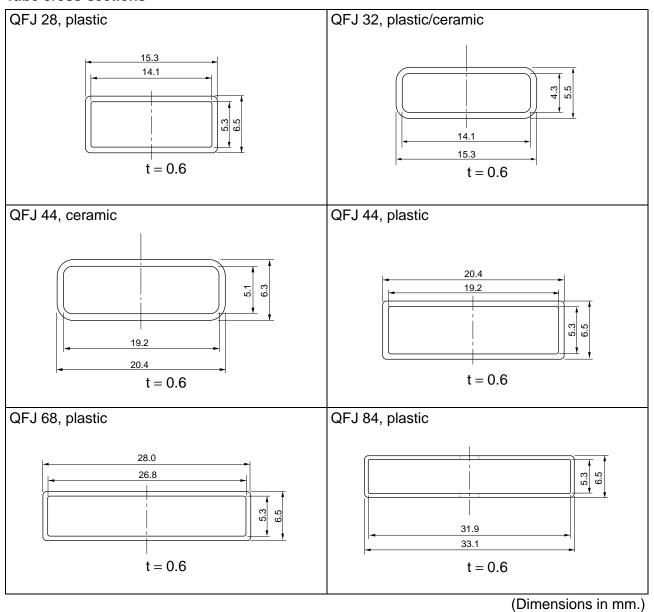
## Maximum quantity per tube (LCC)

Package form	Package code	Maximum quantity			
1 ackage form		pcs/tube	pcs/inner box	pcs/outer box	
LCC16, frit seal	LCC-16C-F01	77	7700	30800	
LCC 48, metal seal	LCC-48C-A01	34	2040	8160	
LCC 32, metal seal/frit seal	LCC-32C-F01	35	2450	9800	
LCC 36, frit seal	LCC-36C-F01	35	2450	9800	
LCC 44, frit seal	LCC-44C-F01	28	1260	5040	
LCC 64, metal seal	LCC-64C-A01	26	832	3328	

6.2.4 QFJ Tube/stopper shape



### **Tube cross-sections**

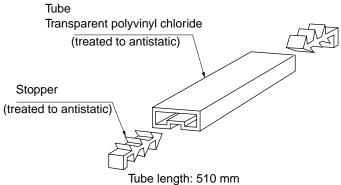


## Maximum quantity per tube (QFJ)

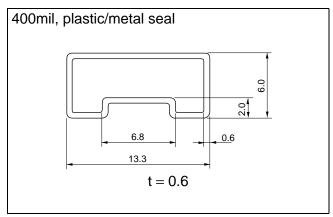
Package form	Package code	Maximum quantity			
l ackage form	1 ackage code	pcs/tube	pcs/inner box	pcs/outer box	
QFJ 28, plastic	LCC-28P-M03	36	2016	8064	
QFJ 32, plastic/ceramic	LCC-32P-M02	30	1680	6720	
	QFJ-32C-C01	30	1680	6720	
QFJ 44, ceramic	QFJ-44C-C01	25	1000	4000	
QFJ 44, plastic	LCC-44P-M02	25	1000	4000	
QFJ 68, plastic	LCC-68P-M02	17	544	2176	
QFJ 84, plastic	LCC-84P-M02	14	336	1344	

6.2.5 SOJ

Tube/stopper shape



### Tube cross-sections



(Dimensions in mm.)

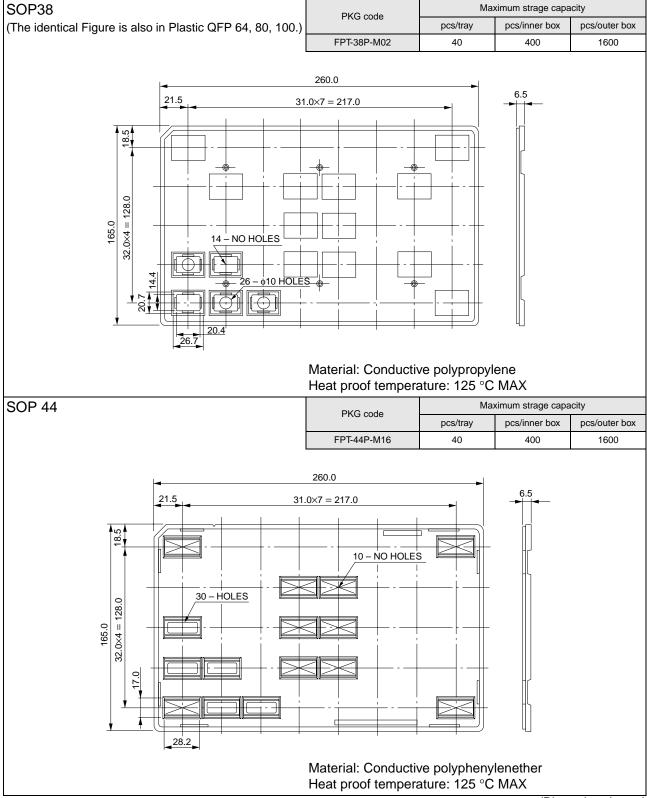
## Maximum quantity per tube (SOJ)

Package form	Package code	Maximum quantity			
		pcs/tube	pcs/inner box	pcs/outer box	
SOJ 28 (400mil),	LCC-28C-A04	26	1560	6240	
plastic/metal seal	LCC-42P-M01	17	1020	4080	

### 6.3 Tray Dimensions

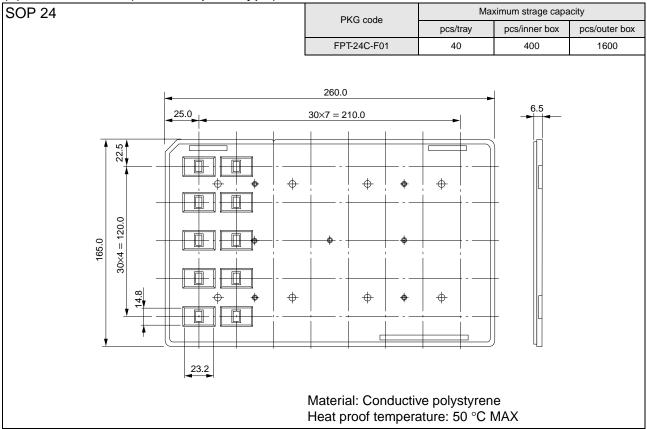
6.3.1 Plastic SOP • SOP • TSOP (heat proof type), ceramic SOP (non-heat proof type)

### (1) Plastic SOP



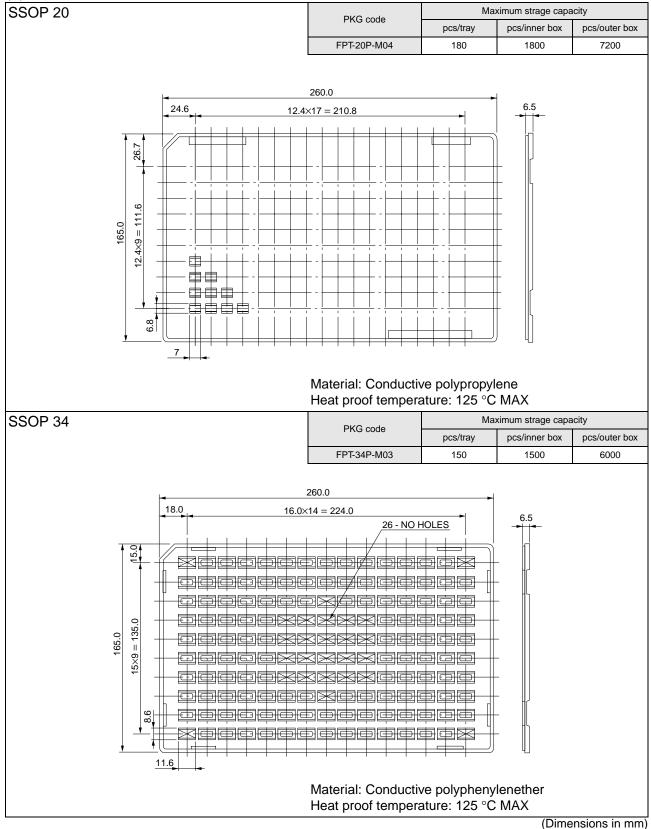
(Dimensions in mm)

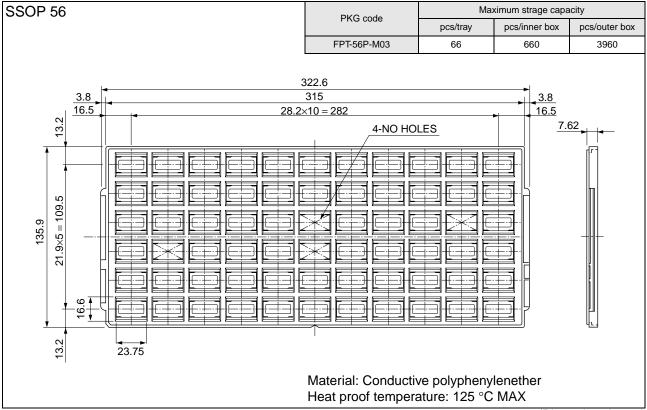
(2) Ceramic SOP (non-heat proof type)



(Dimensions in mm)

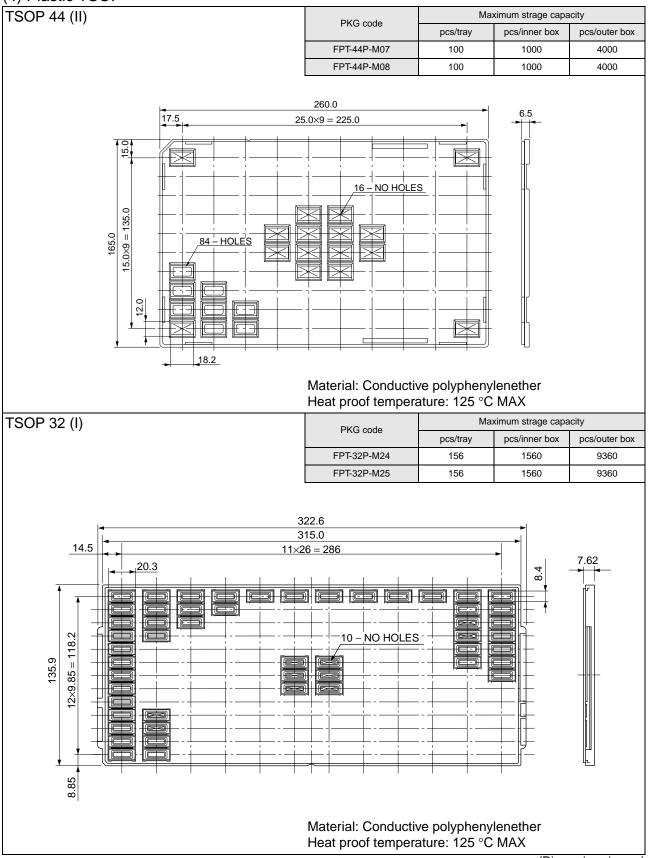
### (3) Plastic SSOP



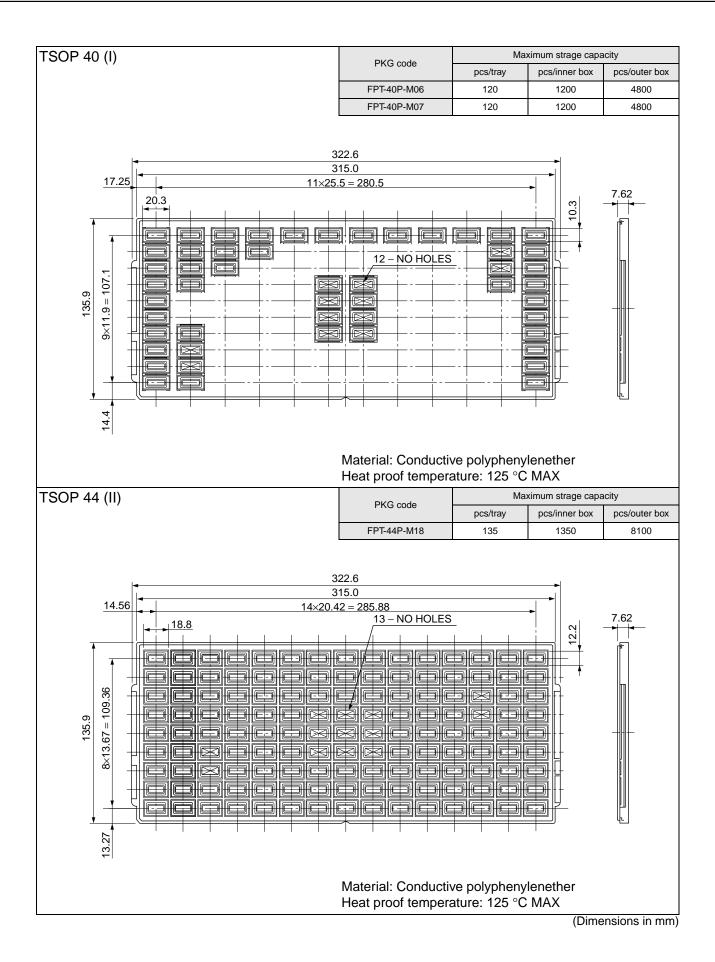


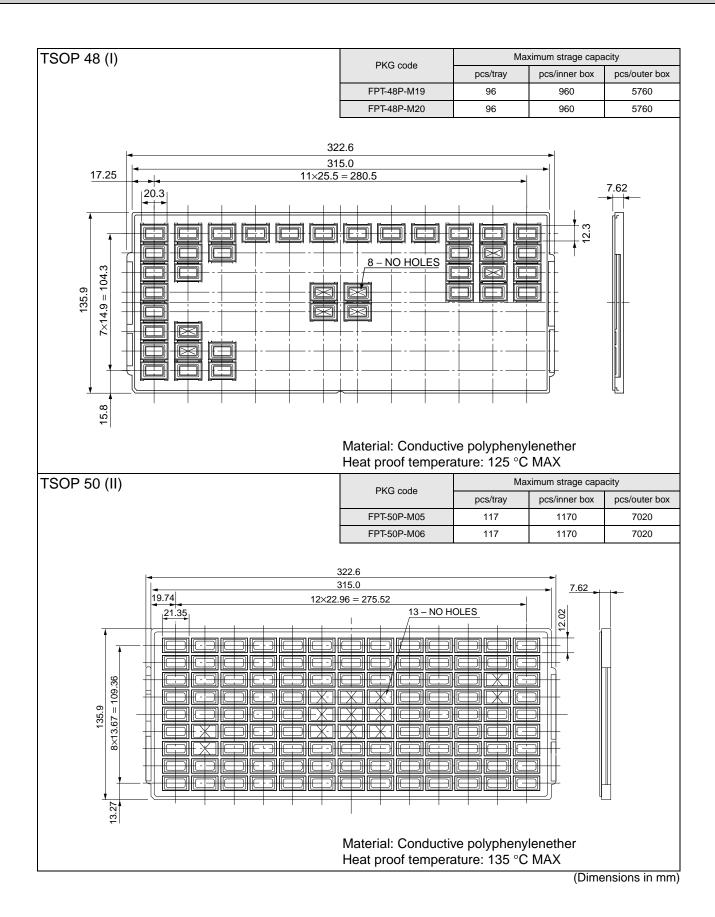
(Dimensions in mm)

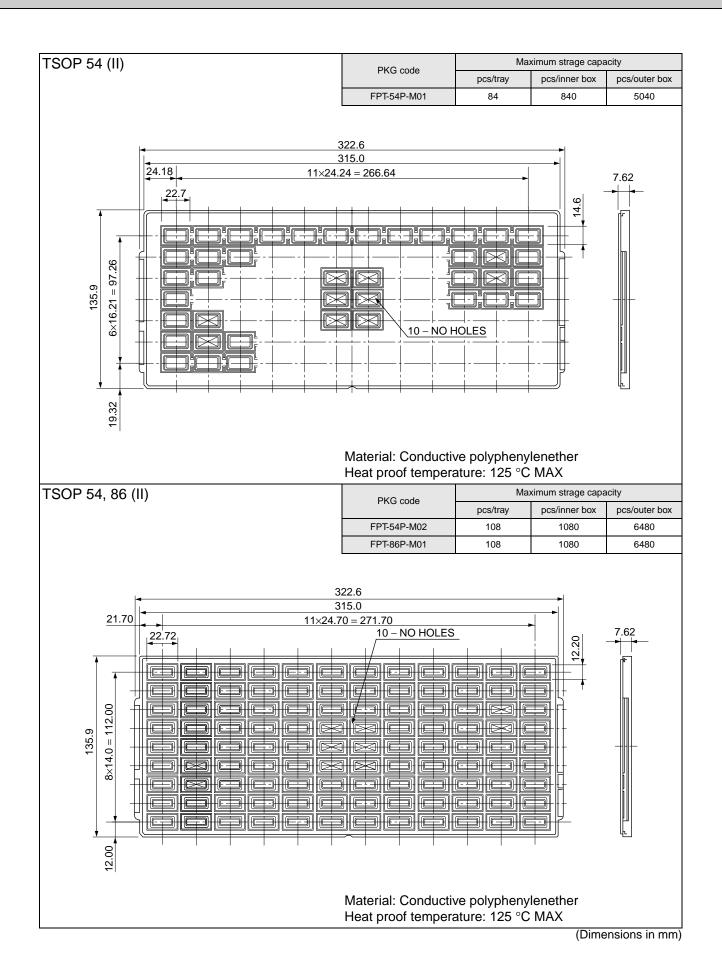


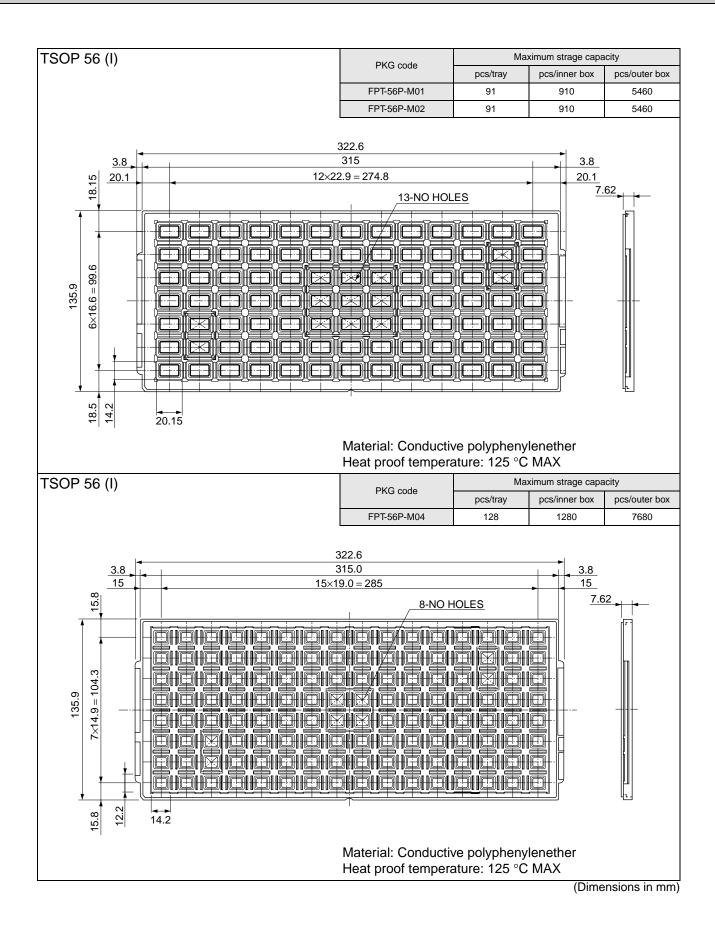


(Dimensions in mm)

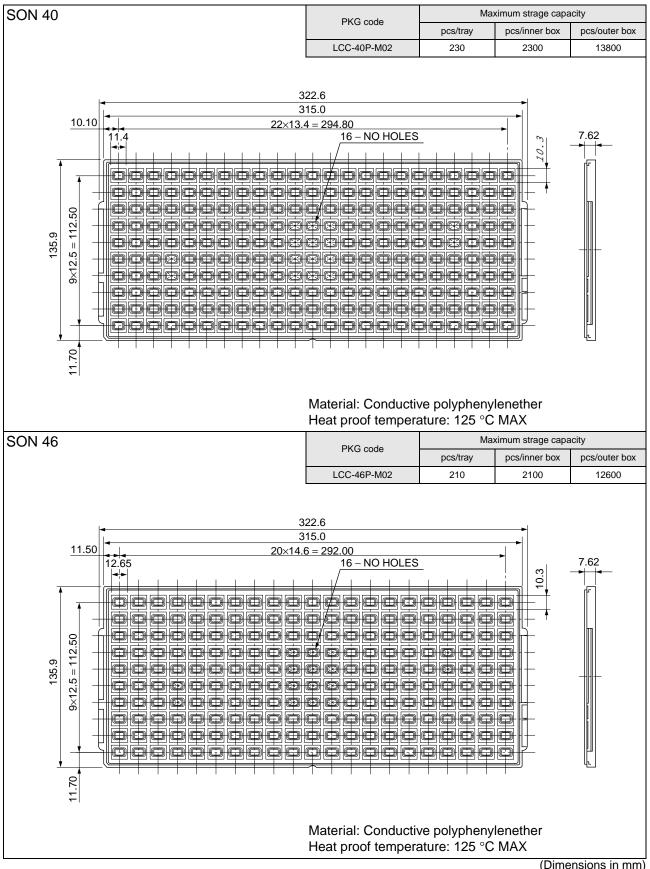




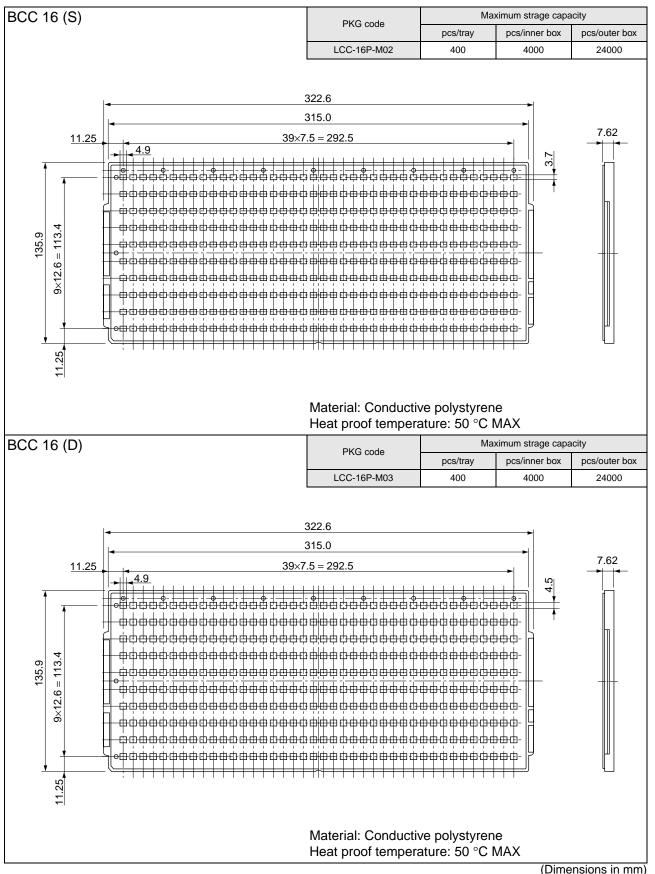


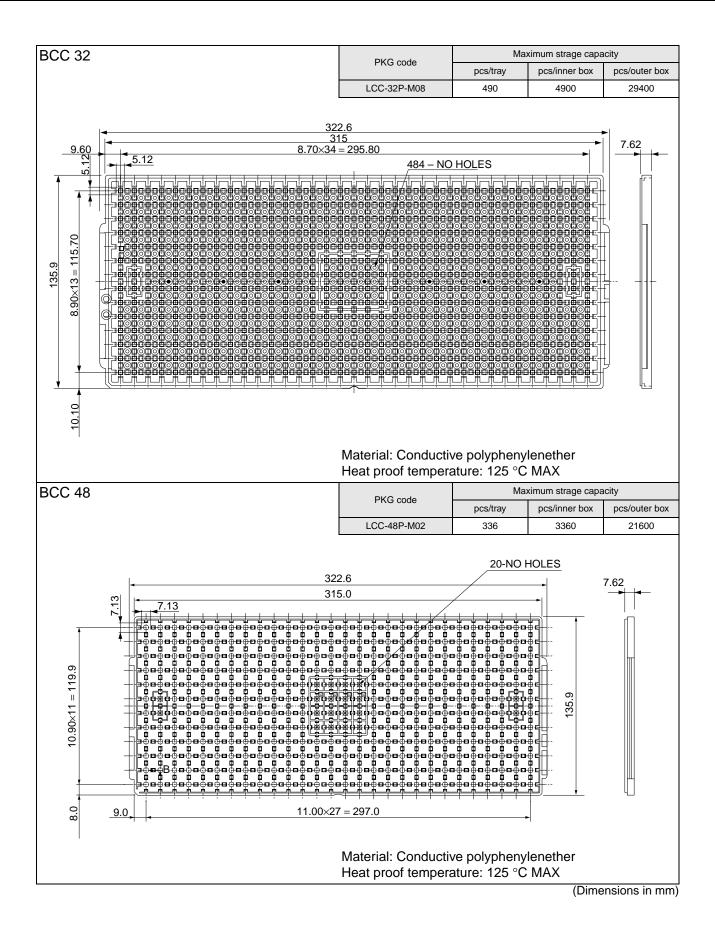


#### 6.3.2 Plastic SON (heat proof type)



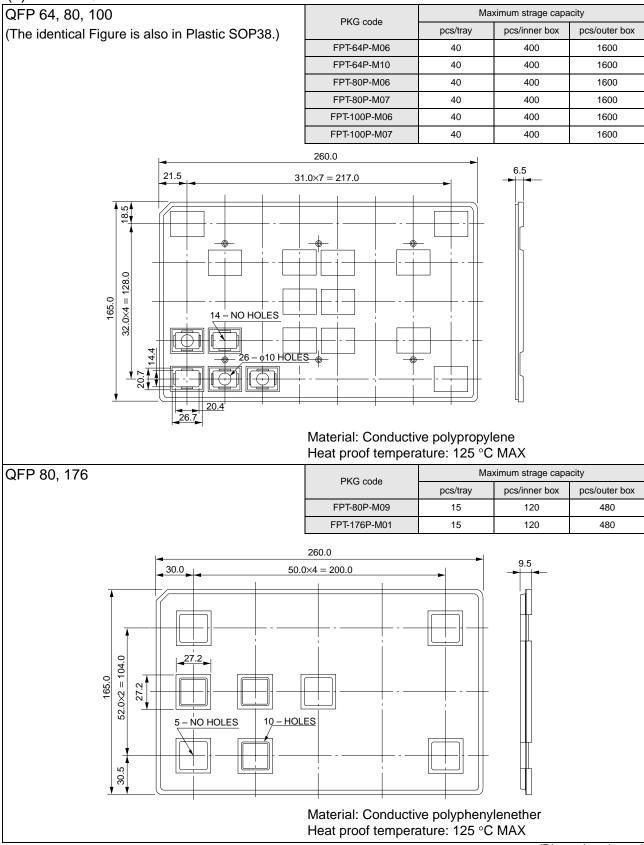
### 6.3.3 Plastic BCC (non-heat proof type)



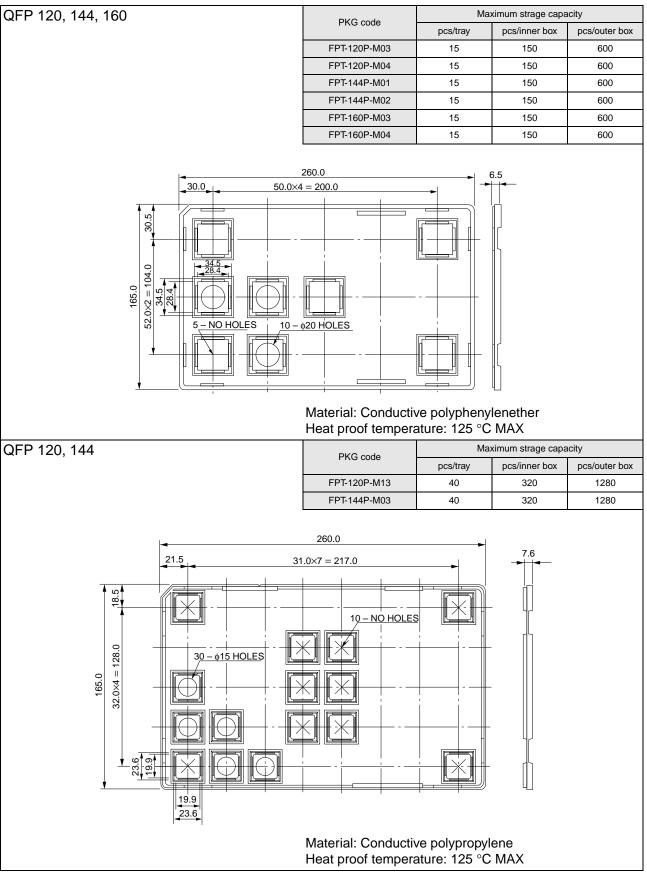


### 6.3.4 Plastic QFP • LQFP • TQFP (heat proof type)

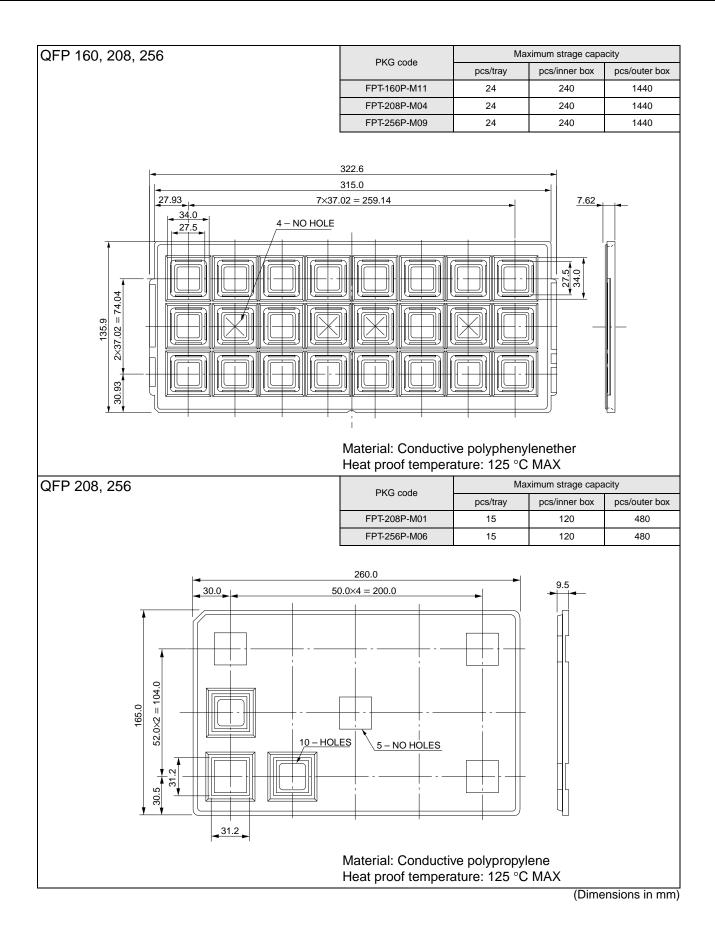
### (1) Plastic QFP

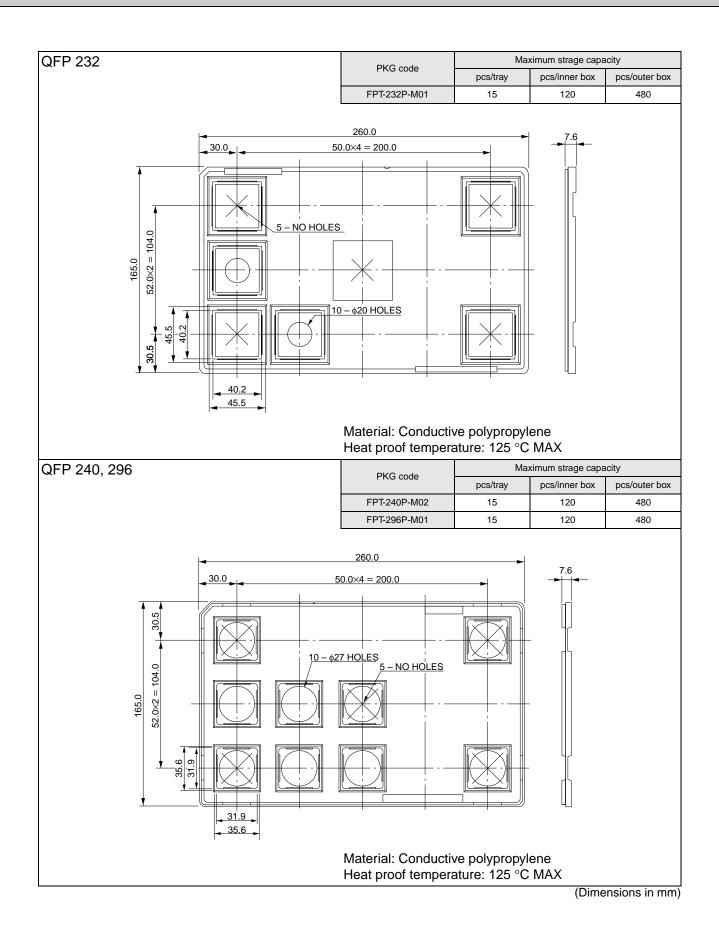


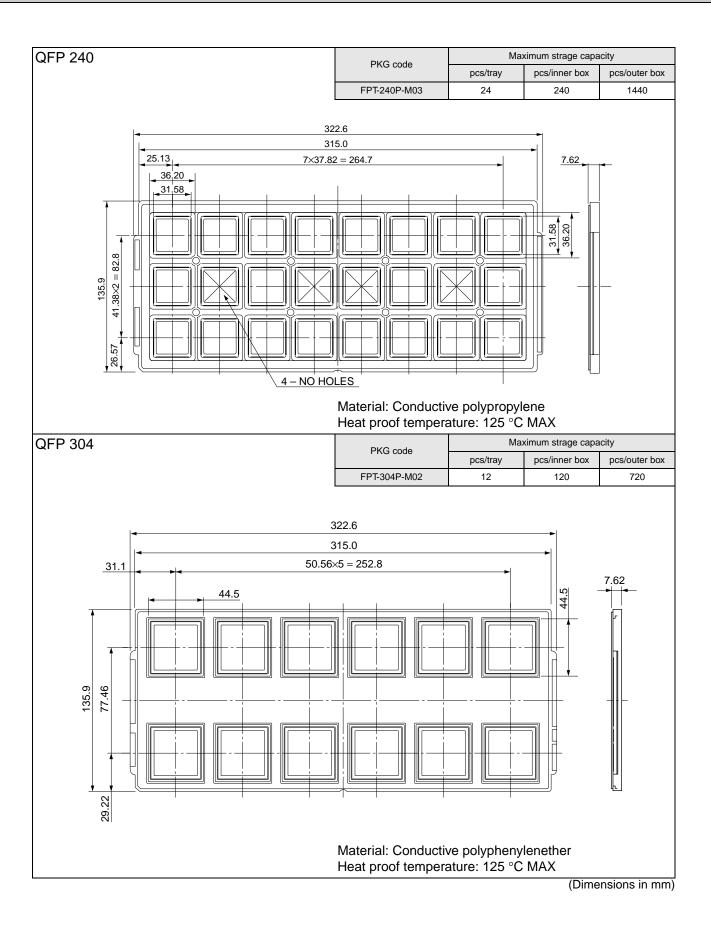
(Dimensions in mm)

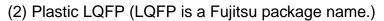


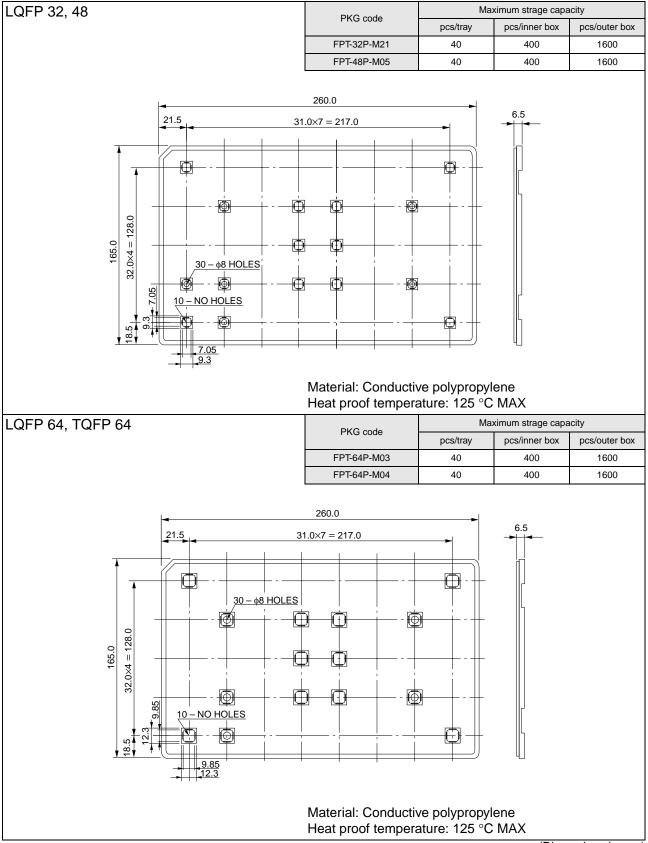
(Dimensions in mm)

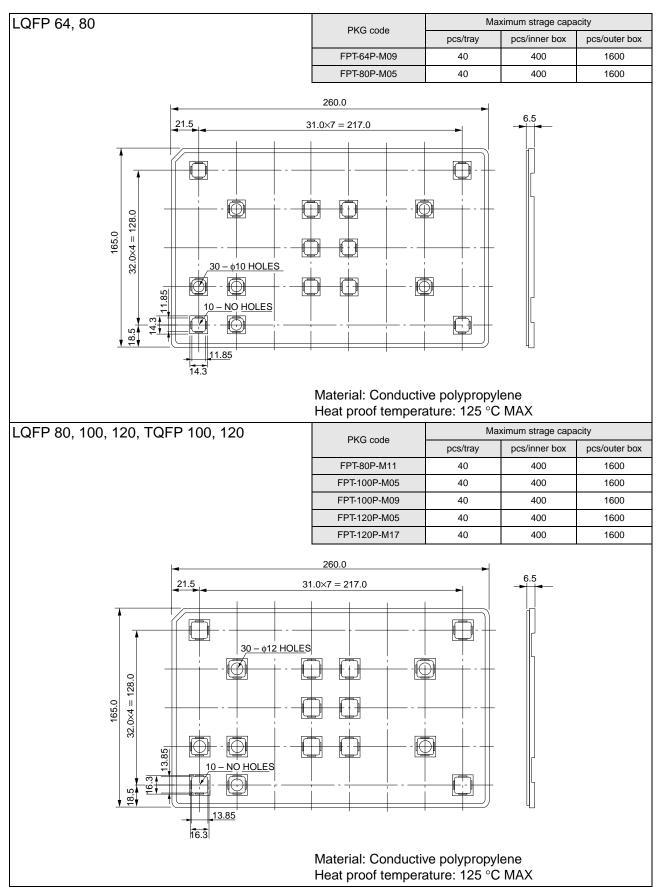


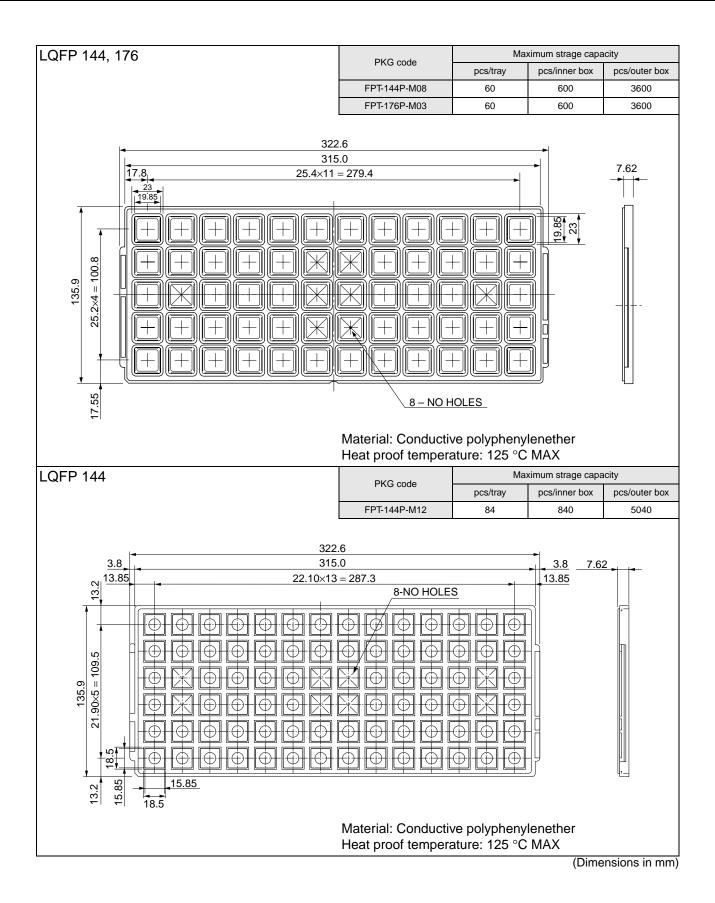


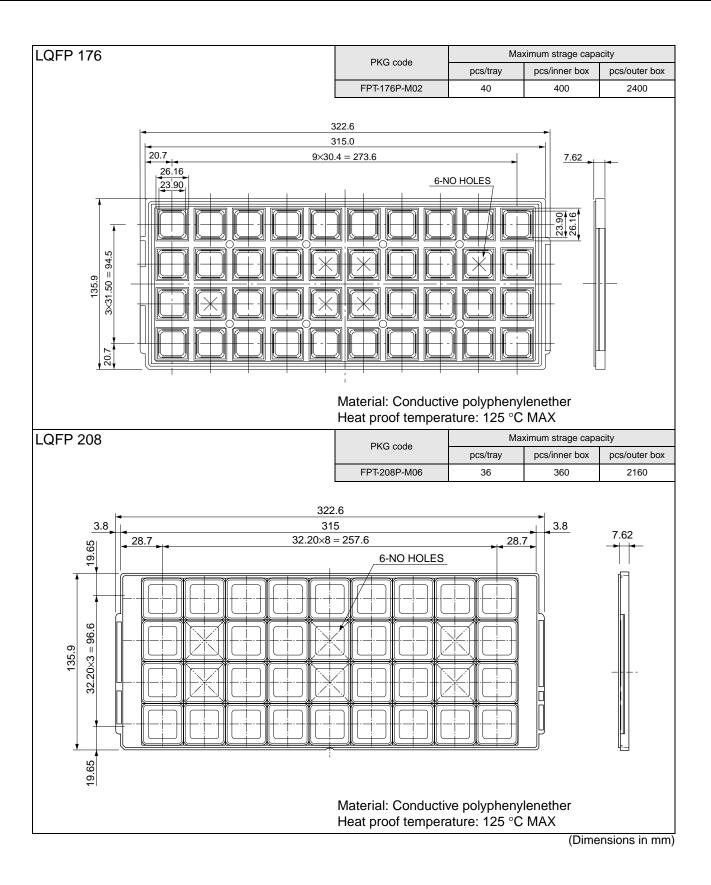










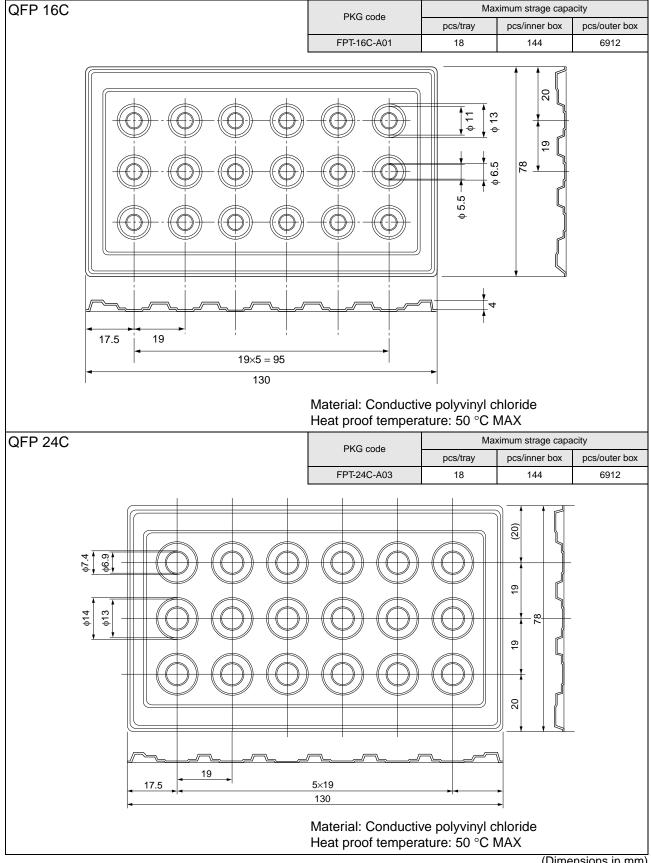


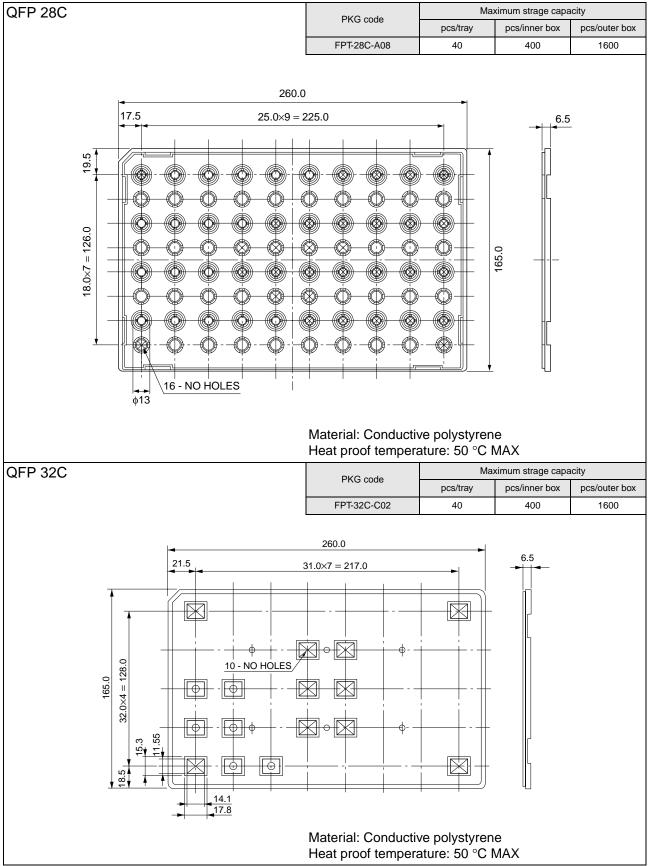
#### (3) Plastic TQFP TQFP 80, 100 Maximum strage capacity PKG code pcs/tray pcs/inner box pcs/outer box FPT-80P-M15 119 1190 7140 FPT-100P-M18 119 1190 7140 322.6 3.8 315.0 $16 \times 17.90 = 286.4$ 14.30 14.30 7.62 13.95 $6 \times 18.00 = 108.0$ <u>11.95</u> 11-NO HOLES Material: Conductive polyphenylenether Heat proof temperature: 125 °C MAX TQFP 100 Maximum strage capacity PKG code pcs/tray pcs/inner box pcs/outer box FPT-100P-M19 4320 72 720 322.6 3.8 315.0 3.8 17.8 17.80 $11 \times 25.40 = 279.4$ 15.45 7.62 $5 \times 21.00 = 105.0$ 135.9 17.75 6-NO HOLES 19.95 23.75

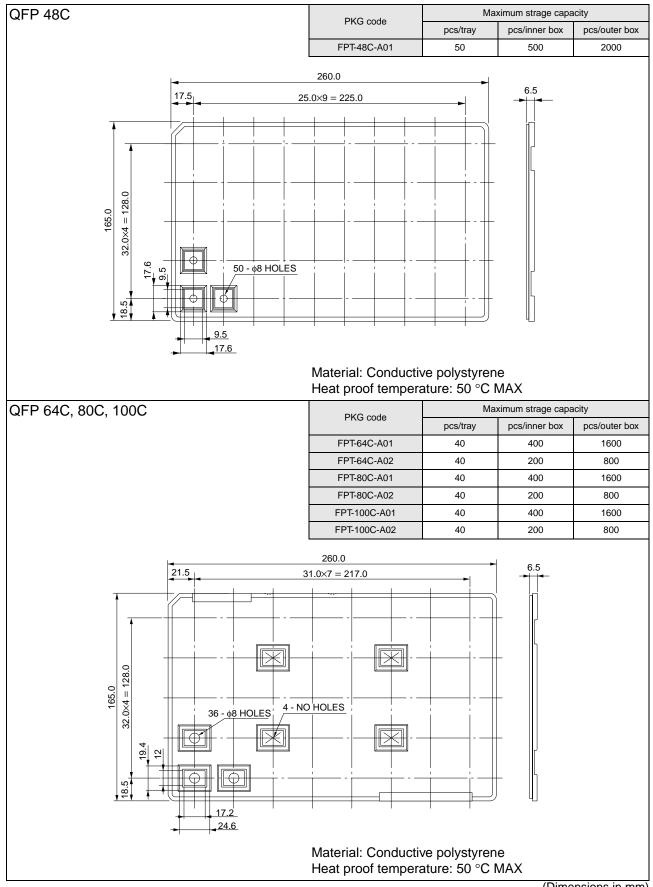
(Dimensions in mm)

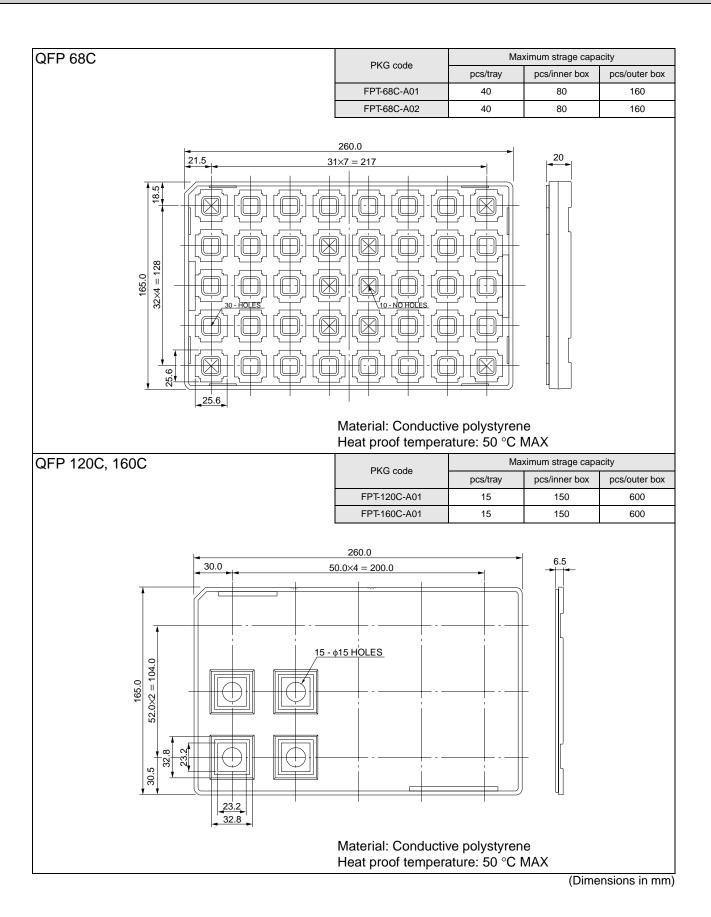
Material: Conductive polyphenylenether Heat proof temperature: 125 °C MAX

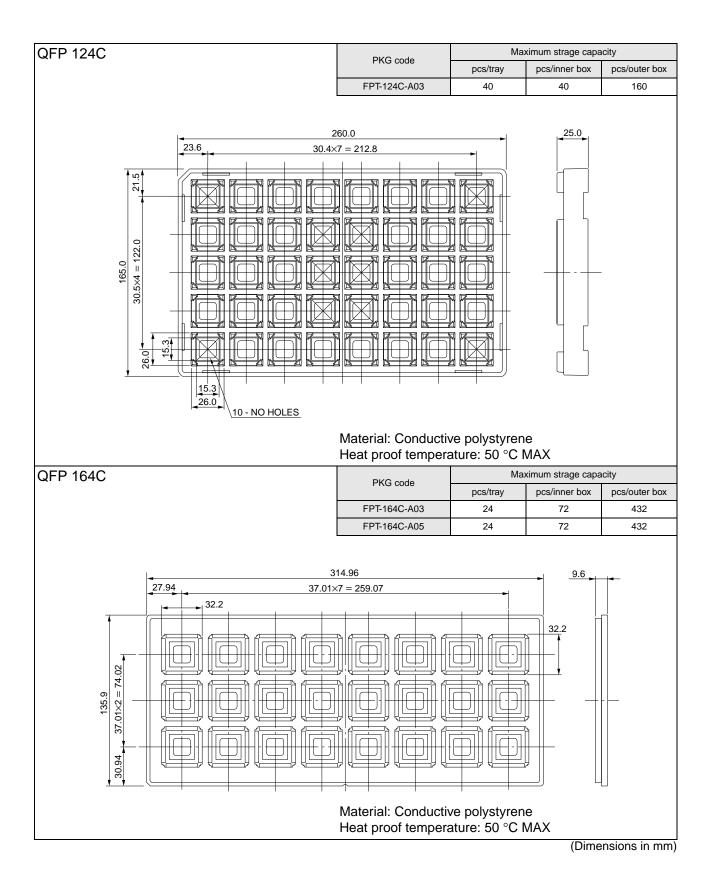
#### Ceramic QFP (non-heat proof type)

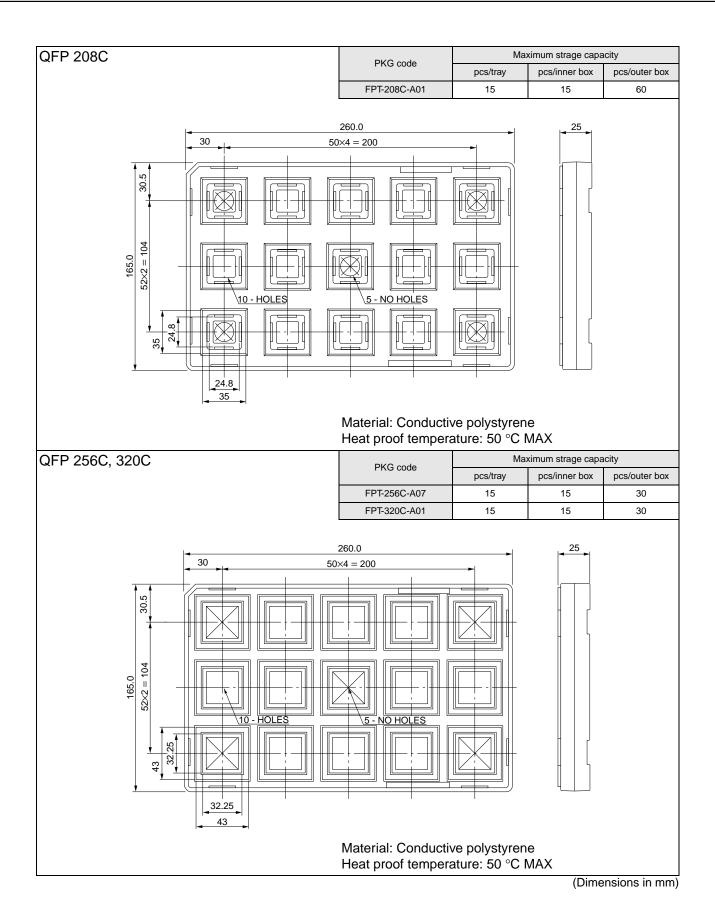


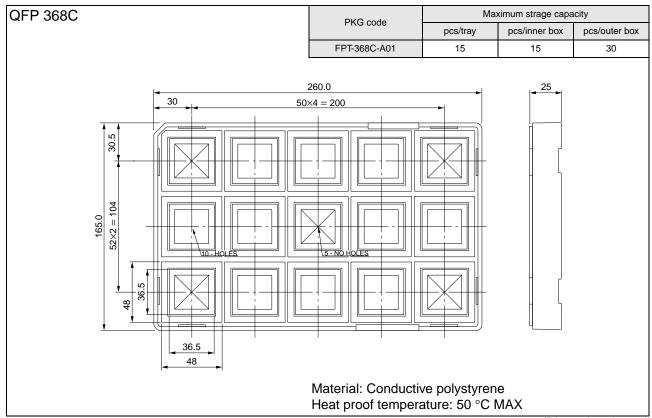




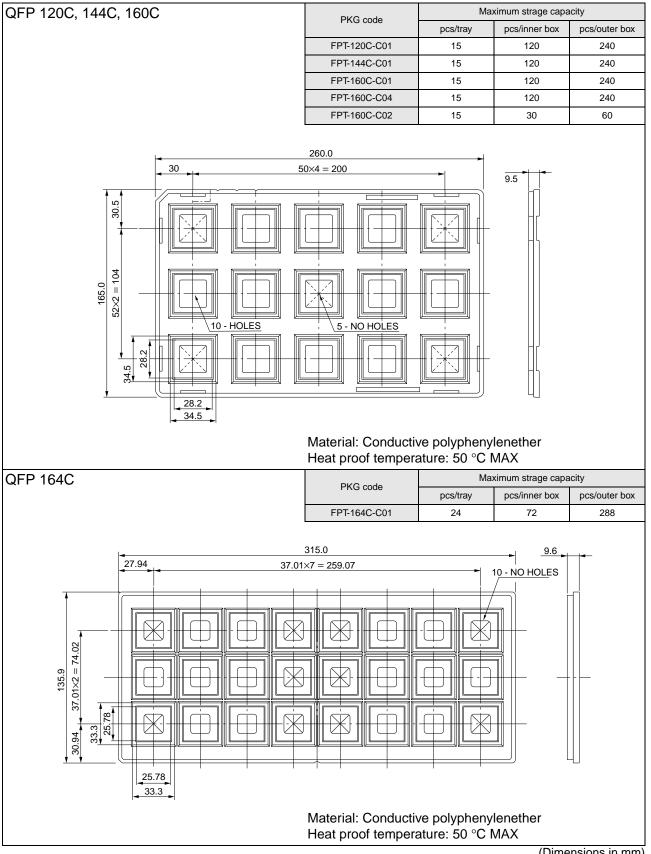


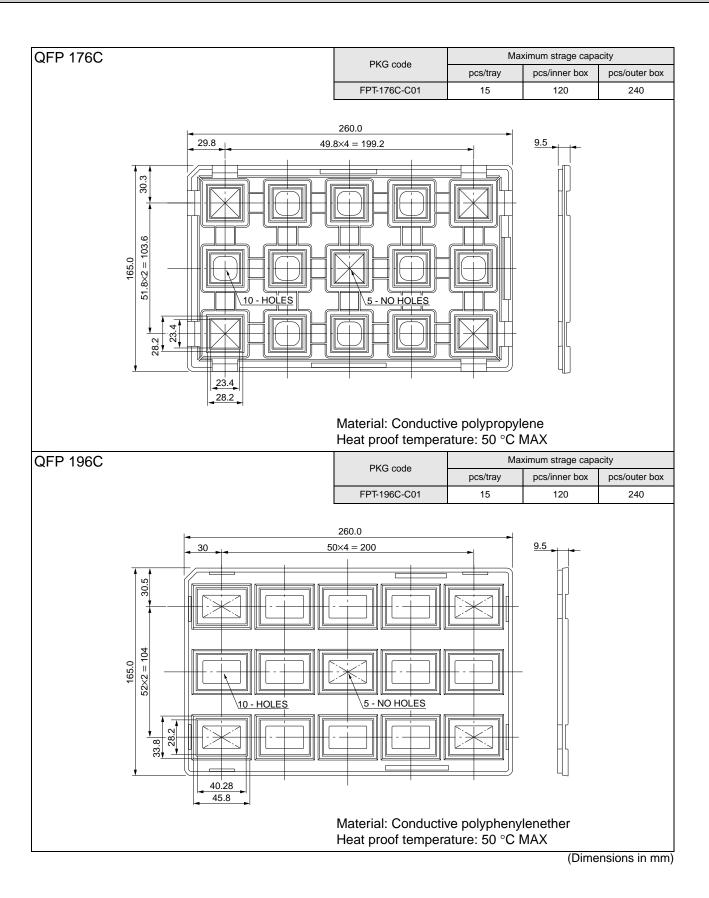


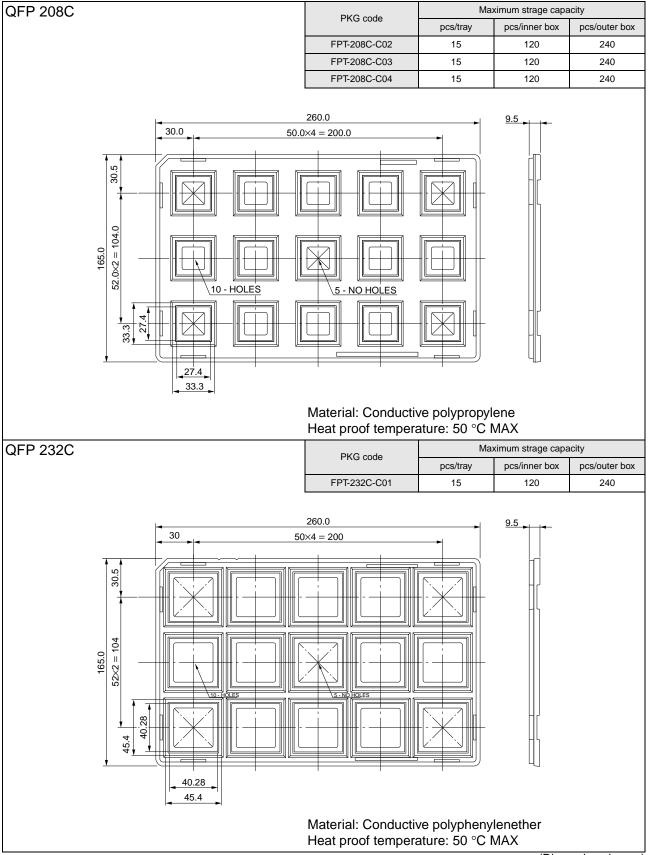


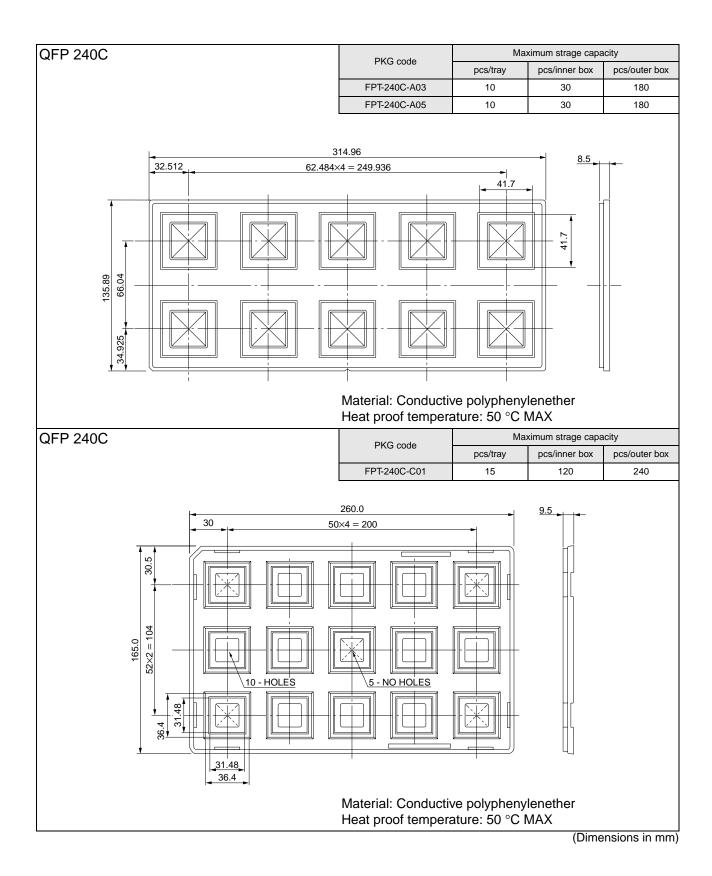


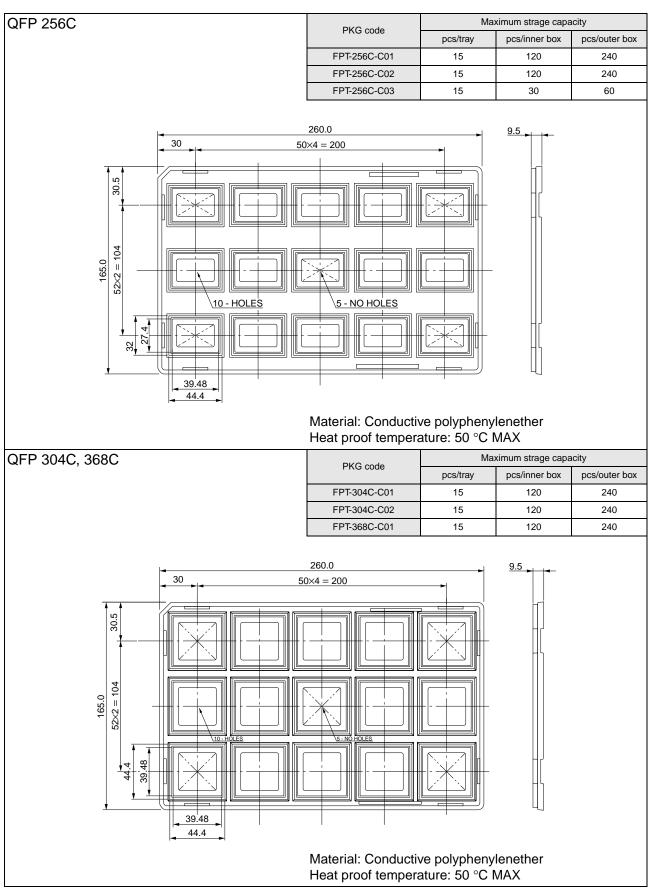
#### Ceramic QFP/Cerquad (non-heat proof type) 6.3.6

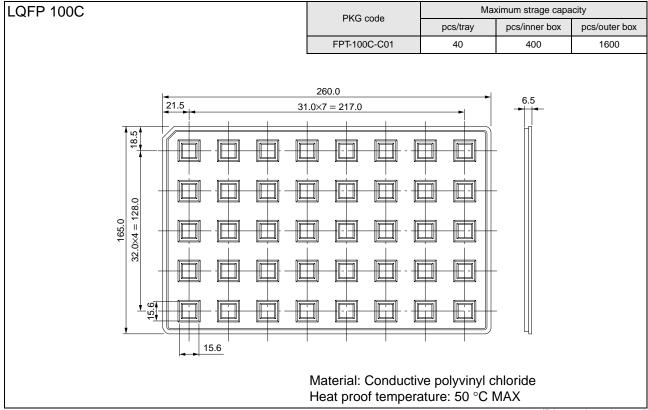




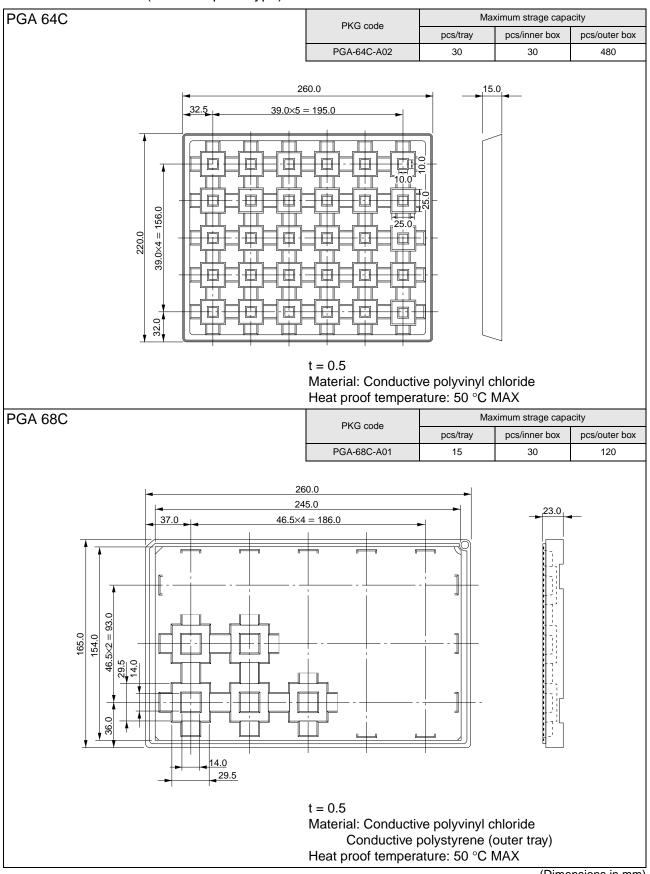


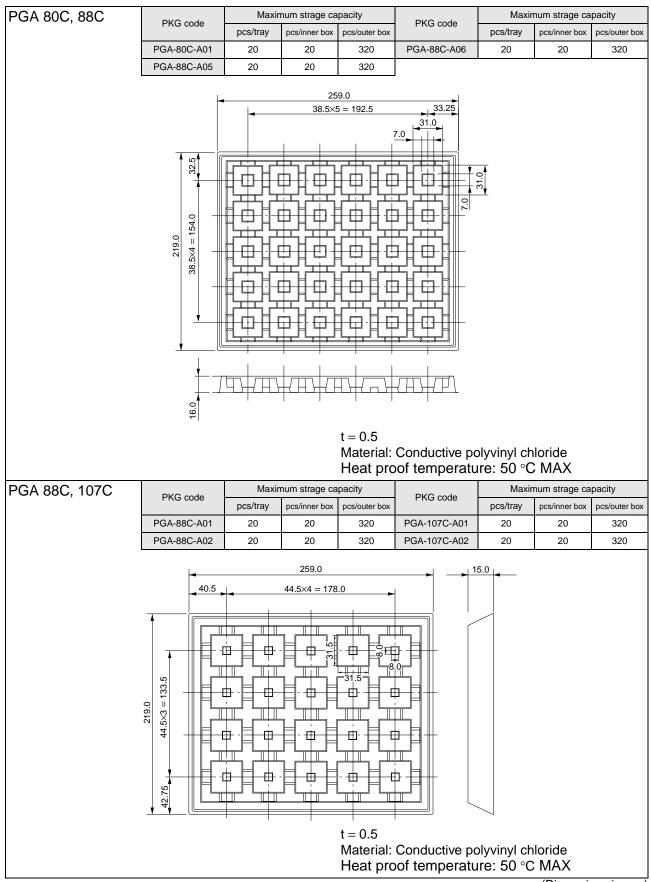


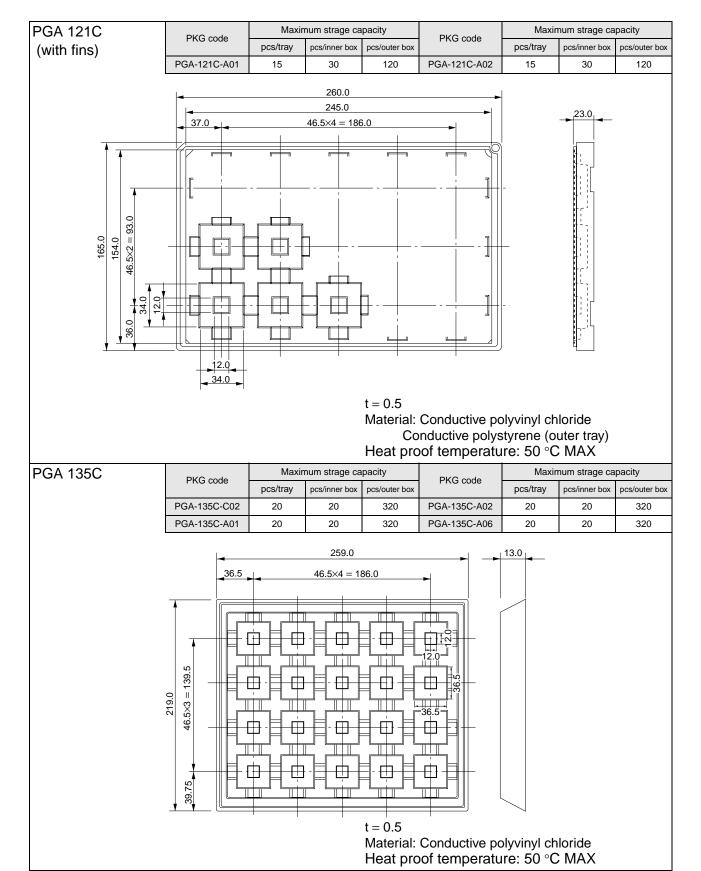


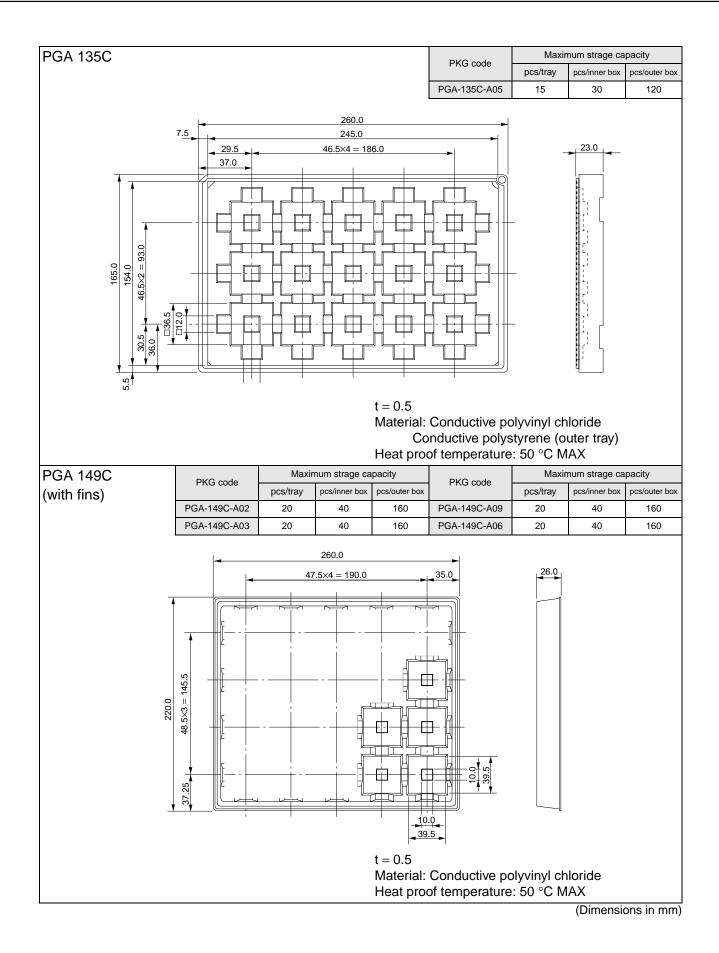


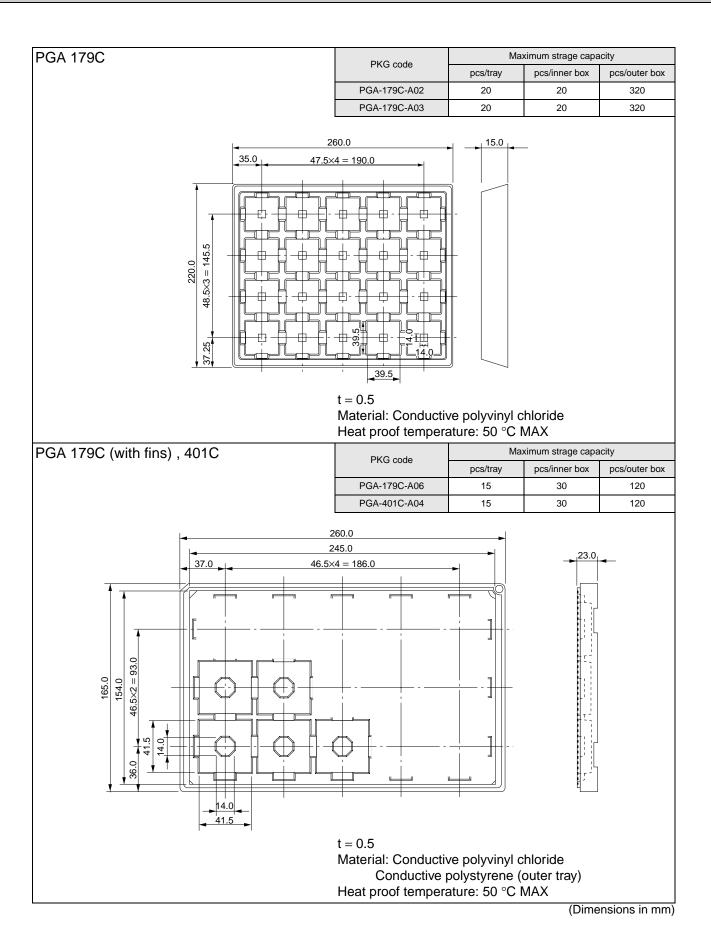
#### Ceramic PGA (non-heat proof type)

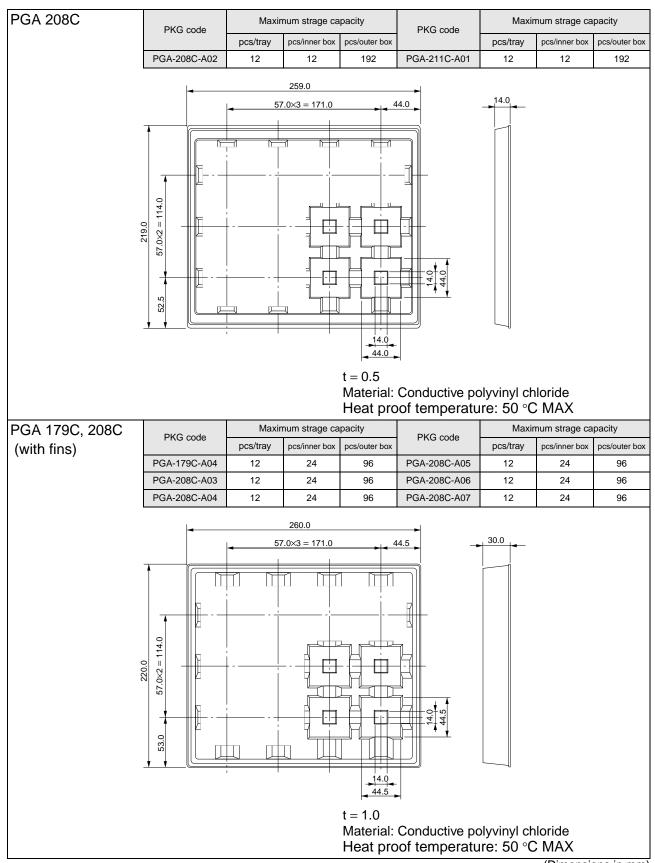


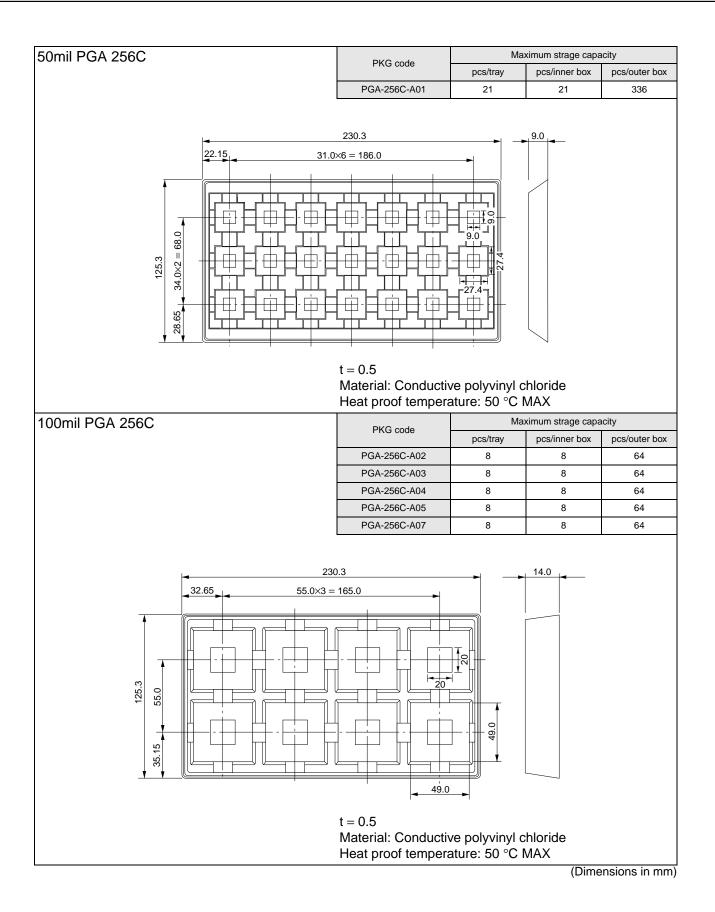


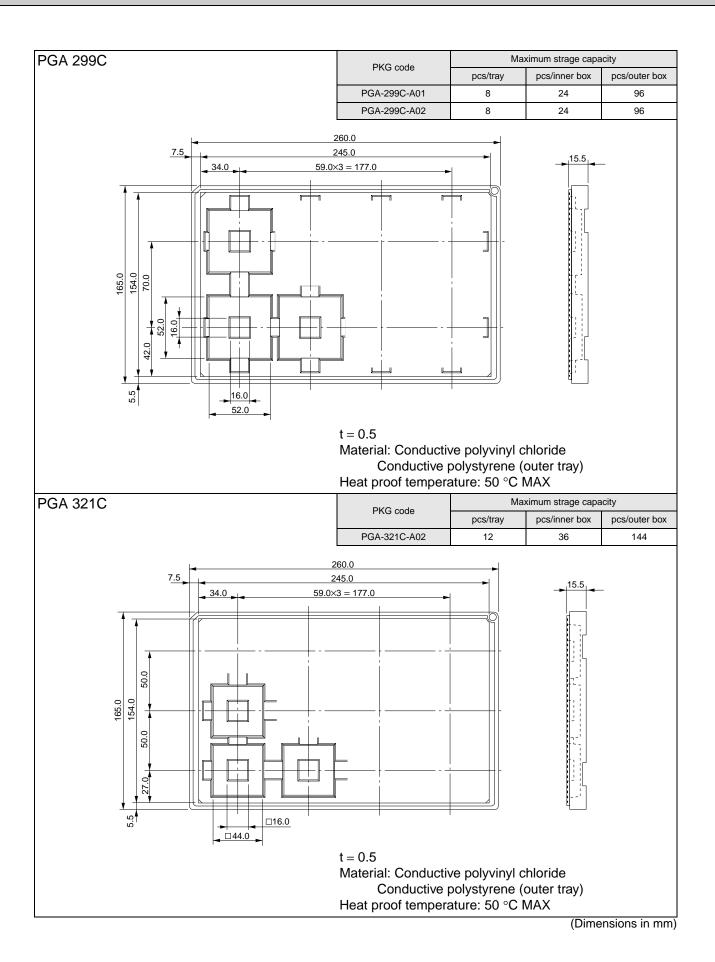


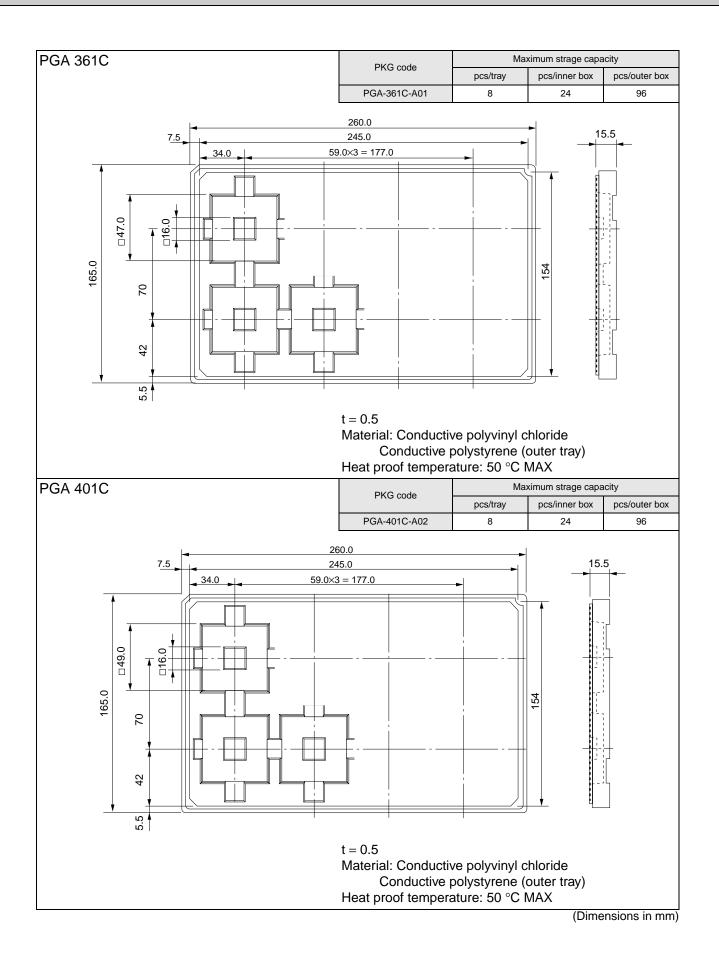


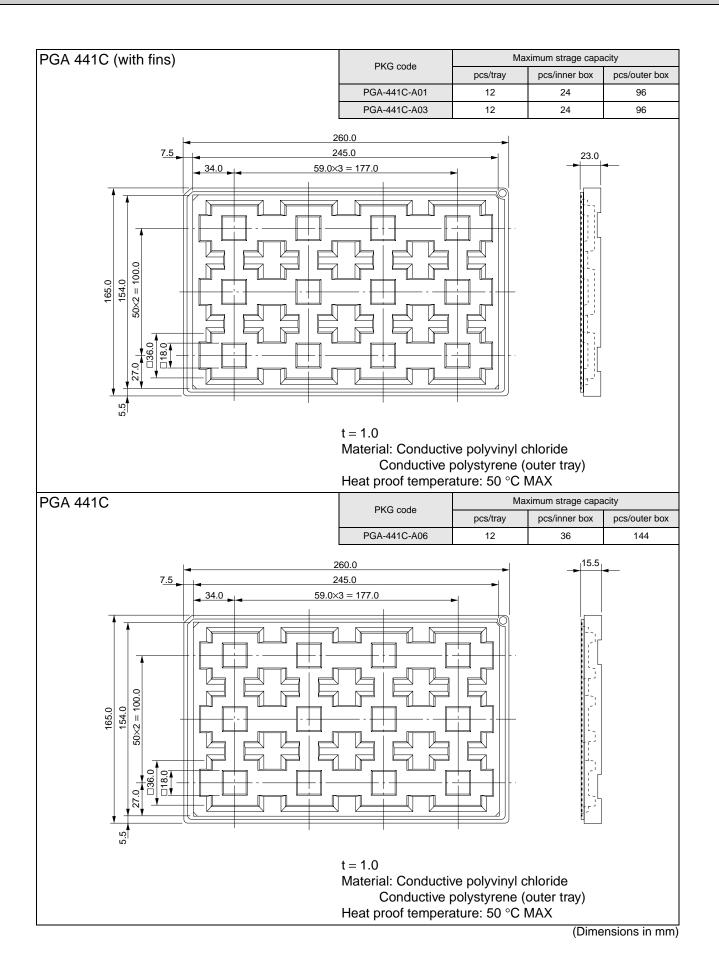




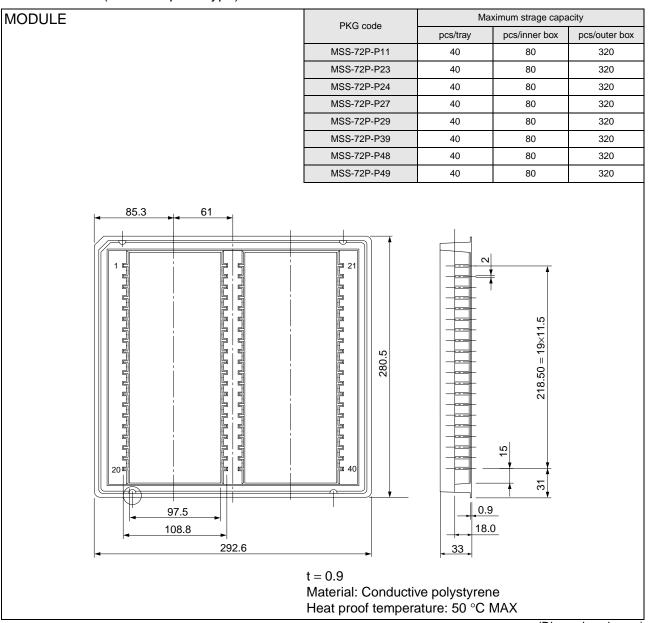


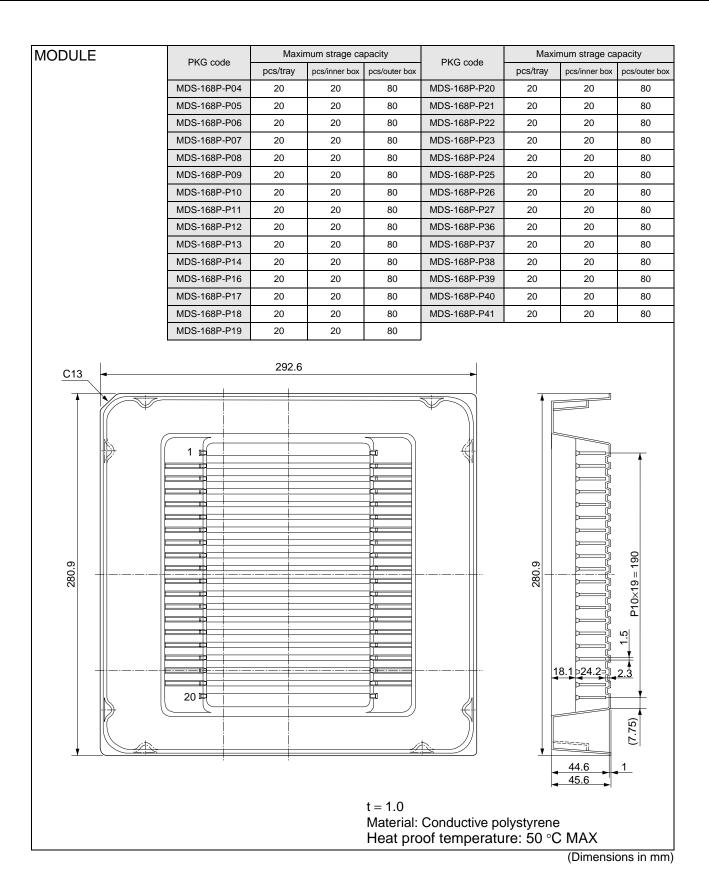




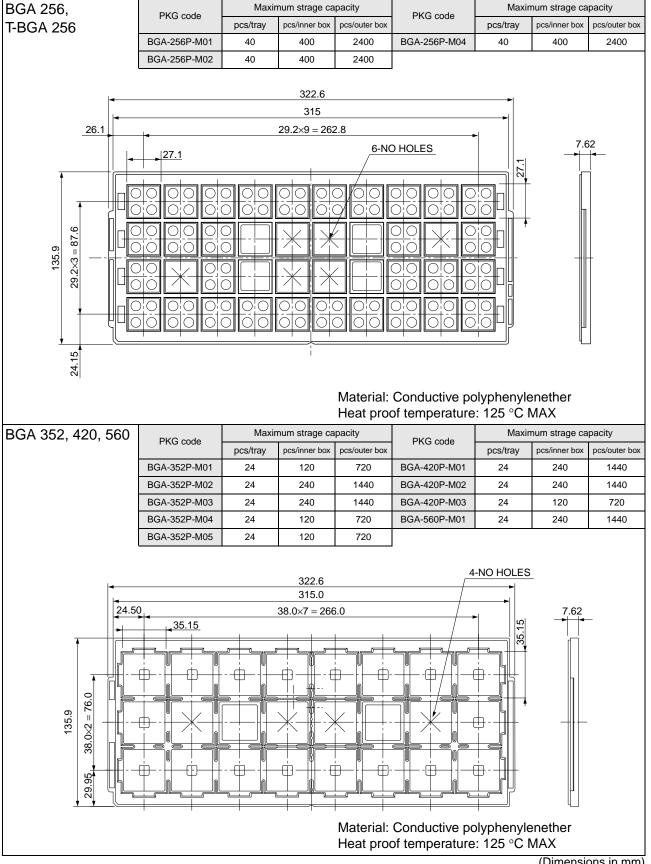


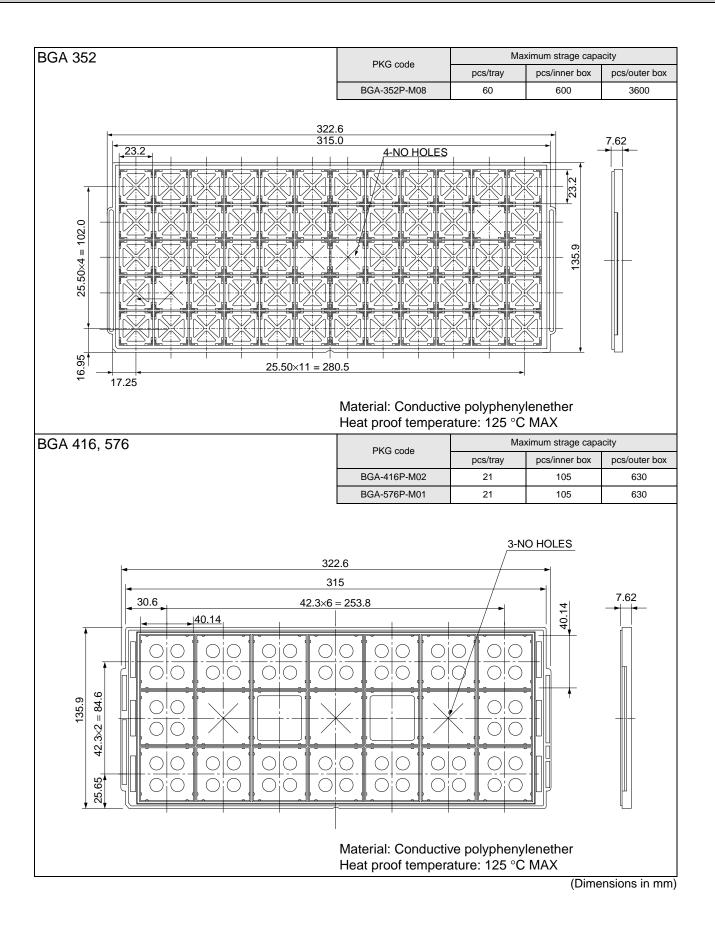
#### 6.3.8 Module (non-heat proof type)

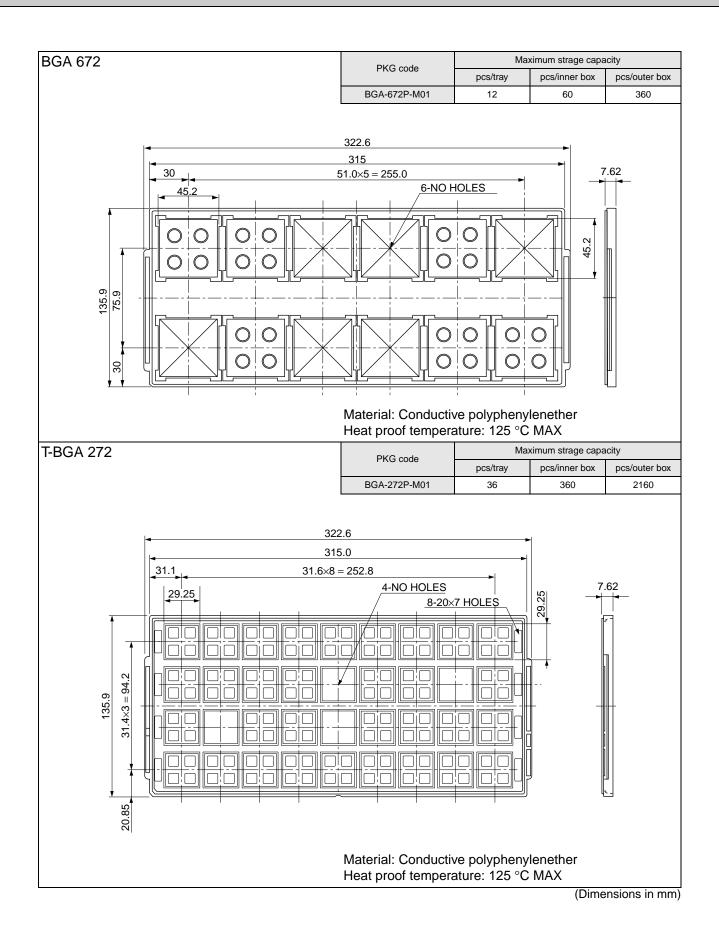


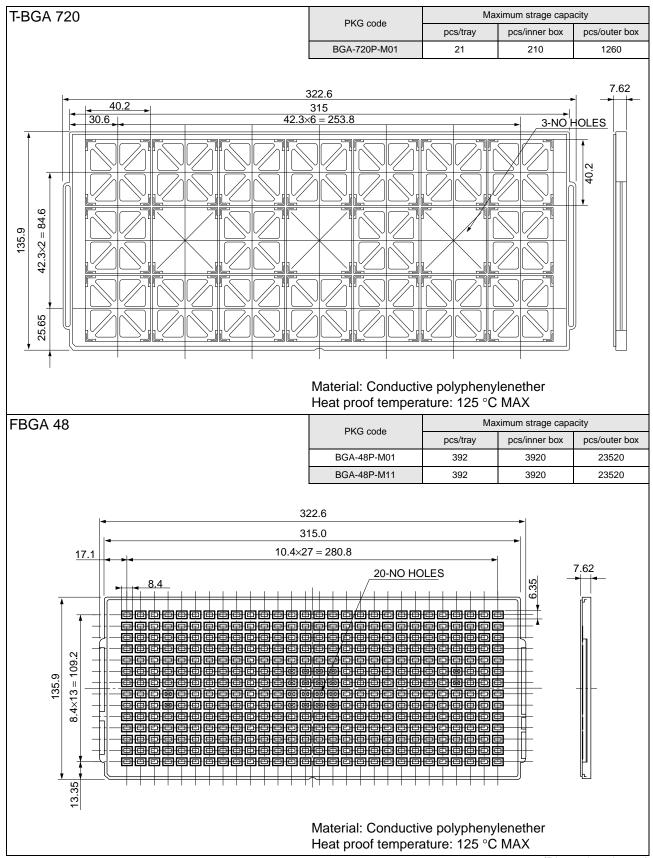


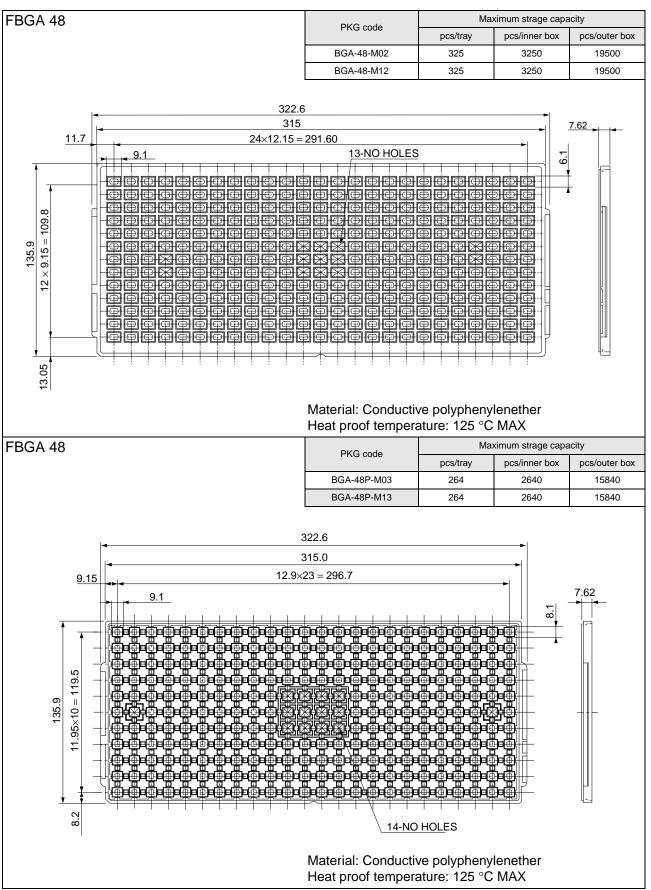
#### 6.3.9 BGA, T-BGA, FBGA (heat proof type)

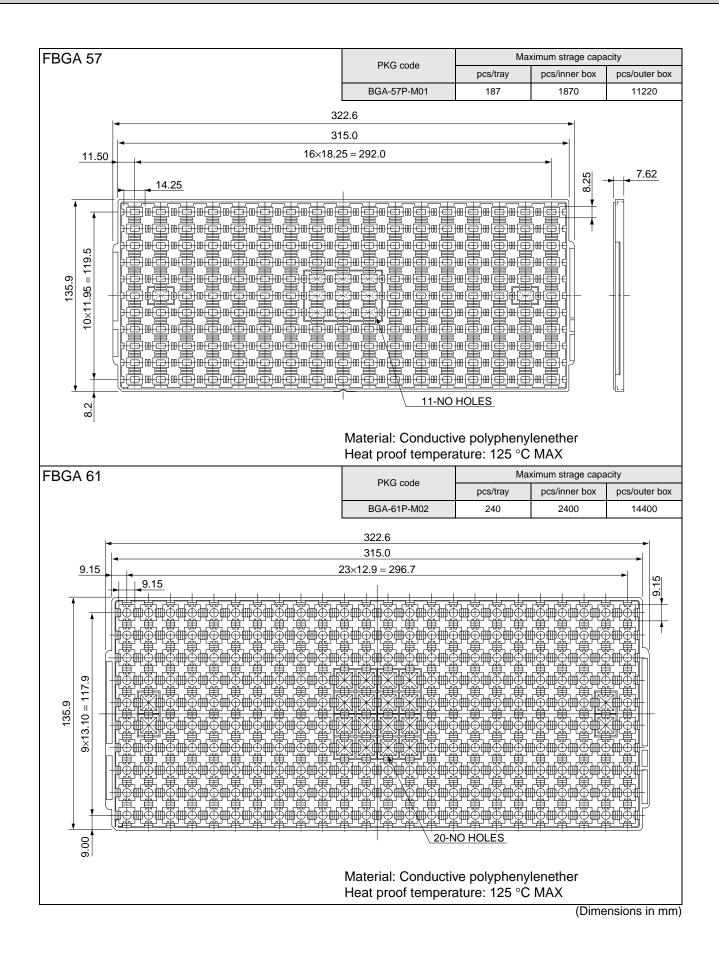


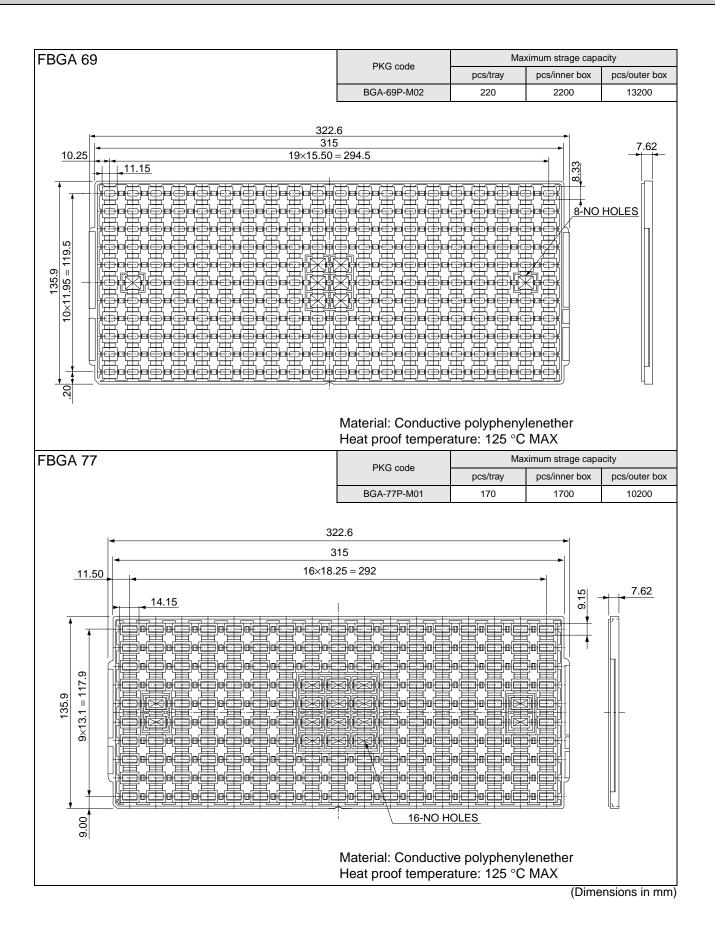


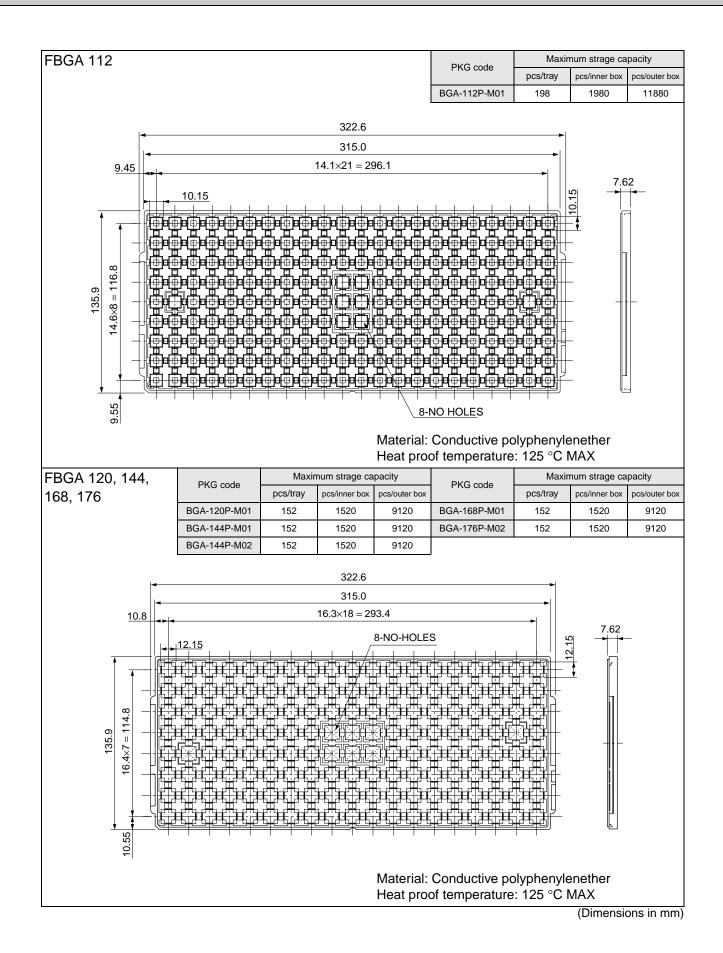


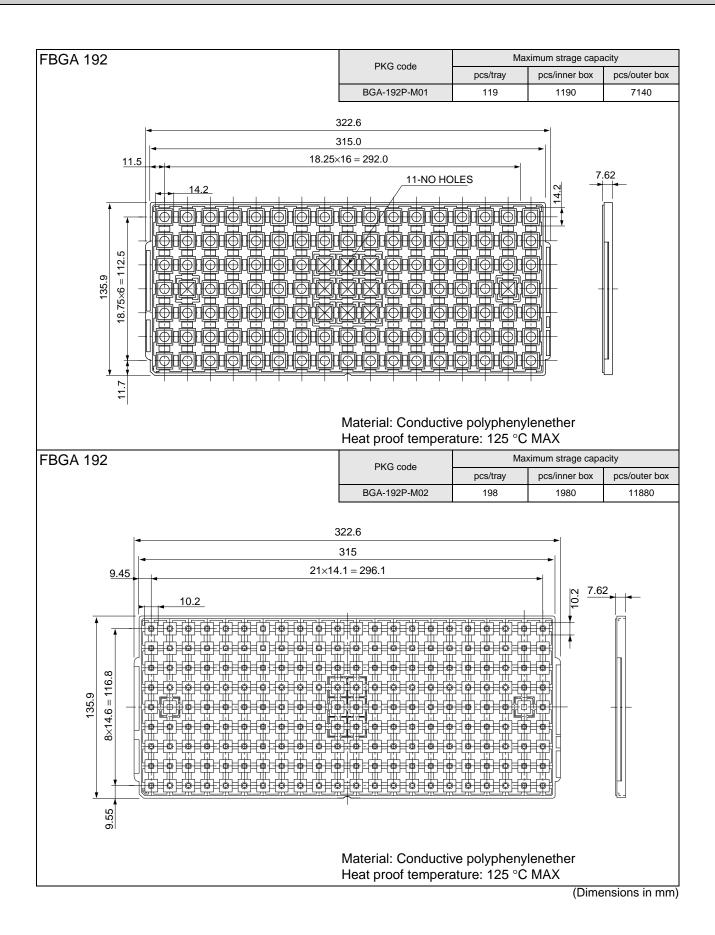


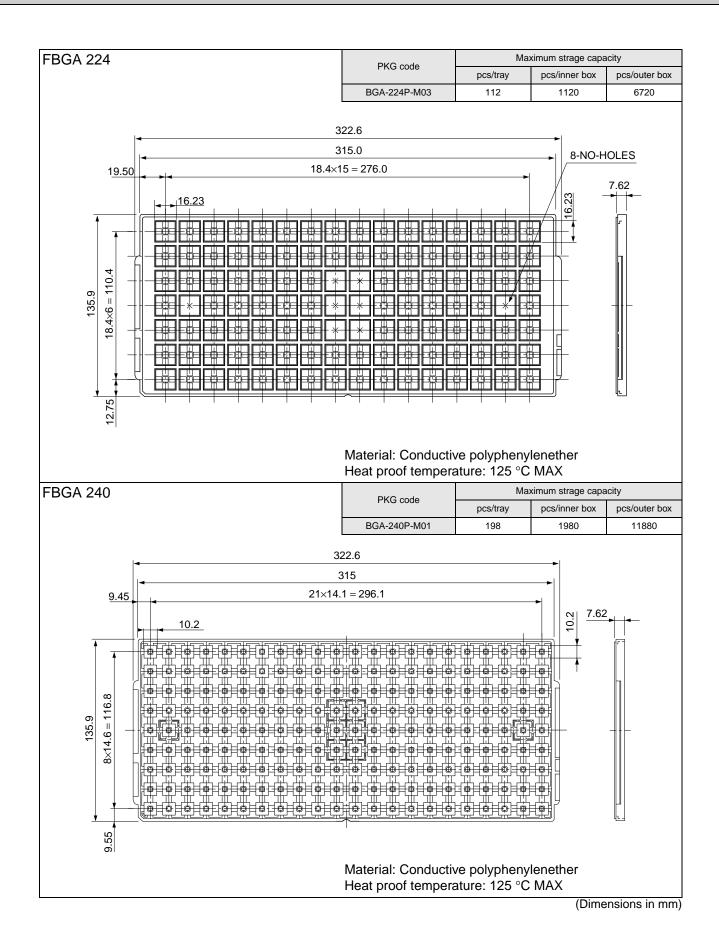


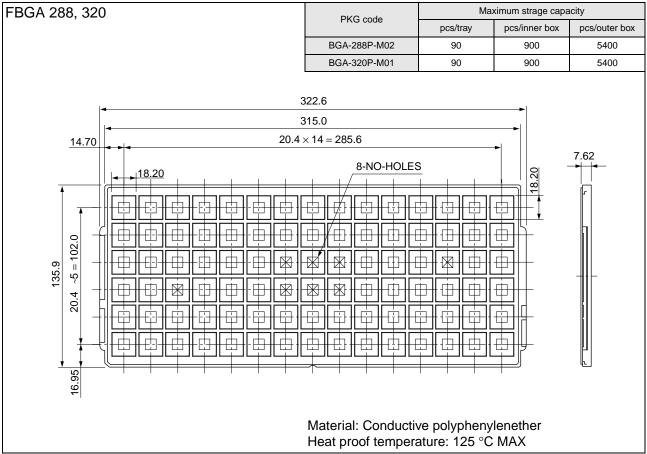






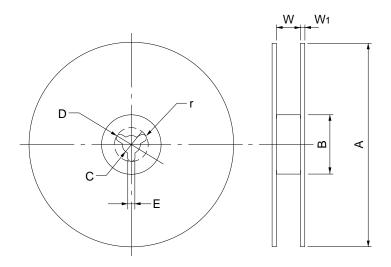






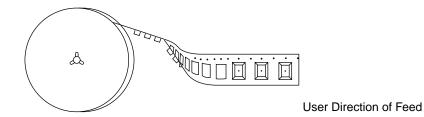
### 6.4 Taping Dimensions

- 6.4.1 Embossed tapes(standard: conforms with JIS)
- (1) Reel dimensions

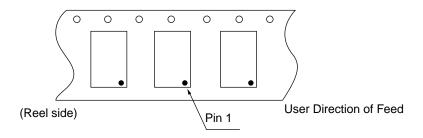


Tape width Symbol	12mm	16mm	24mm	32mm	44mm					
А	330 ± 2.0									
^		$254\pm2.0$								
В	100 <sup>+2.0</sup> <sub>-0</sub> 100 <sup>+2.0</sup> <sub>-0</sub> /150 <sup>+2.0</sup> <sub>-0</sub>									
С		13 ± 0.2								
D		21 ± 0.8								
E	2 ± 0.5									
W	12.4 to 14.4	16.4 to 18.4	24.4 to 26.4	32.4 to 34.4	44.4 to 46.4					
W1	2.9 max.									
r	1 ± 0.2									

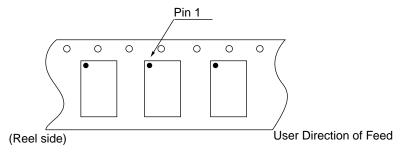
#### (2) IC orientation



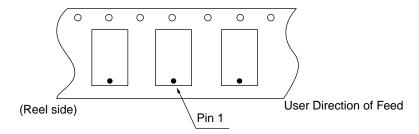
- a) SOP, QFP, SOJ, SON, BCC, FBGA
  - EF type



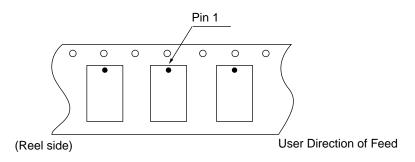
• ER type



- b) LCC, QFJ, TSOP
  - EF type

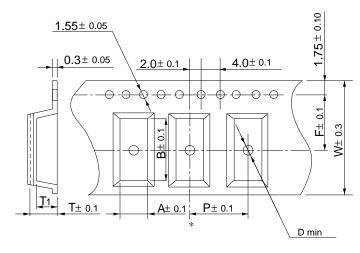


• ER type



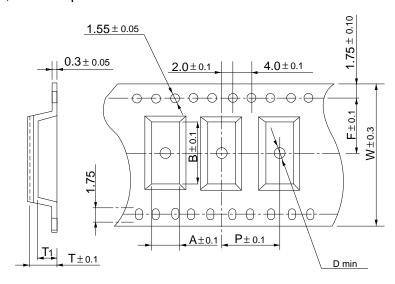
### (3) Tape dimensions

12mm, 16mm, 24mm tape width



\*: The pocket shapes have raised bottoms, except for BCCs.

#### 32mm, 44mm tape width



### • Tape dimansions

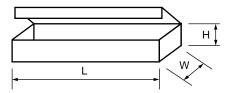
DKO I		Symbol Symbol								
	PKG code	Α	В	F	Р	Т	T1	D	W	
	FPT-8P-M01	8.3	6.65	7.5	12.0	2.85	2.35	2.0	16.0	
	FPT-8P-M02	6.5	5.4	5.5	8.0	2.1	1.7	2.0	12.0	
	FPT-14P-M04	8.5	10.6	7.5	12.0	2.5	2.2	2.0	16.0	
	FPT-16P-M03	10.85	13.0	11.5	12.0	3.55	3.05	2.0	24.0	
	FPT-16P-M04	6.9	10.5	7.5	12.0	2.45	1.95	1.5	16.0	
6	FPT-16P-M06	8.5	10.6	7.5	12.0	2.5	2.2	2.0	16.0	
S O P	FPT-20P-M01	8.2	13.1	11.5	12.0	2.7	2.3	2.0	24.0	
P	FPT-20P-M02	10.85	13.0	11.5	12.0	3.55	3.05	2.0	24.0	
	FPT-20P-M05	10.85	13.0	11.5	12.0	3.55	3.05	2.0	24.0	
	FPT-24P-M01	8.5	15.6	11.5	12.0	2.8	2.2	2.0	24.0	
	FPT-24P-M02	10.65	15.6	11.5	12.0	3.3	2.8	2.0	24.0	
	FPT-28P-M01	10.8	18.2	11.5	16.0	3.3	2.8	2.0	24.0	
	FPT-32P-M03	14.8	21.1	14.2	16.0	3.1	2.6	2.0	32.0	
	FPT-8P-M03	6.7	3.9	5.5	8.0	1.85	1.35	1.5	12.0	
	FPT-16P-M05	6.9	5.5	7.5	12.0	2.0	1.5	1.6	16.0	
	FPT-20P-M03	6.9	7.0	7.5	12.0	2.0	1.5	1.5	16.0	
	FPT-20P-M04	6.9	7.0	7.5	12.0	2.0	1.5	1.5	16.0	
S S O P	FPT-24P-M03	8.1	8.3	7.5	12.0	2.0	1.5	1.6	16.0	
O	FPT-30P-M02	8.1	10.2	7.5	12.0	2.0	1.5	1.6	16.0	
	FPT-34P-M01	10.8	18.2	11.5	16.0	3.3	2.8	2.0	24.0	
	FPT-34P-M02	10.8	18.2	11.5	16.0	3.3	2.8	2.0	24.0	
	FPT-34P-M03	8.55	11.5	11.5	12.0	1.8	1.35	2.0	24.0	
	FPT-40P-M01	12.4	18.2	11.5	16.0	3.3	2.8	2.0	24.0	
	FPT-32P-M24	8.5	20.4	14.2	12.0	1.65	1.25	2.0	32.0	
	FPT-32P-M25	8.5	20.4	14.2	12.0	1.65	1.25	2.0	32.0	
	FPT-40P-M06	10.35	20.4	14.2	16.0	1.7	1.4	2.1	32.0	
	FPT-40P-M07	10.35	20.4	14.2	16.0	1.7	1.4	2.1	32.0	
_	FPT-44P-M07	12.15	18.95	11.5	16.0	1.65	1.25	2.0	24.0	
T S	FPT-44P-M08	12.15	18.95	11.5	16.0	1.65	1.25	2.0	24.0	
O P	FPT-44P-M16	16.6	28.8	20.2	24.0	3.5	3.0	2.0	44.0	
	FPT-44P-M18	12.15	18.95	11.5	16.0	1.65	1.25	2.0	24.0	
	FPT-48P-M19	12.6	20.4	14.2	16.0	1.6	1.2	2.0	32.0	
	FPT-48P-M20	12.6	20.4	14.2	16.0	1.6	1.2	2.0	32.0	
	FPT-50P-M05	12.25	21.35	14.2	16.0	1.7	1.2	2.0	32.0	
	FPT-50P-M06	12.25	21.35	14.2	16.0	1.7	1.2	2.0	32.0	

PKG code		Symbol								
	FING COUE		В	F	Р	Т	T1	D	W	
S O	LCC-40P-M02	10.35	11.2	11.5	12.0	1.25	0.95	2.0	24.0	
N	LCC-46P-M02	10.45	12.55	11.5	12.0	1.35	0.95	2.0	24.0	
R	LCC-16P-M02	3.65	4.8	5.5	8.0	1.0	_	1.5	12.0	
B C C	LCC-16P-M03	4.45	4.8	5.5	8.0	1.0	_	1.5	12.0	
	LCC-48P-M02	7.25	7.25	5.50	12.0	1.0	_	1.5	12.0	
QFP	FPT-32P-M21	9.5	9.5	7.5	12.0	2.2	1.7	1.6	16.0	
L Q F	FPT-48P-M05	9.5	9.5	7.5	12.0	2.2	1.7	1.6	16.0	
F P	FPT-64P-M03	12.5	12.5	11.5	16.0	2.2	1.7	2.0	24.0	
	LCC-28P-M03	12.85	12.85	11.5	16.0	4.8	3.7	2.0	24.0	
	LCC-32P-M02	12.8	15.3	11.5	16.0	4.0	2.8	1.9	24.0	
Q F	LCC-44P-M02	17.9	17.9	14.2	20.0	4.8	3.7	2.0	32.0	
J	LCC-68P-M02	25.6	25.6	20.2	32.0	4.8	3.7	2.0	44.0	
	LCC-84P-M02	30.7	30.7	20.2	36.0	4.8	3.7	2.0	44.0	
S	LCC-28C-A04	11.95	18.95	11.5	16.0	4.45	2.8	2.0	24.0	
J	LCC-42P-M01	11.6	27.75	20.2	16.0	4.1	2.9	1.5	44.0	
F	BGA-48P-M01	6.3	8.3	7.5	8.0	1.85	0.85	1.5	16.0	
B G	BGA-48P-M02	6.1	9.1	7.5	8.0	1.85	0.85	1.5	16.0	
Ā	BGA-48P-M03	8.1	9.25	11.5	12.0	1.26	0.72	1.5	24.0	

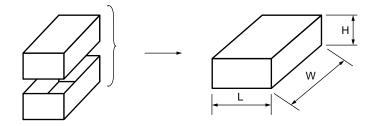
### 6.5 Dimensions for Containers

(1) Dimensions for inner box

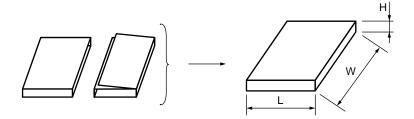
• TypeA



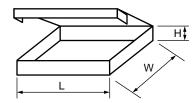
• TypeB



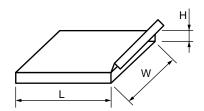
• TypeC



• TypeD



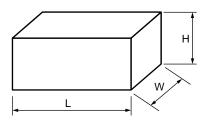
• TypeE



					Inner	Dimensions for inner box			
				type	L	W	Н		
Tube				A	550	125	75		
Tray Dimensions		ions	165.00 × 260.00		В	180	275	45	
			135.90 × 322.60		В	165	360	75	
			135.90 × 314	135.90 × 314.96					
			135.90 × 315.00						
			135.89 × 314.96						
			220.00 × 260.00 219.00 × 259.00		С	225	265	15	
			292.00 × 280	.50	D	320	290	65	
Emboss	Reel	φ330	Tape width	12.16	D	365	345	40	
				24.32		365	345	50	
	φ254			44		365	345	65	
				Е	265	265	50		

(Dimensions in mm)

### (2) Dimensions for outer box



					Dimensions for outer box				
				L	W	Н			
Tube					580	305	210		
Tray	Dimensi	ons	165.00 × 260.00		395	315	215		
			135.90 × 322.60		580	395	210		
			135.90 × 314	1.96					
			135.90 × 315.00						
		135.89 × 314.96							
		220.00 × 260.00		580	305	210			
			219.00 × 259.00						
			292.00 × 280	).50	340	320	295		
Emboss	Reel	Reel \$\phi330\$	Tape width	12.16	425	410	325		
				24.32					
				44					
	ф254		580	305	210				