

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,
AMD EPYC 9845, 2.10 GHz

SPECrate®2017_int_base = 2450

SPECrate®2017_int_peak = Not Run

CPU2017 License: 19

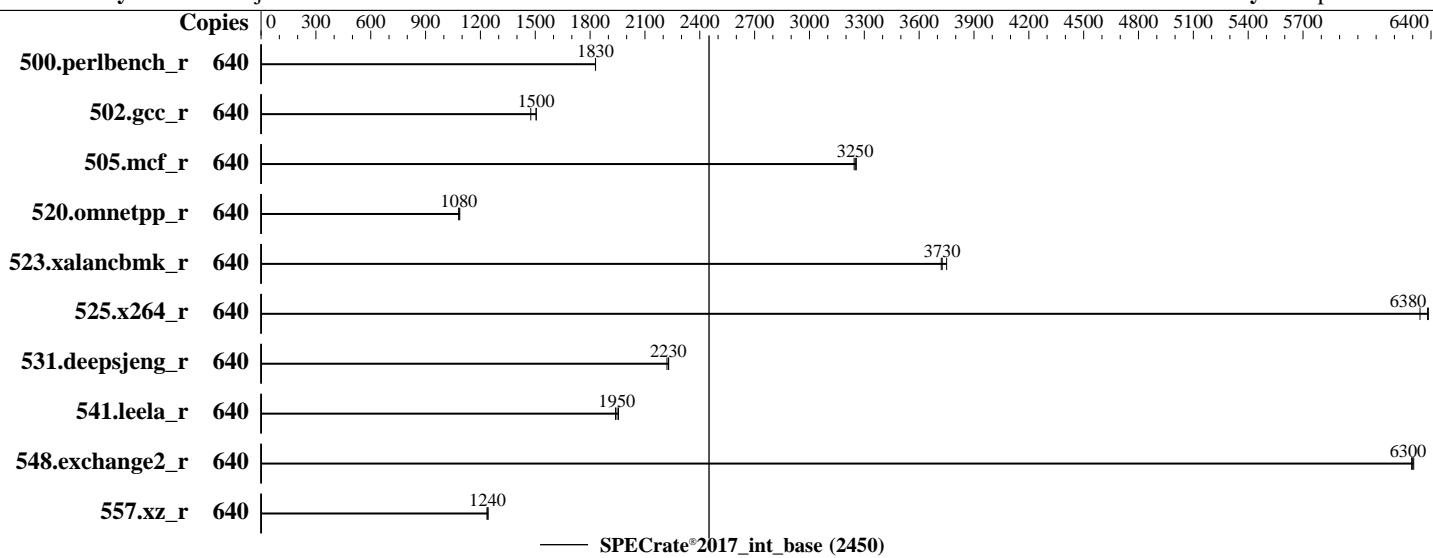
Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Oct-2024

Hardware Availability: Nov-2024

Software Availability: Sep-2024



Hardware

CPU Name: AMD EPYC 9845
 Max MHz: 3700
 Nominal: 2100
 Enabled: 320 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 320 MB I+D on chip per chip, 32 MB shared / 16 cores
 Other: None
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)
 running at 6000
 Storage: 1x SATA SSD, 4 TB
 Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
 Compiler: kernel version
 6.4.0-150600.21-default
 Parallel: C/C++/Fortran: Version 5.0.0 of AOCC
 Firmware: No
 Fujitsu BIOS Version V50.0.0.027 R9.90.0 for
 D4130-A1x. Released Jan-2024
 tested as V5.0.0.27 R9.90 for D4130-A1x Sep-2024
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: None
 Power Management: BIOS set to prefer performance
 at the cost of additional power usage

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,
AMD EPYC 9845, 2.10 GHz

SPECrate®2017_int_base = 2450

SPECrate®2017_int_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Oct-2024

Hardware Availability: Nov-2024

Software Availability: Sep-2024

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	640	557	1830	557	1830	557	1830									
502.gcc_r	640	602	1500	614	1480	602	1500									
505.mcf_r	640	318	3250	319	3240	318	3260									
520.omnetpp_r	640	777	1080	772	1090	775	1080									
523.xalancbmk_r	640	182	3720	180	3750	181	3730									
525.x264_r	640	177	6340	176	6380	175	6390									
531.deepsjeng_r	640	330	2220	329	2230	329	2230									
541.leela_r	640	547	1940	542	1960	544	1950									
548.exchange2_r	640	266	6300	266	6290	266	6310									
557.xz_r	640	559	1240	558	1240	556	1240									

SPECrate®2017_int_base = 2450

SPECrate®2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable THP for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,
AMD EPYC 9845, 2.10 GHz

SPECrate®2017_int_base = 2450

SPECrate®2017_int_peak = Not Run

CPU2017 License: 19

Test Date: Oct-2024

Test Sponsor: Fujitsu

Hardware Availability: Nov-2024

Tested by: Fujitsu

Software Availability: Sep-2024

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/home/Benchmark/speccpu2017r-Turin/amd_rate_aocc500_znver5_A_lib/lib:/home/Benchmark/speccpu2017r-Tur
    in/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:

APBDIS = 0

Determinism Slider = Power

TDP Control = Manual

TDP Limit = 400

Package Power Limit Control = Manual

Package Power Limit = 400

Power Profile Selection = High Performance

NUMA nodes per socket = NPS2

Probe Filter Organization = Shared

FAN Control = Full

```
Sysinfo program /home/Benchmark/speccpu2017r-Turin/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Fri Oct 4 08:27:30 2024
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active

(Continued on next page)

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,
AMD EPYC 9845, 2.10 GHz

SPECrate®2017_int_base = 2450

SPECrate®2017_int_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Oct-2024

Hardware Availability: Nov-2024

Software Availability: Sep-2024

Platform Notes (Continued)

```
16. sysctl  
17. /sys/kernel/mm/transparent_hugepage  
18. /sys/kernel/mm/transparent_hugepage/khugepaged  
19. OS release  
20. Disk information  
21. /sys/devices/virtual/dmi/id  
22. dmidecode  
23. BIOS
```

```
1. uname -a  
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)  
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w  
08:27:30 up 5:02, 2 users, load average: 0.13, 45.38, 270.78  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root ttys1 - 03:25 5:01m 0.02s 0.02s -bash  
root pts/0 10.37.59.48 08:26 18.00s 1.13s 0.18s /bin/bash ./amd_rate_aocc500_znver5_A1.sh
```

```
3. Username  
From environment variable $USER: root
```

```
4. ulimit -a  
core file size          (blocks, -c) unlimited  
data seg size           (kbytes, -d) unlimited  
scheduling priority     (-e) 0  
file size               (blocks, -f) unlimited  
pending signals          (-i) 6185440  
max locked memory       (kbytes, -l) 2097152  
max memory size         (kbytes, -m) unlimited  
open files              (-n) 65536  
pipe size               (512 bytes, -p) 8  
POSIX message queues    (bytes, -q) 819200  
real-time priority      (-r) 0  
stack size              (kbytes, -s) unlimited  
cpu time                (seconds, -t) unlimited  
max user processes       (-u) 6185440  
virtual memory           (kbytes, -v) unlimited  
file locks              (-x) unlimited
```

```
5. sysinfo process ancestry  
/usr/lib/systemd/systemd --switched-root --system --deserialize=42  
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups  
sshd: root [priv]  
sshd: root@pts/0  
-bash  
python3 ./run_amd_intrate_aocc500_znver5_A1_31.py  
/bin/bash ./amd_rate_aocc500_znver5_A1.sh  
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune base --reportable --iterations 3 intrate  
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune base --reportable --iterations 3 --nopower  
--runmode rate --tune base --size test:train:refrate intrate --nopreenv --note-preenv --logfile  
$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2  
specperl $SPEC/bin/sysinfo  
$SPEC = /home/Benchmark/speccpu2017r-Turin
```

(Continued on next page)

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,
AMD EPYC 9845, 2.10 GHz

SPECrate®2017_int_base = 2450

SPECrate®2017_int_peak = Not Run

CPU2017 License: 19

Test Date: Oct-2024

Test Sponsor: Fujitsu

Hardware Availability: Nov-2024

Tested by: Fujitsu

Software Availability: Sep-2024

Platform Notes (Continued)

```
6. /proc/cpuinfo
model name      : AMD EPYC 9845 160-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 26
model          : 17
stepping        : 0
microcode       : 0xb101021
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 192 4K pages
cpu cores       : 160
siblings         : 320
2 physical ids (chips)
640 processors (hardware threads)
physical id 0: core ids 0-159
physical id 1: core ids 0-159
physical id 0: apicids 0-319
physical id 1: apicids 512-831
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

```
7. lscpu
```

```
From lscpu from util-linux 2.39.3:
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:              Little Endian
CPU(s):                 640
On-line CPU(s) list:    0-639
Vendor ID:               AuthenticAMD
BIOS Vendor ID:         Advanced Micro Devices, Inc.
Model name:              AMD EPYC 9845 160-Core Processor
BIOS Model name:         AMD EPYC 9845 160-Core Processor
                         Unknown CPU @ 2.1GHz
BIOS CPU family:        107
CPU family:              26
Model:                  17
Thread(s) per core:     2
Core(s) per socket:     160
Socket(s):              2
Stepping:                0
Frequency boost:        enabled
CPU(s) scaling MHz:    40%
CPU max MHz:             3718.0659
CPU min MHz:             1500.0000
BogoMIPS:                4193.81
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mttr pge mca cmov pat
                        pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                        rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                        extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                        sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                        cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                        osvw ibs skinit wdt tce topoext perfctr_nb bpext
                        perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                        ibrs ibpb stibp ibrs_enhanced vmmcall fsqsbbase tsc_adjust bmi1 avx2
                        smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                        xsavec xgetbv1 xsaves cqmq_llc cqmq_occu_llc cqmq_mb_m_total
```

(Continued on next page)

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,
AMD EPYC 9845, 2.10 GHz

SPECrate®2017_int_base = 2450

SPECrate®2017_int_peak = Not Run

CPU2017 License: 19

Test Date: Oct-2024

Test Sponsor: Fujitsu

Hardware Availability: Nov-2024

Tested by: Fujitsu

Software Availability: Sep-2024

Platform Notes (Continued)

```
cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero iperf
xsaverptr rdpru wbnoinvd amd_ppin cpc_arat npt lbrv svm_lock
nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi
avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
flush_lld debug_swap
AMD-V
```

Virtualization:

L1d cache: 15 MiB (320 instances)

L1i cache: 10 MiB (320 instances)

L2 cache: 320 MiB (320 instances)

L3 cache: 640 MiB (20 instances)

NUMA node(s): 4

NUMA node0 CPU(s): 0-79,320-399

NUMA node1 CPU(s): 80-159,400-479

NUMA node2 CPU(s): 160-239,480-559

NUMA node3 CPU(s): 240-319,560-639

Vulnerability Gather data sampling: Not affected

Vulnerability Itlb multihit: Not affected

Vulnerability Llft: Not affected

Vulnerability Mds: Not affected

Vulnerability Meltdown: Not affected

Vulnerability Mmio stale data: Not affected

Vulnerability Reg file data sampling: Not affected

Vulnerability Retbleed: Not affected

Vulnerability Spec rstack overflow: Not affected

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization

Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP always-on; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	15M	12	Data	1	64	1	64
L1i	32K	10M	8	Instruction	1	64	1	64
L2	1M	320M	16	Unified	2	1024	1	64
L3	32M	640M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus: 0-79,320-399

node 0 size: 385955 MB

node 0 free: 384265 MB

node 1 cpus: 80-159,400-479

node 1 size: 387030 MB

node 1 free: 385390 MB

node 2 cpus: 160-239,480-559

node 2 size: 387030 MB

node 2 free: 385702 MB

node 3 cpus: 240-319,560-639

node 3 size: 386369 MB

node 3 free: 385080 MB

node distances:

node 0 1 2 3

0: 10 12 32 32

(Continued on next page)

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,
AMD EPYC 9845, 2.10 GHz

SPECrate®2017_int_base = 2450

SPECrate®2017_int_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Oct-2024

Hardware Availability: Nov-2024

Software Availability: Sep-2024

Platform Notes (Continued)

```
1: 12 10 32 32  
2: 32 32 10 12  
3: 32 32 12 10
```

9. /proc/meminfo

```
MemTotal: 1583499416 kB
```

10. who -r

```
run-level 3 Oct 4 03:25
```

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

```
Default Target Status  
multi-user running
```

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance iscsi issue-generator kbdsettings kdump kdump-early kdump-notify klog lvm2-monitor nsqd postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore virtqemud wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	autofs autoast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info firewalld fsidd gpm grub2-once haveged ipmi ipmievfd iscsi-init iscsid issue-add-ssh-keys kexec-load ksm kvm_stat libvirt-guests lumask man-db-create multipathd nfs nfs-blkmap nfs-server nfsserver rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd strongswan strongswan-starter svnservice systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-nspawn@ systemd-sysexit systemd-time-wait-sync systemd-timesyncd tcasd tuned udisks2 virtinterfaced virtlockd virtlogd virtnetworkd virtnodeudev virtnwfiltred virtsecretd virtstoraged vncserver@
indirect	pcscd systemd-userdbd tftp wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default  
root=UUID=d1f4aaab-d342-4148-a01a-ae0d7fed3534  
splash=silent  
mitigations=auto  
quiet  
security=apparmor  
crashkernel=384M,high  
crashkernel=72M,low
```

14. cpupower frequency-info

```
analyzing CPU 32:
```

```
current policy: frequency should be within 1.50 GHz and 2.10 GHz.  
The governor "ondemand" may decide which speed to use  
within this range.
```

```
boost state support:
```

```
Supported: yes
```

```
Active: yes
```

15. tuned-adm active

```
It seems that tuned daemon is not running, preset profile is not activated.
```

(Continued on next page)

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,
AMD EPYC 9845, 2.10 GHz

SPECrate®2017_int_base = 2450

SPECrate®2017_int_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Oct-2024

Hardware Availability: Nov-2024

Software Availability: Sep-2024

Platform Notes (Continued)

Preset profile: balanced

```
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space       0
vm.compaction_proactiveness    20
vm.dirty_background_bytes       0
vm.dirty_background_ratio       10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy       0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode           1
```

```
17. /sys/kernel/mm/transparent_hugepage
defrag           [always] defer defer+madvise madvise never
enabled          [always] madvise never
hpage_pmd_size  2097152
shmem_enabled    always within_size advise [never] deny force
```

```
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs        60000
defrag                      1
max_ptes_none                511
max_ptes_shared              256
max_ptes_swap                64
pages_to_scan                4096
scan_sleep_millisecs         10000
```

```
19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

```
20. Disk information
SPEC is set to: /home/Benchmark/speccpu2017r-Turin
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdb3        xfs   3.5T  98G  3.4T   3%  /home
```

```
21. /sys/devices/virtual/dmi/id
Vendor:          FUJITSU
Product:         PRIMERGY RX2450 M2
Product Family: SERVER
Serial:          xxxxxxxxxxxx
```

(Continued on next page)

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,
AMD EPYC 9845, 2.10 GHz

SPECrate®2017_int_base = 2450

SPECrate®2017_int_peak = Not Run

CPU2017 License: 19

Test Date: Oct-2024

Test Sponsor: Fujitsu

Hardware Availability: Nov-2024

Tested by: Fujitsu

Software Availability: Sep-2024

Platform Notes (Continued)

22. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

18x Samsung M321R8GA0EB2-CCPKC 64 GB 2 rank 6400, configured at 6000
6x Samsung M321R8GA0EB2-CCPPC 64 GB 2 rank 6400, configured at 6000

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: FUJITSU // American Megatrends Inc.
BIOS Version: V5.0.0.35 R9.90.0_NoPSB for D4129-A1x
BIOS Date: 09/13/2024
BIOS Revision: 9.90
Firmware Revision: 2.45

Compiler Version Notes

C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++ | 520.omnetpp_r(base) 523.xalancmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Fortran | 548.exchange2_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

(Continued on next page)

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,
AMD EPYC 9845, 2.10 GHz

SPECrate®2017_int_base = 2450

SPECrate®2017_int_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Oct-2024

Hardware Availability: Nov-2024

Software Availability: Sep-2024

Base Compiler Invocation (Continued)

Fortran benchmarks:

flang

Base Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-Wl,-mllvm -Wl,-extra-inliner -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc-ext -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang -lamdalloc-ext
-ldl

(Continued on next page)

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,
AMD EPYC 9845, 2.10 GHz

SPECrate®2017_int_base = 2450

SPECrate®2017_int_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Oct-2024

Hardware Availability: Nov-2024

Software Availability: Sep-2024

Base Optimization Flags (Continued)

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto  
-fepilog-vectorization-of-inductions -mllvm -optimize-strided-mem-cost  
-floop-transform -mllvm -unroll-aggressive -mllvm -unroll-threshold=500  
-lamdlibm -lflang -lamdalloc -ldl
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2024-10-04 08:27:29+0900.

Report generated on 2024-10-07 21:22:01 by CPU2017 PDF formatter v6716.