

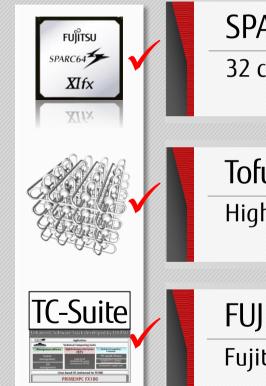
Key Technologies of FUJITSU Supercomputer

PRIMEHPC FX100



Technology highlights





SPARC64[™] XIfx

32 compute cores with HPC-ACE2 and 2 assistant cores

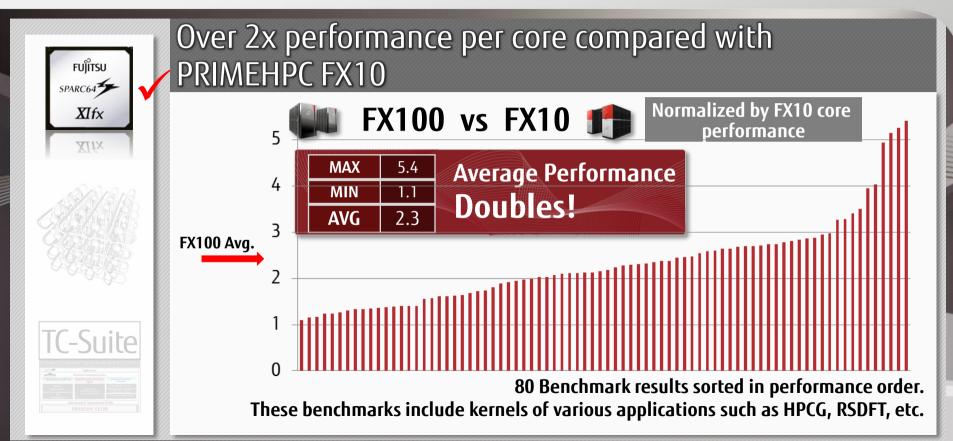
Tofu interconnect 2

Highly scalable interconnect with low latency and high bandwidth

FUJITSU Technical Computing Suite (TC-Suite)

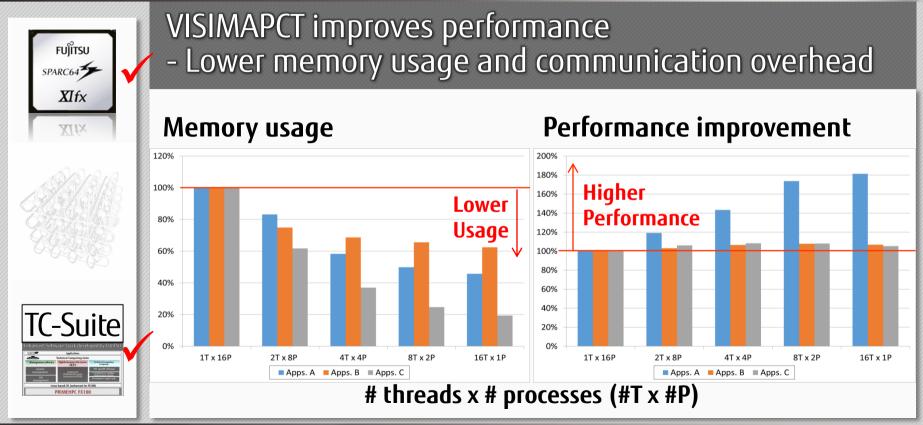
Fujitsu-developed HPC software stack optimized for PRIMEHPC series

Improved performance for real applications

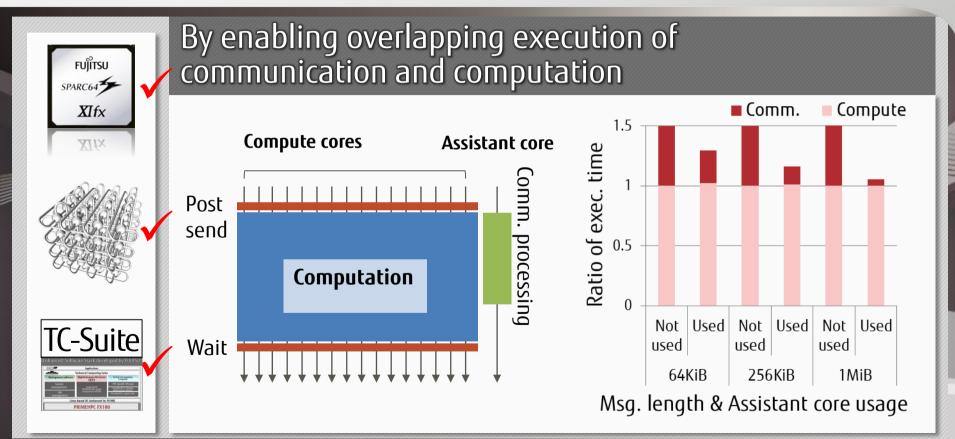


Effect of VISIMPACT

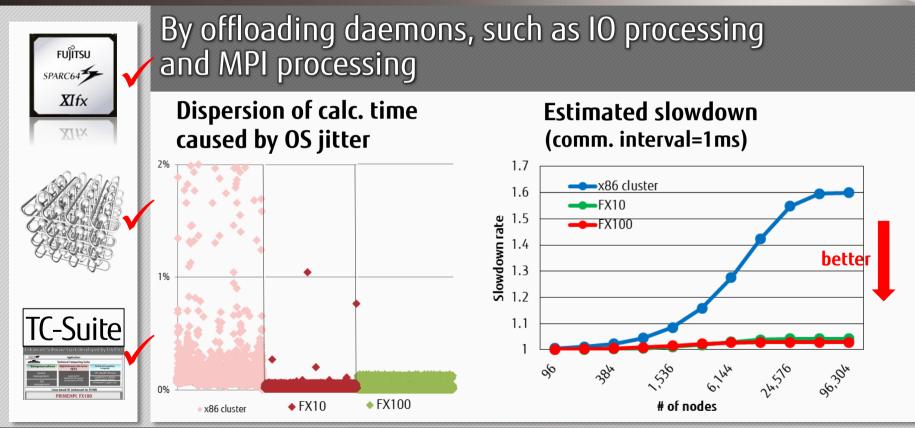




Assistant cores offload non-blocking comm. Fujitsu



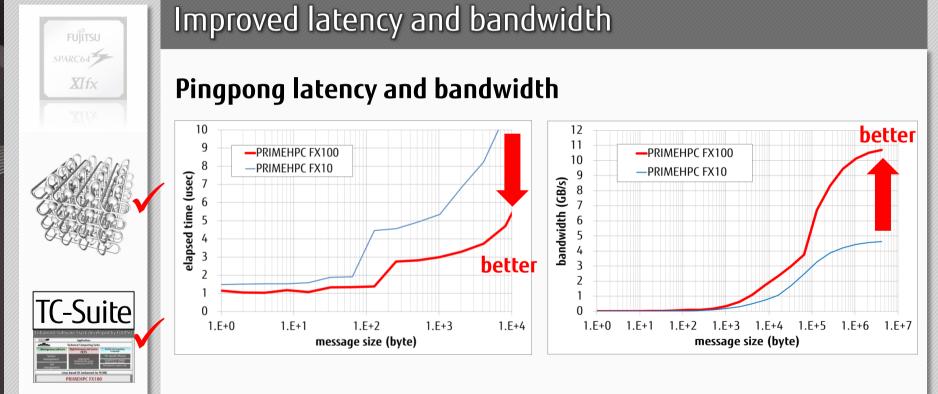
Assistant cores eliminate OS jitter



5

High performance Tofu Interconnect 2



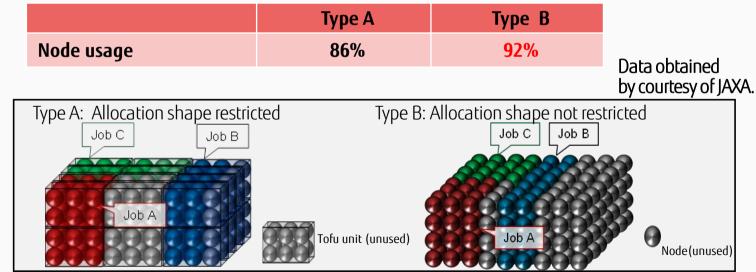


TC-Suite: Flexible job allocation



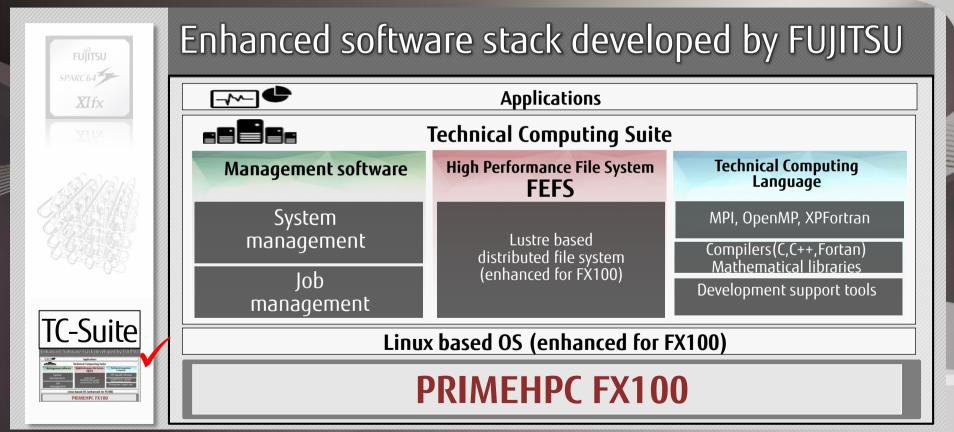


High node usage rate to 92% can be attained and job waiting time can be shortened by allowing type B Type A reduces MPI comm. time with Tofu specific algorithm



Technical Computing Suite





FUJITSU

shaping tomorrow with you