

The K computer



World's No.1 on TOP500

4 HPCC class 1 awards Gordon Bell Prize (High perf.)

Project Overview



Project Overview

- Fujitsu and RIKEN are jointly developing the Next-Generation Supercomputer under an initiative by MEXT (Ministry of Education, Culture, Sport and Science)
- System architecture: Scalar Parallel
- Linpack Performance: 10.51 petaflops
- Slated for completion in 2012

K computer

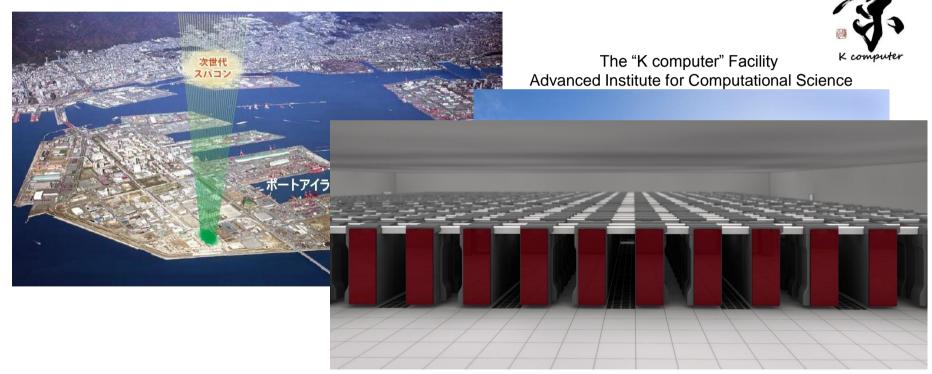
Schedule of the project

2006 2	2007	2008	2009	2010	2011	2012
Conceptual design	Detailed design		Prototype, evaluation	Production, installatio	n and adjustment	Tuning and improvement

Project Overview



■ Located at Port Island in Kobe City



System Overview



- System Overview
 - Ultra-large scale system powered by 88,128 processors
 - Fujitsu has brought together cutting-edge technologies including SPARC64 VIIIfx, high-performance, highly-reliable and low power consumption processor



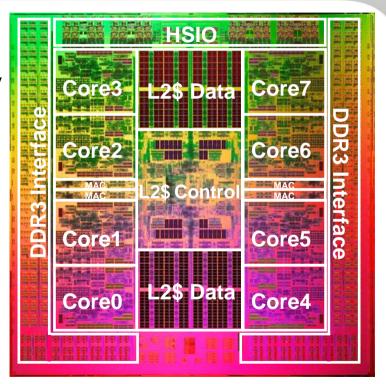
System	Linpack performance: 10.51 petaflops # of CPUs: 88,128 CPUs Total memory: 1.3 petabyte
CPU	SPARC64 VIIIfx (8cores, 128 gigaflops)
Interconnect	6-dimensional Mesh/Torus topology

Processor: SPARC64 VIIIfx



- High-performance and highly reliable, plus low power consumption necessary for ultra-large scale system
 - Integrated cores, cache and memory I/F

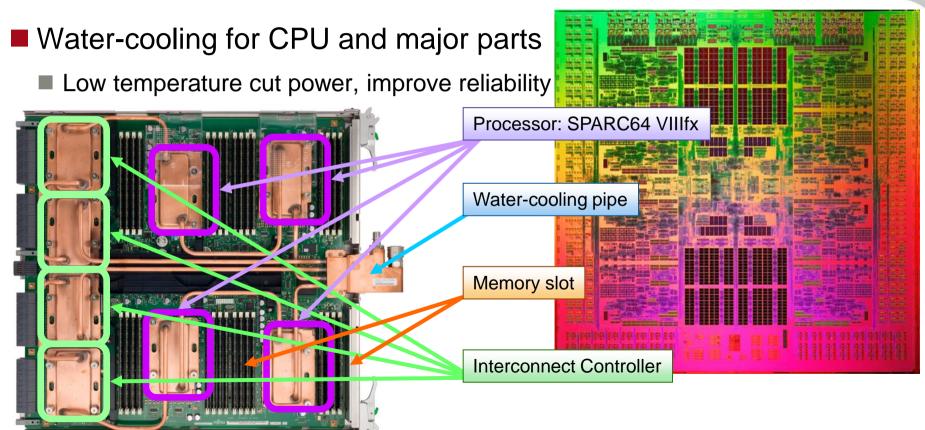
Number of cores		8 cores	
Clock		2 GHz	
Cache L1		I: 32KB/core, D: 32KB/core	
	L2	6MB (Shared cache)	
Peak Performance		128 gigaflops	
Power consumption		58W (typical)	
Process		45 nm	



SPARC64 VIIIfx Chip Layout

System Board





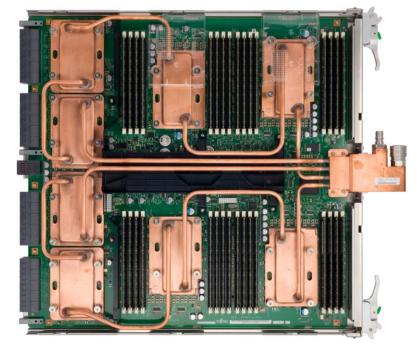
System Rack



■ 102 processors in 1 system rack

■ 3 cabinets comparable to the original Earth Simulator

(320 cabinets)





Installation Image



- Ultra-large system with 88,128 processors
 - Innovative 6 dimensional mesh/torus interconnect
 - System software also provided by Fujitsu







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