

$1/\Phi = 1 + \Phi$

HPC, simply.



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Industrial Application Challenges





XEON PHI Cornerstones





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Application Tuning on XEON PHI

FUjitsu

Coarse grain

- Inter Process
 Communication
- One sided Data Exchange
- Collective Operations
- IOs

Memory hierarchy

- Memory tuning (cache, stack, heap)
- Cache management
- Loop blocking
- Aggressive prefetch

Understanding the way application and XEON PHI interact together for optimal performance tuning

Fine Grain

- Multi-Threading Execution
- Loop dependencies
- Inter core synchronization
 - Thread scheduling

Vectorization

- Loop transformation
- CPU Usage
- Vectorization & SIMD extension
- In order instruction execution tuning
- Reductions

Embedded tools

Fujitsu Technology

- Non intrusive tools for fully optimized application binaries performance analysis
- Low overhead (<5%) and reduced memory footprint (few MBs per core)
- Per-thread based rather than system wide
- Integrates measurement data associated with same context from different threads

Efficient & Simple Usage

- Analyzing threads hotspots
- Identify scaling losses
- Quantify load imbalance
- Collect call path trace
- Understanding load imbalance and temporal patterns
- Integrates both MPI and OpenMP



Multi-Thread Analysis





- Impact of message passing
- IOs profile

Multi-Process Analysis





Specific MPI tool multiple XEON-PHI oriented:

- Heterogeneous large MPI application (MPMD)
- Focus on global application behavior
- Used to identify global bottlenecks of production applications with real data

Profiling Accuracy





And the story continues !





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