

Fujitsu M10/SPARC M10 Systems

Product Notes for XCP Version 2352



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Preface

This document describes the latest information about XSCF Control Package (XCP) and the important and latest information regarding hardware, firmware, software, and documents of SPARC M10 Systems.

Note - If a newer version of XCP than the version supported in this document is released, only the document supporting the latest version of XCP is updated. In addition to reading this document, visit the following websites to see the document supporting the latest version of XCP. Check the contents and usage to confirm whether there are any corrections in the documents related to the XCP version that you use.

- Global site
<http://www.fujitsu.com/global/products/computing/servers/unix/sparc/downloads/manuals/>
 - Japanese site
<http://www.fujitsu.com/jp/products/computing/servers/unix/sparc/downloads/manual/>
-

Fujitsu M10 is sold as SPARC M10 Systems by Fujitsu in Japan.
Fujitsu M10 and SPARC M10 Systems are identical products.

Audience

This document is designed for system administrators with advanced knowledge of a computer network and Oracle Solaris.

Information on Firmware

This is information for customers of Fujitsu.

Obtain the firmware for your server from the following sources.

- Global site
Please contact your local support provider to obtain the files required for a firmware update.
- Japanese site

Customers who subscribed to SupportDesk can obtain the firmware from the SupportDesk-Web.

The following files are provided.

- Firmware program file (XSCF Control Package (XCP) file)
- XSCF extended MIB (XSCF-SP-MIB) definition file

Note - XSCF Control Package (XCP): XCP is a packaged control program of the hardware that configures SPARC M10 Systems. An XCP file includes the XSCF firmware, OpenBoot PROM firmware, Power-On Self-Test firmware, and Hypervisor firmware.

How to Use This Document

This document supports all models of SPARC M10 Systems. Depending on the server you use, read the related items listed in the following table.

Chapter Title in This Document	SPARC M10-1	SPARC M10-4	SPARC M10-4S
Chapter 1 Software Requirements	x	x	x
Chapter 2 XCP 2352-Related Information	x	x	x
Chapter 3 Information on Software	x	x	x
Chapter 4 Information on SPARC M10-1 Hardware	x		
Chapter 5 Information on SPARC M10-4 Hardware		x	
Chapter 6 Information on SPARC M10-4S Hardware			x
Chapter 7 Information on the PCI Expansion Unit	x (when introduced)	x (when introduced)	x (when introduced)
Chapter 8 Information on SPARC M10 Manuals	x	x	x

The information about firmware and software described in [Chapter 1](#), [Chapter 2](#), and [Chapter 3](#) is common to all models. Some of the information are only related to a specific model. In such a case, the applicable model name is indicated.

Information about hardware devices is provided separately in [Chapter 4](#), [Chapter 5](#), and [Chapter 6](#) for each model. The contents common to all models are described in all chapters describing hardware. Therefore, when you check the hardware information about multiple models, you will find that some contents are duplicated in some chapters.

Check the information about the PC expansion unit ([Chapter 7](#)) when installing the PCI expansion unit.

Descriptions of document revisions are provided ([Chapter 8](#)) for each document. Check whether the contents of your document have been revised.

Related Documentation

All documents for your server are available online at the following locations.

- Sun Oracle software-related documents (Oracle Solaris, etc.)
<http://docs.oracle.com/en/>
- Fujitsu documents
Global site
<http://www.fujitsu.com/global/products/computing/servers/unix/sparc/downloads/manuals/>
Japanese site
<http://www.fujitsu.com/jp/products/computing/servers/unix/sparc/downloads/manual/>

The following table lists documents related to SPARC M10 Systems.

Documentation Related to the SPARC M10

Manual Names (*1)

Fujitsu M10/SPARC M10 Systems Product Notes

*Fujitsu M10/SPARC M10 Systems Getting Started Guide (*2)*

Fujitsu M10/SPARC M10 Systems Quick Guide

*Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Important Legal and Safety Information (*2)*

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Safety and Compliance Guide

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Security Guide

Software License Conditions for Fujitsu SPARC M12 and Fujitsu M10/SPARC M10

Fujitsu SPARC Servers/SPARC Enterprise/PRIMEQUEST Common Installation Planning Manual

Fujitsu M10-1/SPARC M10-1 Installation Guide

Fujitsu M10-4/SPARC M10-4 Installation Guide

Fujitsu M10-4S/SPARC M10-4S Installation Guide

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Domain Configuration Guide

Fujitsu M10-1/SPARC M10-1 Service Manual

Fujitsu M10-4/Fujitsu M10-4S/SPARC M10-4/SPARC M10-4S Service Manual

Crossbar Box for Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Service Manual

PCI Expansion Unit for Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Service Manual

Fujitsu M10/SPARC M10 Systems PCI Card Installation Guide

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual

*Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 RCIL User Guide (*3)*

Manual Names (*1)

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF MIB and Trap Lists

Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Glossary

External USB-DVD Drive user guide

*1 The listed manuals are subject to change without notice.

*2 Printed manuals are provided with the product.

*3 This document applies specifically to the SPARC M12/M10 and FUJITSU ETERNUS disk storage system.

Notes on Safety

Read the following documents thoroughly before using or handling the SPARC M10 Systems.

- *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Important Legal and Safety Information*
- *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Safety and Compliance Guide*

Text Conventions

This manual uses the following fonts and symbols to express specific types of information.

Font/Symbol	Meaning	Example
AaBbCc123	What you type, when contrasted with on-screen computer output. This font is used to indicate an example of command input.	XSCF> adduser jsmith
AaBbCc123	The names of commands, files, and directories; on-screen computer output. This font is used to indicate an example of command output in the frame.	XSCF> showuser -P User Name: jsmith Privileges: useradm auditadm
<i>Italic</i>	Indicates the name of a reference manual.	See the <i>Fujitsu M10-1/SPARC M10-1 Installation Guide</i> .
" "	Indicates the names of chapters, sections, items, buttons, or menus.	See "Chapter 2 Network Connection."

Command Syntax in the Text

While the XSCF commands have a section number of (8) or (1), it is omitted from the text.

For details on the commands, see the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual*.

Syntax of the Command-Line Interface (CLI)

The command syntax is as follows:

- A variable that requires the input of a value is in *Italics*.
 - An optional element is enclosed in [].
 - A group of options for an optional keyword is enclosed in [] and delimited by |.
-

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- Global site
<http://www.fujitsu.com/global/contact/>
- Japanese site
<http://www.fujitsu.com/jp/products/computing/servers/unix/sparc/contact/>

Chapter 1

Software Requirements

This chapter describes the software requirements for using SPARC M10 Systems.

- [XCP/Oracle Solaris and Required SRUs/Patches](#)
- [How to Obtain XCP and Oracle Solaris SRUs/Patches/Oracle VM Server for SPARC](#)
- [Web Browser](#)
- [Software Supporting Remote Storage](#)
- [Existing XCP Firmware Versions and Support Information](#)

XCP/Oracle Solaris and Required SRUs/Patches

The following table is a list about XCP, Oracle Solaris, and required SRUs/patches supported on SPARC M10 systems.

Table 1-1 XCP and Oracle Solaris Supported by SPARC M10 Systems

Server	XCP	Oracle Solaris
SPARC M10-1		
SPARC64 X+ 3.7 GHz	2250 or later	See "Table 1-2 Oracle Solaris and Required SRUs/Patches Supported by SPARC M10 Systems."
SPARC64 X+ 3.2 GHz	2210 or later	
SPARC64 X+ 2.8 GHz	2290 or later	
SPARC64 X 2.8 GHz	2012 or later	
SPARC M10-4		
SPARC64 X+ 3.7 GHz	2250 or later	See "Table 1-2 Oracle Solaris and Required SRUs/Patches Supported by SPARC M10 Systems."
SPARC64 X+ 3.4 GHz	2210 or later	
SPARC64 X 2.8 GHz	2012 or later	
SPARC M10-4S (Direct inter-chassis connection)		
SPARC64 X+ 3.7 GHz	2210 or later	See "Table 1-2 Oracle Solaris and Required SRUs/Patches Supported by SPARC M10 Systems."
SPARC64 X 3.0 GHz	2031 or later	
SPARC M10-4S (Connection through crossbar box)		
SPARC64 X+ 3.7 GHz	2210 or later	See "Table 1-2 Oracle Solaris and Required SRUs/Patches Supported by SPARC M10 Systems."
SPARC64 X 3.0 GHz	2043 or later	

Table 1-2 Oracle Solaris and Required SRUs/Patches Supported by SPARC M10 Systems

Oracle Solaris	Required Package (*4) Required Product (*5)	Required SRU (*4) Required Patch (*5)	Supported Configuration
Oracle Solaris 11.2 or later	system/ldoms(*1) system/ldoms/ ldomsmanager(*2)	None	Control domain, guest domain, and nonvirtualized configurations
Oracle Solaris 11.1	system/ldoms(*1) system/ldoms/ ldomsmanager(*2)	SRU 1.4 or later(*3)	Control domain, guest domain, and nonvirtualized configurations
Oracle Solaris 10 1/13 (*6)(*7)	Oracle VM Server for SPARC 3.0 or later (*3)(*8)(*9)	150310-03 or later	Control domain, guest domain, and nonvirtualized configurations

Table 1-2 Oracle Solaris and Required SRUs/Patches Supported by SPARC M10 Systems (*continued*)

Oracle Solaris	Required Package (*4) Required Product (*5)	Required SRU (*4) Required Patch (*5)	Supported Configuration
Oracle Solaris 10 8/11 (*7)	-	Oracle Solaris 10 1/13 SPARC Bundle 150310-03 or later	Guest domain (*10)(*11)
Oracle Solaris 10 9/10 (*7)	-	Oracle Solaris 10 1/13 SPARC Bundle 150310-03 or later	Guest domain (*10)(*11)

*1 Required for all domains. It is included in group/system/solaris-large-server and group/system/solaris-small-server.

*2 Required for the control domain and the nonvirtualized configurations. It is included in group/system/solaris-large-server and group/system/solaris-small-server.

*3 Required for the control domain and the nonvirtualized configurations.

*4 For Oracle Solaris 11.

*5 For Oracle Solaris 10.

*6 If Oracle Solaris 10 1/13 is to be run in the control domain, the CPUs that can be assigned to the control domain are those CPUs that are mounted on logical system boards with LSB numbers 0 to 7.

*7 There are no LSB number limits on the CPUs that can be assigned to a guest domain. The maximum number of CPUs (vcpus) in an Oracle Solaris 10 guest domain must not be greater than 1024.

*8 When using the SPARC M10-4S, see "Notes When Using a Version Older Than Oracle VM Server for SPARC 3.1.0.1."

*9 When using the SPARC M10-4S, see "Notes on Using Oracle Solaris 10."

*10 An additional procedure must be executed before installation of Oracle Solaris 10 9/10 or Oracle Solaris 10 8/11 on a guest domain. For details, see the procedure in "Notes on Installing Oracle Solaris 10 on a Guest Domain."

*11 Only virtual I/O devices are supported.

Notes on Installing Oracle Solaris 10 on a Guest Domain

You may encounter Bug 7167998 ("inter-cpu-latency" MD property causes Solaris guest-domain panic) when installing Oracle Solaris 10 9/10 or Oracle Solaris 10 8/11 on a SPARC M10 guest domain. To avoid the bug, use network installation and the following procedure to install Oracle Solaris 10 9/10 or Oracle Solaris 10 8/11.

Note - Before applying the Oracle Solaris 10 1/13 SPARC Patch Bundle, do not change the configuration or OpenBoot PROM environment variables of the guest domain.

1. Mount boot_archive of Oracle Solaris 10 9/10 or Oracle Solaris 10 8/11.

```
# chmod -t
install-server-path/install-dir-path/Solaris_10/Tools/Boot/
platform/sun4v/boot_archive

# lofiadm -a
install-server-path/install-dir-path/Solaris_10/Tools/Boot/platform/
sun4v/boot_archive

Device name will be output. For example:
/dev/lofi/1
```

```
# mount /dev/lofi/1 /mnt
```

2. **Edit and add the following line to the `/mnt/etc/system` file.**

```
set xc_tick_limit = 0x1
```

3. **Unmount `boot_archive`.**

```
# umount /mnt  
  
# lofiadm -d /dev/lofi/1  
  
# chmod +t  
install-server-path/install-dir-path/Solaris_10/Tools/Boot/platform/sun4v/boot_archive
```

4. **Install Oracle Solaris 10 9/10 or Oracle Solaris 10 8/11 through network installation.**
5. **Apply the Oracle Solaris 10 1/13 SPARC Patch Bundle, and then install patch 150310-03 or later.**

Notes on Using Oracle Solaris 10

- [SPARC M10-4S]
When the control domain is running on Oracle Solaris 10 with Oracle VM Server for SPARC version 3.1.0.1 or older, if the physical partition dynamic reconfiguration (PPAR DR) feature is enabled, the `ldoms/ldmd` service fails to start and Oracle VM for SPARC does not work.

The PPAR DR feature is enabled at the time of shipment. Therefore, either disable the PPAR DR feature before setting up logical domains or update Oracle VM Server for SPARC to version 3.1.0.1 or later.

Configuration of the PPAR DR feature is performed with the `setpparmode(8)` command of XSCF firmware. For details on the `setpparmode(8)` command, see the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual*.

To update the Oracle VM Server for SPARC version to 3.1.0.1, apply patch 150817-01 after installing Oracle VM Server for SPARC 3.1.
- [SPARC M10-4S]
When Oracle Solaris 10 is running on the control domain, in order to perform dynamic reconfiguration of physical partitions (PPAR DR) with the `deleteboard(8)` and `addboard(8)` commands, update to Oracle VM Server for SPARC 3.1.1 or later. When updating to 3.1.1, apply patch 150817-02 after installing Oracle VM Server for SPARC 3.1.

Notes When Using a Version Older Than Oracle VM Server for SPARC 3.1.0.1

[SPARC M10-4S]

When the physical partition dynamic reconfiguration (PPAR DR) feature is enabled on an Oracle VM Server for SPARC version that is older than 3.1.0.1, the ldoms/ldmd service fails to start and thus Oracle VM Server for SPARC does not function.

If using an Oracle VM Server for SPARC version that is older than 3.1.0.1, disable the PPAR DR feature before setting up logical domains.

Configuration of the PPAR DR feature is performed with the `setpparmode(8)` command of XSCF firmware. For details on the `setpparmode(8)` command, see the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual*.

Meanwhile, in the case of Oracle Solaris 11, Oracle VM Server for SPARC 3.1.0.1 is included in SRU 11.1.14.5.0.

Required XCP/Oracle Solaris and Required SRUs/Patches for Enabling Dynamic Reconfiguration of Physical Partitions

The following table is a list about XCP, Oracle Solaris, and required SRUs/patches for enabling dynamic reconfiguration of physical partitions.

Table 1-3 XCP, Oracle Solaris, and Required SRUs/Patches Needed for Physical Partition Dynamic Reconfiguration

Server	XCP	Oracle Solaris	Required Package Required Product	Required SRU Required Patch	Supported Configuration
SPARC M10-4S	2220 or later	Oracle Solaris 11.2 or later	system/ldoms (*1) system/ldoms/ldomsmanager (*2)	None	Control domain, guest domain, and nonvirtualized configurations
		Oracle Solaris 11.1	system/ldoms (*1) system/ldoms/ldomsmanager (*2)	SRU 11.1.14.5.0 or later(*3)	Control domain, guest domain, and nonvirtualized configurations
		Oracle Solaris 10 1/13	Oracle VM Server for SPARC 3.1(*4)	150817-02 or later (*4)(*5)	Control domain, guest domain, and nonvirtualized configurations
		Oracle Solaris 10 9/10	-	Oracle Solaris 10 1/13 SPARC Bundle 150310-03 or later	Guest domain (*6)(*7)

Table 1-3 XCP, Oracle Solaris, and Required SRUs/Patches Needed for Physical Partition Dynamic Reconfiguration (*continued*)

Server	XCP	Oracle Solaris	Required Package Required Product	Required SRU Required Patch	Supported Configuration
		Oracle Solaris 10 8/11	-	Oracle Solaris 10 1/13 SPARC Bundle 150310-03 or later	Guest domain (*6)(*7)

*1 Required for the control domain and the guest domain. It is included in group/system/solaris-large-server and group/system/solaris-small-server.

*2 Required only for the control domain. It is included in group/system/solaris-large-server and group/system/solaris-small-server.

*3 Required for the control domain and the guest domain. Though SRU 11.1.14.5.0 includes Oracle VM Server for SPARC 3.1.0.1, the modification for Solaris 11.1 to fix BugID#17709858 is required to ensure stable DR functionality. This issue has been resolved in SRU 11.1.15.4.0 and later.

*4 Required only for the control domain.

*5 Do not apply patch 150400-01 to 150400-06.

*6 An additional procedure must be executed before installation of Oracle Solaris 10 9/10 or Oracle Solaris 10 8/11 on a guest domain. For details, see the procedure in "Notes on Installing Oracle Solaris 10 on a Guest Domain."

*7 Only virtual I/O devices are supported.

Required XCP/Oracle Solaris and Required SRUs/Patches for Enabling Dynamic Reconfiguration of PCIe Endpoint Devices

The following table is a list about XCP, Oracle Solaris, and required SRUs/patches for enabling dynamic reconfiguration of PCIe endpoint devices.

Table 1-4 XCP, Oracle Solaris, and SRUs/Patches Needed for Dynamic Reconfiguration of PCIe Endpoint Devices

Server	XCP	Oracle Solaris	Required Package Required Product	Required SRU Required Patch
SPARC M10-1 SPARC M10-4 SPARC M10-4S	2230 or later	Oracle Solaris 11.3 or later	system/ldoms(*1) system/ldoms/ldomsmanager(*2)	None
		Oracle Solaris 11.2	system/ldoms(*1) system/ldoms/ldomsmanager(*2)	SRU 11.2.2.5.0 or later
		Oracle Solaris 11.1(*4)	system/ldoms(*1)	SRU 11.1.17.5.0 or later(*3)
		Oracle Solaris 10 1/13	Oracle VM for SPARC 3.1(*5)(*6)	150817-03 or later(*5)

*1 Required for the control domain and other domains. It is included in group/system/solaris-large-server and group/system/solaris-small-server.

*2 Required only for the control domain. It is included in group/system/solaris-large-server and group/system/solaris-small-server.

*3 Required for the control domain and other domains.

*4 Can be used only in domains other than the control domain.

*5 Required only for the control domain.

*6 There are patches required other than the Oracle VM Server for SPARC patch. For details, see "Required Oracle Solaris OS Versions for Oracle VM Server for SPARC 3.1.1.1" in the *Oracle VM Server for SPARC 3.1.1.1, 3.1.1, and 3.1 Release Notes*.

Software Supporting Software on Chip

The Software on Chip on the SPARC64 X+/SPARC64 X processor can be used with the software below.

When Used With Oracle Database

Table 1-5 Software Supporting Software on Chip (When Used With Oracle Database)

Function	Operating Environment
The decimal floating-point arithmetic processing (Oracle Number)	Oracle Database 12c or later Oracle Solaris 11.1 or later
SIMD (In-memory processing)	Oracle Database 12c Enterprise Edition Oracle Database In Memory Oracle Solaris 11.1 or later
Cryptographic processing	Oracle Database 12c Enterprise Edition Oracle Advanced Security Oracle Solaris 11.1 or later

When Used With a User Application

Table 1-6 Software Supporting Software on Chip (When Used With a User Application)

Function	Operating Environment
The decimal floating-point arithmetic processing (IEEE 754)	Oracle Solaris Studio 12.4 or later Oracle Solaris 11.2 SRU 11.2.4.6.0 or later
SIMD	Oracle Solaris Studio 12.4 or later Oracle Solaris 11.2 SRU 11.2.4.6.0 or later

How to Obtain XCP and Oracle Solaris SRUs/Patches/Oracle VM Server for SPARC

Obtain the latest XCP firmware and Oracle Solaris SRUs/patches/Oracle VM Server for SPARC from the following sources.

- Global site
Please contact your local support provider to obtain the files required for the

update.

- Japanese site
Customers who subscribed to SupportDesk can obtain the files from the SupportDesk-Web.

Web Browser

[Table 1-7](#) lists web browsers for which compatibility with XSCF Web operation has been confirmed. For other information about XSCF Web, see "[Notes and Restrictions on XSCF Web](#)."

Table 1-7 Versions of Web Browsers Where Operation is Already Confirmed

Web Browser	Version
Microsoft Internet Explorer	8.0, 9.0, 10.0, and 11.0
Mozilla Firefox, 32-bit version	10.0 to ESR 60

Software Supporting Remote Storage

[Table 1-8](#) is a list about XCP, Oracle Solaris, and required SRUs/patches for using remote storage.

Table 1-8 XCP, Oracle Solaris, and Required SRUs/Patches for Enabling Remote Storage

XCP	Oracle Solaris	Required SRU(*1) Required Patch(*2)
2260 or later	Oracle Solaris 11.2 or later	None
	Oracle Solaris 11.1	SRU 2.5 or later(*3)
	Oracle Solaris 10 1/13	None

*1 For Oracle Solaris 11.

*2 For Oracle Solaris 10.

*3 To assign remote storage as a virtual disk to a guest domain, this SRU must be applied to the service domain.

[Table 1-9](#) lists the Windows OS operating environments on terminals that support remote storage.

Table 1-9 Operating Environments on Terminals

Type	Operating Environment
Windows OS	Windows Vista, Windows 7, Windows 8, Windows 8.1 Windows 10

Also, a Java Runtime Environment appropriate to the browser used with XSCF Web must be installed on the terminal. [Table 1-10](#) lists combinations of browsers and Java Runtime Environments.

Table 1-10 Combinations of Browsers Used With XSCF Web and Java Runtime Environments

Browser	Java Runtime Environment
Microsoft Internet Explorer 8 and 9, 32-bit version	Java Runtime Environment 8, 32-bit version
Microsoft Internet Explorer 8 and 9, 64-bit version	Java Runtime Environment 8, 64-bit version
Microsoft Internet Explorer 10 and 11	Java Runtime Environment 8, 32-bit version
Mozilla Firefox 10.0 to ESR 52, 32-bit version (*1)	Java Runtime Environment 8, 32-bit version

*1 Java plug-in support ended with ESR 60. This means you cannot use remote storage from ESR 60 or later. Use Internet Explorer.

Existing XCP Firmware Versions and Support Information

The following table lists the XCP firmware versions that have been released for the SPARC M10 systems, as well as the main support and update information for each version.

Table 1-11 Existing XCP Firmware Versions and Support Information

XCP Version	Main Support Information	CMU Version
XCP 2352	- Security fixes - Support of the sethwproperty(8) and showhwproperty(8) commands	02.35.0002
XCP 2351	Security fixes	02.35.0001
XCP 2350	Support of the function for checking the time before starting a physical partition	02.35.0000
XCP 2340	Improvement when the OpenBoot PROM security mode is enabled	02.34.0000

Table 1-11 Existing XCP Firmware Versions and Support Information (*continued*)

XCP Version	Main Support Information	CMU Version
XCP 2332	Improvement of XCP firmware defects	02.33.0000
XCP 2330	Support for CPU Activation Interim Permit in the SPARC M10-4S	02.33.0000
XCP 2322	Improvement of XCP firmware defects	02.32.0002
XCP 2321	Improvement of XCP firmware defects	02.32.0001
XCP 2320	- Support for CPU Activation Interim Permit - Support for the verified boot for the network boot - Improvement of logs when a file system error occurs	02.32.0000
XCP 2290	- Support for the SPARC64 X+ processor (2.8 GHz) in the SPARC M10-1 - Support for enabling/disabling the IPMI service when using the remote power management function - Support for the deferred dump function	02.29.0000
XCP 2280	- Support for live migration of Oracle Solaris kernel zones - Support for the verified boot of the guest domain	02.28.0000
XCP 2271	Support for Virtual SCSI Host Bus Adapter (vHBA) function	02.27.0001
XCP 2260	- Support for remote storage - Support for addition/deletion specification when configuring CPU Activation - Support for the function to reduce fan noise - Support for performance register access control feature provided by Oracle VM Server for SPARC 3.2	02.26.0000
XCP 2250	- Support for SPARC64 X+ processor (3.7 GHz) in SPARC M10-1/M10-4 - Support for the verified boot	02.25.0000
XCP 2240	- Support for the DNS round-robin for the host name of the NTP server - Support for 1984 LDC endpoints per guest domain - Support for Dynamic PCIe bus assignment provided with Oracle VM Server for SPARC 3.2	02.24.0000
XCP 2232	Security fixes	02.23.0000
XCP 2231	Security fixes	02.23.0000
XCP 2230	- Support for Internet Explorer 10 and 11 in XSCF Web - Support for Extensible Firmware Interface GUID Partition Table (EFI GPT) labels - Support for recovery mode provided by Oracle VM Server for SPARC 3.1 - Support for the function for dynamically reconfiguring PCIe endpoint devices through combination with Oracle VM Server for SPARC - Support for Oracle Solaris kernel zones	02.23.0000
XCP 2221	Improvement of XCP firmware defects	02.22.0000

Table 1-11 Existing XCP Firmware Versions and Support Information (*continued*)

XCP Version	Main Support Information	CMU Version
XCP 2220	<ul style="list-style-type: none">- Support of the dynamic reconfiguration (DR) function of the system board in a physical partition (PPAR)- Support for XSCF startup mode function (SPARC M10-1)	02.22.0000
XCP 2210	<ul style="list-style-type: none">- Support for SPARC64 X+ processors- Support for CPU operational mode, which specifies how a CPU should operate- Enable the factory default configuration for the feature of dynamic reconfiguration of physical partitions (PPAR DR).- The LDAP, Active Directory, LDAP over SSL features have been supported for the XSCF user account- The SR-IOV feature in combination with Oracle VM Server for SPARC is supported- The "no-io" feature when configuring physical partitions is supported	02.21.0000
XCP 2092	Improvement of XCP firmware defects	02.09.0000
XCP 2091	Improvement of XCP firmware defects	02.09.0000
XCP 2090	<ul style="list-style-type: none">- Support of 64 GB memory- Support of multiple specifications of the port and community string for the Trap host of the SNMP agent function- Support for automatic XCP firmware version synchronization between chassis when maintenance is done in the off state of the input power supply	02.09.0000
XCP 2080	Support of the Auto Service Request (ASR) function	02.08.0000
XCP 2070	Improvement of XCP firmware defects	02.07.0000
XCP 2052	<ul style="list-style-type: none">- Support of the AES algorithm encryption function for the SNMP agent function- Support of different memories in the chassis	02.05.0002
XCP 2051	Improvement of XCP firmware defects	02.05.0001
XCP 2050	Support of the direct I/O function for the PCI expansion unit (SPARC M10-4S)	02.05.0000
XCP 2044	Support of the direct I/O function for the PCI expansion unit (SPARC M10-1/M10-4)	02.04.0004
XCP 2042	Support of the crossbar box (configuration of up to 16 BB)	02.04.0001
XCP 2041	Support of the PCI expansion unit	02.04.0001
XCP 2032	Improvements related to the Oracle VM Server for SPARC software	02.03.0001
XCP 2031	Support of SPARC M10-4S (configuration of up to 4 BB)	02.03.0001
XCP 2013	Improvement of XCP firmware defects	02.01.0003
XCP 2012	Support of SPARC M10-1/M10-4	02.01.0002

Note - The XSCF firmware and CMU firmware versions in XCP can be checked with the `version -c xcp -v` command.

When the CMU firmware version is updated, turn off the power to the physical partition and then turn it on again to complete the CMU firmware update. For details on the firmware update, see "Chapter 16 Updating the XCP Firmware" in the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide*.

Note - The CMU firmware version may differ with the XCP version.

For example, in XCP 2042, the XSCF version is 02.04.0002, whereas the CMU version is 02.04.0001.

Chapter 2

XCP 2352-Related Information

This chapter provides XCP 2352-related information.

- [Latest Information on XCP 2352](#)
- [Notes and Restrictions](#)
- [XCP 2352 Problems and Workarounds](#)

Latest Information on XCP 2352

This section describes major changes in XCP 2352.

This XCP version modifies XSCF firmware and CMU firmware.

Note - After the firmware update completes, power-off/power-on of the physical partition may be required.

For details on the firmware update, see "Chapter 16 Updating the XCP Firmware" in the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide*.

- Security fixes. (RTIF2-180704-001)
- The `sethwproperty(8)` and `showhwproperty(8)` commands are now supported. For details on the `sethwproperty(8)` and `showhwproperty(8)` commands, see "[Chapter 8 Information on SPARC M10 Manuals](#)."

Notes and Restrictions

This section describes notes and restrictions that are known as of this release.

Notes on Secure Socket Layer (SSL) 3.0

For security reasons, you cannot use SSL 3.0 with XCP 2240 or later. Use Transport Layer Security (TLS) 1.2, 1.1, or 1.0.

Notes on Time Zones

- The time zones (regions/place names) supported by the XSCF can be changed to support the latest time zone information.
For any previously set time zone that the system cannot use anymore, the XSCF switches from this unusable time zone to Coordinated Universal Time (UTC) and operates in UTC.

When the set time zone operates in UTC, execute the `settimezone -c settz -a` command, and check the settable time zones. If the time zone list does not have a set time zone, set a time zone again.

- In a system that implements daylight saving time, suppose that the current time in the daylight saving time period is changed by the `setdate -s` command to a time that is within one hour of the ending of daylight saving time. Then, the set time will be outside of daylight saving time (that is, in winter time).
In the following example, an attempt is made to set the local time to 1:40 AM October 31 (JDT) during daylight saving time, and daylight saving time ends 2:00 AM October 31. The result is that the time is set to the winter time of 1:47 AM October 31 (JST).

```
XSCF> showdate
Fri Oct 31 01:20:00 JDT 2014
XSCF> setdate -y -s 103101402014.00
Fri Oct 31 01:40:00 JST 2014
The XSCF will be reset. Continue? [y|n] :y
Thu Oct 30 16:40:00 UTC 2014
:
Omitted (XSCF automatically rebooted)

XSCF> showdate
Fri Oct 31 01:47:16 JST 2014
```

To set a time within the period immediately before or after daylight saving time switches, execute the `setdate -u` command, and set the time in UTC.

Restrictions on Audit

- The log file transfer function of the audit log set with the setaudit archive command and the setaudit disable command is not supported at present.
- If the policy for writing to an audit record is set to stop temporarily ("suspend"), degradation due to an error may occur or the XSCF may be reset.
In the setaudit(8) command, specify overwriting ("count") by default for the write policy for the audit trail. For details on this problem, see "RTIF2-141208-001." From XCP 2250 onwards, specifying "suspend" invokes the same behavior as when "count" is specified.

Notes on Dynamic Reconfiguration of Physical Partitions

- Suppose that you are going to add or delete a system board (PSB), using dynamic reconfiguration of physical partitions. Before executing the addboard(8) or deleteboard(8) command, execute the showhardconf(8) command to confirm that [Status] of every XBBOX#xx and BB#xx is "Normal."
- Suppose that you execute dynamic reconfiguration of physical partitions in an environment where SR-IOV is used. If you assign a physical function (PF) to one of the domains in a physical partition and then assign a virtual function (VF) created from the assigned physical function (PF) to the same domain, the deleteboard(8) command may fail.
- In the case of dynamic reconfiguration of physical partitions, when adding an I/O device, it is necessary to either stop the domain or use the delayed reconfiguration feature to integrate the I/O device into the logical domain after executing the addboard(8) command. Moreover, when removing an I/O device, after removing the I/O device by stopping the domain or using the delayed reconfiguration feature, execute the deleteboard(8) command in order to disconnect the system board. For details on each procedure, see the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Domain Configuration Guide*.
Note that you can add and delete I/O devices without stopping the logical domains and without delayed reconfiguration on Oracle Solaris 11.2 SRU 11.2.8.4.0 or later.
- If you execute dynamic reconfiguration of physical partitions on Oracle Solaris 11.2 SRU 11.2.8.4.0 or earlier, the results of the prtdiag command executed from the control domain may differ from the actual physical configuration. You can update it to the latest information by executing svcadm restart picl.
- While adding or removing system boards with the addboard(8) or deleteboard(8) command of the DR feature, do not reboot all the XSCFs with either the rebootxscf -a command or from the XSCF Web interface. If such an operation is performed, not only will the DR processing terminate abnormally, but also Oracle Solaris on a running physical partition may hang, a hardware failure may occur, physical partitions may stop abnormally, and replacement of parts may become necessary.

- If even one logical domain in the OpenBoot PROM state is situated in the physical partition, dynamically reconfiguring the physical partition causes it to end with an error. Execute dynamic reconfiguration of the physical partition after changing the logical domain to any of the following states: state where Oracle Solaris is running, bound state, or inactive state.
- Do not specify either "unbind=resource" or "unbind=shutdown" at the -m option of the deleteboard(8) command when executing dynamic reconfiguration of physical partitions when the version of Oracle VM Server for SPARC is earlier than 3.2. It is possible that the logical domain will hang or the deleteboard(8) command will fail.
- Memory modules (DIMMs) of different capacities cannot be mounted on a chassis that is the target of dynamic reconfiguration of physical partitions. Make sure that the mounted memory modules (DIMMs) on chassis whose physical partition is the target of dynamic reconfiguration, are all of the same capacity. Meanwhile, there is no problem if the number of DIMMs differs from chassis to chassis.
- Do not apply patch 150400-01 to 150400-06 on Oracle Solaris 10. In such a case, dynamic reconfiguration of the physical partitions may cause a system panic (CR 17510986).

Notes on Mixing SPARC64 X+ Processors With SPARC64 X Processors

To mix SPARC64 X+ processors with SPARC64 X processors, configure each type as a unit of the system board configuration in units of the SPARC M10 system chassis. Inside each SPARC M10 system chassis, SPARC64 X+ processors cannot be mixed with SPARC64 X processors and mounted together. Within the SPARC M10-4/M10-4S, the system configuration includes the CPU memory unit lower (CMUL) and CPU memory unit upper (CMUU). These two units must have the same type of processor.

Notes and Restrictions on the SR-IOV Function

Notes

- Before maintenance on PCI Express (PCIe) cards that use the SR-IOV function is performed with either dynamic reconfiguration (DR) of physical partitions or PCI hot plug (PHP), perform the following procedure.
 1. **Remove all virtual functions (VF) from the I/O domains by executing the `ldm remove-io` command.**
 2. **Destroy all virtual functions (VF) by executing the `ldm destroy-vf` command.**

For the maintenance procedure using DR or PHP, see the *Fujitsu M10-4/Fujitsu M10-4S/SPARC M10-4/SPARC M10-4S Service Manual* or the *PCI Expansion Unit for Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Service Manual*.

For details on the `ldm` command, see the *Oracle VM Server for SPARC Administration Guide* for the version used.

After performing maintenance with DR or PHP, execute the following procedure, if necessary.

3. Create virtual functions (VF) by executing the `ldm create-vf` command.

4. Assign the virtual functions (VF) to the I/O domains by using the `ldm add-io` command.

- The on-board LAN interfaces of SPARC M10 systems support the SR-IOV feature.
- The static SR-IOV feature is supported from Oracle VM Server for SPARC 3.0 onwards.
- The dynamic SR-IOV feature is supported from Oracle VM Server for SPARC 3.0 onwards.
- For details on the SR-IOV function, see the *Oracle VM Server for SPARC Administration Guide* for the version used. For the necessary fixes when using the SR-IOV function, see the *Oracle VM Server for SPARC Release Notes* for the version used.
- For a list of PCI cards that support the SR-IOV feature, refer to the *Fujitsu M10/SPARC M10 Systems PCI Card Installation Guide*.

Notes on the OpenBoot PROM

- When security-mode in OpenBoot PROM environment variables is set to command or full, and at least 1 and no more than 8 viewable character strings are set for security-password, the OpenBoot PROM security mode is enabled and a password is required for executing commands or performing operations. For details, see "H.4 Behavior With the Security Mode Enabled" in the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide*.
- When the XCP firmware is updated to XCP 2260 or later, information for CD-ROM and remote storage is added to OpenBoot PROM device aliases. To use added device aliases when the system operates with a logical domain configuration other than factory-default, perform the following procedure in the control domain after updating the XCP firmware to XCP 2260 or later.

1. Confirm the logical domain configuration information saved to the XSCF.

The following example shows that two sets of configuration information (i.e., logical domain configuration information `ldm-set1`, which is currently in use, and `factory-default`) are stored.

```
# ldm list-spconfig
ldm-set1 [current]
factory-default
```

2. Execute the following commands on all of the logical domains, including the control domain.

Specify the name of the logical domain for <ldom>.

```
# ldm set-variable fix_foo=true <ldom>
# ldm remove-variable fix_foo <ldom>
```

3. Store the updated configuration information for the logical domain again.

Delete the stored ldm-set1 and then store the information again.

```
# ldm rm-sconfig ldm-set1
# ldm add-sconfig ldm-set1
```

4. Restart all logical domains.

- If you execute the sendbreak(8) command after the OpenBoot PROM banner appears but before OpenBoot PROM startup is completed, the following error message is output. In this case, the boot command cannot be executed.
FATAL: OpenBoot initialization sequence prematurely terminated.

In this case, set the OpenBoot PROM environment variable auto-boot? to false at the ok prompt, and then execute the reset-all command. When the OpenBoot PROM is restarted, set auto-boot? to true, and execute the boot command.
- When you use the setpparam(8) command of the XSCF firmware to set an OpenBoot PROM environment variable, such as nvramrc, the maximum number of characters that can be set is 254.
If you want to set a string of 255 or more characters in an OpenBoot PROM environment variable, such as nvramrc, do so in the OpenBoot PROM or Oracle Solaris environment. Note that, however, the maximum number of characters is 1024.
- The OpenBoot PROM device aliases disk and net are not created for Oracle VM Server for SPARC disks and logical domains to which no network is assigned. To execute disk boot or network boot by specifying the device alias disk or net, set the device aliases disk and net by using the OpenBoot PROM nvalias command.
- From XCP 2210 onward, at the time of starting up the OpenBoot PROM, the following message is output before the OpenBoot PROM banner is displayed.

```
NOTICE: Entering OpenBoot.
NOTICE: Fetching Guest MD from HV.
NOTICE: Starting additional cpus.
NOTICE: Initializing LDC services.
NOTICE: Probing PCI devices.
NOTICE: Finished PCI probing.
```

- Suppose the following: the OpenBoot PROM environment variable multipath-boot? is true; the boot -L command is executed to display boot environments (BEs) that, existing in the root pool, allow booting; and the number of a boot environment is selected from the displayed interactive menu. The system does not return to the ok prompt but restarts the OpenBoot PROM.
The number of restarts depends on the OpenBoot PROM environment variable boot-device. The OpenBoot PROM restarts as many times as the number of configured devices, after which the message "ERROR: All device paths in

boot-device have failed." is displayed and the ok prompt reappears.

To avoid this problem, set the environment variable `multipath-boot?` of OpenBoot PROM to false before executing the `boot -L` command or execute the `boot device_path -L` command.

- Suppose you execute the boot command with no arguments while the OpenBoot PROM environment variable "multipath-boot?" is set to "false" and all attempted booting from the devices specified in the OpenBoot PROM environment variable "boot-device" fails. The message output to the domain console in such a case is changed from XCP 2250 as follows.
 - Message prior to XCP 2250

```
{0} ok boot
Boot device: disk File and args:

Can't open boot device

ERROR: boot-read fail

{0} ok
```

Or

```
{0} ok boot
Boot device: disk File and args:

Can't locate boot device

ERROR: boot-read fail

{0} ok
```

- Message XCP 2250 or later

```
{0} ok boot
No viable default device found in boot-device variable.

{0} ok
```

Notes on Maintenance of a CPU Memory Unit, Motherboard Unit, XSCF Unit, PSU Backplane, or Crossbar Backplane Unit

- Note on correcting the time (1)
After the replacement of the FRU and the micro SD card, and after the system has been running for a long time, an XSCF reboot followed by PPAR startup may

cause the Oracle Solaris time to deviate.

This problem occurs when all of the following conditions are true:

- Both the FRU (CPU memory units, motherboard unit, XSCF unit, PSU backplane, or crossbar backplane unit) and the micro SD card are replaced.
- System: SPARC M10-1, SPARC M10-4, or SPARC M10-4S (single-chassis configuration)
- The setting specifying the XSCF as an NTP client is enabled.
- XCP 2240 or later

To avoid this problem, be sure to perform the following procedure after replacing the FRU and the micro SD card.

[Workaround]

1. **Execute the `showlogs event` command to check whether the XSCF time and NTP server time are successfully synchronized.**

If multiple event logs are registered, see the latest event log.

```
XSCF> showlogs event -rM
Date           Message
Mar 25 11:45:31 JST 2017  NTP service found NTP server to synchronize.
```

When "NTP service found NTP server to synchronize." appears, the time is correctly synchronized. Then, the following steps are not necessary.

2. **Execute the `setntp` command to temporarily disable the XSCF NTP client function.**

```
XSCF> setntp -s client -c disable
```

3. **Reboot the XSCF.**

```
XSCF> rebootxscf -a
```

4. **Execute the `setdate` command to set the XSCF time to the current time.**

In the following example, the XSCF time is set to 2017/3/25 12:00:00.

```
XSCF> setdate -s 032512002017.00
```

The XSCF automatically reboots when the time is set.

5. **Execute the `setntp` command to re-enable the XSCF NTP client function.**

```
XSCF> setntp -s client -c enable
```

6. **Reboot the XSCF.**

```
XSCF> rebootxscf -a
```

Note - Even if the XSCF time and NTP server time fail to synchronize and the following message appears, this workaround has been completed.

```
XSCF> showlogs event -rM
```

```
Date                Message
Mar 25 12:15:31 JST 2017  NTP service failed to reach appropriate NTP server.
```

- Note on correcting the time (2)
Due to the problem reported in RTIF2-140711-001 and RTIF2-141204-001, the Oracle Solaris time may deviate at PPAR startup.

To avoid this problem, be sure to perform the following procedure after maintenance.

Note - Before performing the procedure in "Note on correcting the time (2)," check whether the conditions in "Note on correcting the time (1)" are met. If they are met, first perform the procedure in "Note on correcting the time (1)."

[Workaround]

1. Execute the showdateinfo(8) command to check the times of the control domain and guest domain.

- For XCP 2350 or later
Check the times of the control domain and guest domain.
If the times are correct, the subsequent steps are unnecessary.
If the times are not correct, go to step 2.
- For XCP 2340 or earlier
Go to step 2 because it is not possible to check the times with the showdateinfo (8) command.

Note - The times of the control domain and guest domain in the XSCF time zone are displayed.

Example: Physical partition is PPAR#0

```
XSCF> showdateinfo -p 0
PPAR-ID      : 0
config_name  : 10guest_config
XSCF information:
-----
XSCF
Date        : Aug 03 19:56:16 JST 2017
Logical domains information:
-----
primary
Date        : Aug 03 19:56:16 JST 2017
guest 0
Date        : Aug 03 19:56:16 JST 2017
```

```
guest 1
  Date   : Aug 03 19:56:16 JST 2017
guest 2
  Date   : Aug 03 19:56:16 JST 2017
XSCF>
```

2. **Specify false for the OpenBoot PROM environment variable auto-boot?, and execute the setpparparam(8) command to disable the autoboot function of the control domain. Also execute the setpparmode(8) command to disable startup of guest domains.**

Example: Physical partition is PPAR#0

```
XSCF> setpparparam -p 0 -s bootscript "setenv auto-boot? false" -y
XSCF> setpparmode -p 0 -m guestboot=off
```

3. **Execute the poweron(8) command to power on the target physical partition.**

```
XSCF> poweron -p 0 -y
```

4. **Execute showdomainstatus -p 0 -g primary, and confirm that "OpenBoot Running" appears under Status.**
5. **Execute the console(8) command to switch to the control domain console.**

```
XSCF> console -p 0 -y
:
Omitted
{0} ok
```

6. **Execute the boot -s command to start Oracle Solaris in single user mode.**

```
{0} ok boot -s
```

7. **Execute the date command of Oracle Solaris to set the control domain time.**
Example: Setting for 12:20:00 on March 25, 2017

```
primary# date 0325122017.00
```

Note - Execute the showdate(8) command to display the XSCF time, and confirm that the control domain time of the target physical partition is the same as the XSCF time.

8. **Switch to multi-user mode.**

```
primary# exit
```

9. **For a logical domain configuration, set the correct time for guest domains.**

For details on how to check for a logical domain configuration, see "Checking between a logical domain configuration and the factory-default configuration." Perform the following procedure on every guest domain to set the time.

a. Check the CONS value of the guest domain.

```
primary# ldm list-domain
NAME          STATE      FLAGS    CONS   VCPU  MEMORY  UTIL  NORM  UPTIME
primary       active    -n-cv-   SP     8     3968M   0.2%  0.1%  18h 37m
guest         bound     ------ 5000   8     2G
```

b. Disable the autoboot function of the guest domain.

```
primary# ldm set-variable auto-boot?=false guest
```

c. Start the guest domain.

```
primary# ldm start-domain guest
```

d. Open the guest domain console.

```
primary# telnet localhost 5000
:
{0} ok
```

e. Execute the boot -s command to start Oracle Solaris in single user mode.

```
{0} ok boot -s
```

f. Execute the date command of Oracle Solaris to set the guest domain time.
Example: Setting for 12:30:00 on March 25, 2017

```
guest# date 0325123017.00
```

g. Switch to multi-user mode.

```
guest# exit
```

h. If the guest domain is an NTP client, execute the ntpq -np command, and confirm synchronization with the NTP server.

The asterisk ("*") at the beginning of a line below indicates that the NTP server in the "remote" column is synchronized with the guest domain.

```
guest# while [ 1 ]; do ntpq -np; sleep 60; done
remote          refid      st t when poll reach  delay  offset  jitter
=====
*203.0.113.156  203.0.113.70  4 u  46  64   3   0.458  0.754  7.243
remote          refid      st t when poll reach  delay  offset  jitter
=====
```

```
*203.0.113.156  203.0.113.70    4 u   39   64   7   0.478  1.487  5.664
      remote      refid      st t when poll reach  delay  offset  jitter
=====
*203.0.113.156  203.0.113.70    4 u   35   64  17   0.450  2.164  5.077
^C
guest#
```

i. If necessary, re-enable the autoboot function of the guest domain.

```
primary# ldm set-variable auto-boot?=true guest
```

10. If necessary, re-enable the functions that were disabled in step 1.

For the autoboot function of the control domain, specify true for the OpenBoot PROM environment variable auto-boot?, and execute the setpparparam(8) command to enable the function. For startup of guest domains, execute the setpparmode(8) command to re-enable it.

Example: Physical partition is PPAR#0

```
XSCF> setpparparam -p 0 -s bootscript "setenv auto-boot? true" -y
XSCF> setpparmode -p 0 -m guestboot=on
```

11. For a logical domain configuration, save logical domain configuration information.

For details on how to check for a logical domain configuration, see "Checking between a logical domain configuration and the factory-default configuration."

Example: Saving logical domain configuration information under the name of "config_A"

```
primary# ldm list-domain -l
Omitted
primary# ldm list-spconfig
Omitted
primary# ldm remove-spconfig config_A
primary# ldm add-spconfig config_A
```

Checking between a logical domain configuration and the factory-default configuration

You can check whether the configuration is a logical domain configuration by using the showdomainconfig command.

(1) Logical domain configuration

If a value other than factory-default is set below, as indicated by "^", it is a logical domain configuration.

```
XSCF> showdomainconfig -p 0
PPAR-ID      :0
Booting config
  (Current)  :config_A
             ^^^^^^^^
  (Next)     :config_A
```

```

                ^^^^^^^^^
-----
Index          :1
config_name    :factory-default
domains        :1
date_created   :-
-----
Index          :2
config_name    :config_A
domains        :1
date_created   :'2014-03-05 17:33:17'

```

(2) factory-default configuration

If factory-default is set below, as indicated by "^", it is not a logical domain configuration but in the factory-default configuration.

```

XSCF> showdomainconfig -p 0
PPAR-ID        :0
Booting config
  (Current)     :factory-default
                ^^^^^^^^^^^^^^^^^
  (Next)        :factory-default
                ^^^^^^^^^^^^^^^^^
-----
Index          :1
config_name    :factory-default
domains        :1
date_created   :-

```

- [SPARC M10-1/SPARC M10-4/SPARC M10-4S]

The setting information of the CPU Activation and the CPU Activation key may be deleted when the CPU memory unit lower (CMUL) or the motherboard unit (MBU) is replaced. To restore the CPU Activation setting information and the CPU Activation key, it is necessary to save the CPU Activation setting information and CPU Activation key beforehand, using the `dumpconfig(8)` command, and restore them with the `restoreconfig(8)` command.
- [SPARC M10-4S]

To replace the CPU memory unit lower (CMUL) or to add the SPARC M10-4S, execute the `replacefru(8)` or `addfru(8)` command and follow the maintenance menu.

Suppose that the system contains a crossbar box or consists of multiple SPARC M10-4S units and that the firmware is XCP 2044 or later. If the CPU memory unit lower (CMUL) is replaced or if the SPARC M10-4S is added without using the maintenance menu, the XSCF of the replaced/added device may not start.

Should this occur, turn off the input power to the replaced/added SPARC M10-4S. Subsequently, using the maintenance menu, replace the CPU memory unit lower (CMUL) or install the SPARC M10-4S again.
- [SPARC M10-4S]

From XCP 2090 onward, XCP firmware versions are automatically synchronized between chassis in the following situation: the CPU memory unit lower (CMUL)

or XSCF unit is replaced or the SPARC M10-4S or crossbar box is added without using the maintenance menu and with the input power turned off.

After replacement or expansion of components, if the "XSCF firmware update now in progress. BB#xx, please wait for XSCF firmware update complete." message is output after logging in to the master XSCF, the XCP firmware is in the process of being updated automatically.

Automatic synchronization of the XCP firmware versions can be confirmed by referring to the "XCP firmware version synchronization completed" message, which is output by the showlogs monitor, showlogs event, or showmonitorlog command.

Do not execute the following until the firmware update is complete.

- Turn off the input power supply
 - Execute the poweron(8) command
 - Execute the testsb(8) command
 - Execute the diagxbu(8) command
 - Execute the getflashimage(8) or the flashupdate(8) command
- If cold replacement of CPU memory unit lower (CMUL) or a motherboard unit (MBU) is done in the following procedure, the physical partitions (PPAR) may not start.
 1. Execute the "restoredefaults -c factory" command.
 2. After XSCF has stopped, turn off the input power of the system (AC OFF).
 3. Replace the CMUL or MBU without replacing the microSD card.
 4. Turn on the input power of the system.

[Workaround]

Replace CMUL or MBU in the following procedure.

1. Execute the "restoredefaults -c factory" command.
2. After you have stopped the XSCF in step 1, turn off/on the input power of the system (AC OFF/ON), and then confirm that the XSCF has started.
3. Replace the targeted CMUL or MBU.

[How to restore]

Restore with the following procedure:

1. Turn off the input power of the system (AC OFF).
2. Temporarily mount the microSD card that was mounted on the CMUL or MBU, when the "restoredefaults -c factory" command was executed, to the new unit.
3. Confirm that XSCF has started after turning on the input power (AC ON).
4. Turn off the input power of the system (AC OFF).
5. Extract the microSD card that was temporarily mounted and mount the original microSD card.
6. Turn on the input power of the system (AC ON).

Notes on CPU Activation

- If the XSCF setting information is initialized by executing the `restoredefaults(8)` command in XCP 2032 or earlier, the information of the CPU Activation key is also initialized.

Before executing the `restoredefaults(8)` command, save the CPU Activation key and then restore the information, or re-register the CPU Activation key.

In addition, when you replace a CPU memory unit or motherboard, the CPU Activation key may be erased. For details, see "[Notes on Maintenance of a CPU Memory Unit, Motherboard Unit, XSCF Unit, PSU Backplane, or Crossbar Backplane Unit.](#)"

- If you execute the `restoredefaults -c xscf` command on XCP 2041 or later, the CPU Activation key information is deleted, not only from the XSCF unit but also from the backup information on the XSCF. On the other hand, even if you execute the `restoredefaults -c factory` command, the information of the CPU Activation key is not deleted.

To initialize all settings to factory defaults, including the CPU Activation key information, use the `-c factory -r activation` option.

- XSCF setting information saved by the `dumpconfig(8)` command contains CPU Activation information and CPU Activation keys.

You can use the `restoreconfig(8)` command to restore the CPU Activation information and CPU Activation keys that were saved with the `dumpconfig(8)` command.

Therefore, if you configure CPU Activation or install a CPU Activation key when configuring settings for the XSCF, such as when configuring an XSCF network or physical partition (PPAR), we recommend that you first save the CPU Activation information and CPU Activation key by using the `dumpconfig(8)` command. To save and restore only CPU Activation keys, execute the `dumpcodactivation(8)` and `restorecodactivation(8)` commands, respectively. Note, however, that CPU Activation information cannot be saved and restored. Use the `showcod(8)` and `setcod(8)` commands to reconfigure CPU Activation.

Notes and Restrictions on XSCF Web

Notes

(1) Common to browsers

- When you import XCP or update the firmware by using XSCF Web, "Session is invalid" may appear on the web browser.
- If the timeout of the XSCF shell is short when you import XCP by using XSCF Web, XCP importing fails. Set the timeout of the XSCF shell to 30 minutes or longer.
Select the [menu] tab and select the [XSCF] - [Settings] - [Autologout] menu. Then, enter a value of 30 minutes or longer for [Time-out value].

- Suppose you select the [XSCF] - [Settings] - [CoD Reservation] menu on XSCF Web and change the setting value by clicking the [Reserve CoD Resources] button in the [PPAR Usage and Reservation] table. If you specify a value smaller than the current setting value, the following warning message dialog box appears for you to confirm that the correct value was entered.

PROC Permits assigned is less than current setting. Continue?

Click the [OK] button to continue the setting, and click the [Cancel] button to cancel the setting.

(2) **Internet Explorer**

If you use an XSCF Web version from XCP 2221 or earlier with Internet Explorer 10 or 11, you may see distorted screen layouts, such as disordered tables or superimposed characters. If you see a distorted XSCF Web screen layout in the above environment, use Firefox.

However, this problem does not affect system operation.

(3) **Firefox**

If you use XSCF Web in a Firefox environment, the browser may prompt you to save the login ID and password when you log in to the XSCF. In this case, do not save the login ID and password. If you save the login ID and password, the saved data may be displayed on LDAP, SMTP, and REMCS web pages.

Note - Remote Customer Support System (REMCS) is supported only for SPARC M10 systems sold within Japan by Fujitsu.

Configure either of the following settings to disable the login ID/password save function of the browser:

- Disable the login ID/password save function throughout the browser. Select [Tools] - [Options] - [Security] tab, and uncheck [Remember passwords for sites] in [Passwords].
- Specify the site as an exception to login ID and password saving. Select [Tools] - [Options] - [Security] tab, and check [Remember passwords for sites] in [Passwords]. Then, click the [Never Remember Password for This Site] button in the dialog box that appears for ID and password saving when you log in to the XSCF. This registers the address of the XSCF in the [Exceptions] list for ID and password saving, and the dialog box for ID and password saving does not appear at subsequent logins to the XSCF.

Restrictions

(1) **Common to browsers**

There are currently no known restrictions.

(2) **Internet Explorer**

If you use Internet Explorer 8 in a Windows 7 environment, built-in Administrator accounts cannot be used.

(3) **Firefox**

There are currently no known restrictions.

Notes on Firmware Update

- [SPARC M10-4S]
If you update the firmware by executing the flashupdate(8) command or using XSCF Web, the time for the processing depends on the number of SPARC M10-4S chassis or crossbar boxes that compose the system.
- The relationship of the master XSCF and the standby XSCF after the update of the XSCF firmware depends on the version of the updated XCP.
The following table shows the relationship between the master XSCF and the standby XSCF, as well as the firmware update operation for each XCP version.

Table 2-1 Difference Between Firmware Update in XCP 2050 or Later and in XCP 2044 or Earlier

XCP Version	Relationship Between Master XSCF and Standby XSCF	Operation During Firmware Update
XCP 2050 or later	The master XSCF and the standby XSCF that have been switched over during update will return to the status before the switchover.	<ol style="list-style-type: none">1. Execute the flashupdate(8) command from the master XSCF of BB#00.<ul style="list-style-type: none">-> The XSCF of BB#00 that executed the flashupdate(8) command is in the standby status immediately after an XSCF reset.-> XSCF automatic switchover is performed after the update has been completed.-> When XSCF automatic switchover has completed, the XSCF of BB#00 that executed the flashupdate(8) command is restored to the status of the original master.2. Confirm that the update has been completed by referring to the log message "XCP update has been completed." from the master XSCF of BB#00.

Table 2-1 Difference Between Firmware Update in XCP 2050 or Later and in XCP 2044 or Earlier (*continued*)

XCP Version	Relationship Between Master XSCF and Standby XSCF	Operation During Firmware Update
XCP 2044 or earlier	The master XSCF and the standby XSCF that have been switched over during update will stay switched over.	<ol style="list-style-type: none"> 1. Execute the flashupdate(8) command from the master XSCF of BB#00. -> The XSCF of BB#00 that executed the flashupdate(8) command is in the standby status immediately after an XSCF reset. -> XSCF automatic switchover is not performed after the update has been completed. For this reason, the XSCF of BB#00 that executed the flashupdate(8) command will remain in the standby status. 2. Confirm that the update has been completed by referring to the log message "XCP update has been completed." from the master XSCF of BB#01. 3. To restore the status of the master and standby XSCFs to the status before the update, execute the switchscf(8) command from the master XSCF of BB#01 to restore the XSCF of BB#00 to the master.

- When updating the firmware, errors described in "[Problems With XCP and Workarounds](#)" may occur. In such a case, take the actions described in workarounds, then update again.

Notes When a PCI Expansion Unit is Connected

- Even with the platadm privilege, you can use -c check and -c update in the ioadm(8) command in XCP 2260 or later. For details, see the man page for the ioadm(8) command or the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual*.
- [SPARC M10-1]
 If one of the following operations is performed, the logical domain configuration of the physical partition will return to the factory-default state at the next control domain start time. Also, the OpenBoot PROM environment variables of the control domain may be initialized.
 - Updating the firmware from XCP 2043 or earlier to XCP 2044 or later in a system connected to a PCI expansion unit
 - Adding/Removing a PCI expansion unit in a system to which the firmware XCP 2044 or later is applied

Before the operation, save the logical domain configuration information from Oracle Solaris to an XML file. Also, write down the setting information for the

OpenBoot PROM environment variables of the control domain in advance to set it again.

Table 2-2 indicates the information that may need to be saved/restored when updating the firmware from XCP 2043 or earlier to XCP 2044 or later in a system connected to a PCI expansion unit.

Table 2-2 Required Operations When Updating the Firmware From XCP 2043 or Earlier to XCP 2044 or Later

PCI Expansion Unit Connected	Current Domain Configuration	Rebuilding Oracle VM Server for SPARC Configuration	Setting OpenBoot PROM Environment Variable Again
No	factory-default (Control domain only)	Not required	Not required
No	With logical domains other than control domain	Not required	Not required
Yes	factory-default (Control domain only)	Not required	Not required
Yes	With logical domains other than control domain	Required (XML file)	Required

Table 2-3 indicates the information that may need to be saved/restored when adding/removing a PCI expansion unit in a system to which the firmware XCP 2044 or later is applied.

Table 2-3 Required Operations When Adding/Removing a PCI Expansion Unit in a System to Which the Firmware XCP 2044 or Later is Applied

PCI Expansion Unit Connected	Current Domain Configuration	Rebuilding Oracle VM Server for SPARC Configuration	Setting OpenBoot PROM Environment Variable Again
No (adding)	factory-default (Control domain only)	Not required	Not required
No (adding)	With logical domains other than control domain	Required (XML file)	Required (*1)
Yes (adding/removing)	factory-default (Control domain only)	Not required	Not required
Yes (adding/removing)	With logical domains other than control domain	Required (XML file)	Required (*1)

*1 Not required for XCP 2230 or later.

Note - Execute the `ldm list-constraints -x` command to save to an XML file, and then execute the `ldm init-system -i` command to restore from an XML file. To display the OpenBoot PROM environment variables, execute the `printenv` command from the `ok` prompt. For details on this procedure, see "1.7.3 How to Save/Restore the Logical Domain Configuration Information and the OpenBoot PROM Environment Variable" in the *PCI Expansion Unit for Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Service Manual*.

■ [SPARC M10-4/M10-4S]

Suppose that one of the following operations is performed with the `setpciboxdio(8)` command using the following firmware: XCP 2044 or later on the SPARC M10-4, or XCP 2050 or later on the SPARC M10-4S. Then, the logical domain configuration of the physical partition will return to the factory-default state at the next control domain start time. Also, the OpenBoot PROM environment variables of the control domain may be initialized.

- Changing the enable/disable setting of the direct I/O function for the PCI expansion unit
- Adding/Removing/Replacing a PCI expansion unit in a PCI slot of a SPARC M10 system chassis where the direct I/O function for the PCI expansion unit is enabled

You can execute the `setpciboxdio(8)` command regardless of whether there is a PCI expansion unit. Before doing so, save the logical domain configuration information from Oracle Solaris to an XML file. Also, write down the setting information for the OpenBoot PROM environment variables of the control domain to set it again.

The information may need to be saved/restored when changing the enable/disable setting of the direct I/O function for the PCI expansion unit by executing the `setpciboxdio(8)` command. See [Table 2-4](#) to check required operations.

Table 2-4 Required Operations When Toggling the Enable/Disable Setting of the Direct I/O Function

PCI Expansion Unit Configured	Current Domain Configuration	Rebuilding Oracle VM Server for SPARC Configuration	Setting OpenBoot PROM Environment Variable Again
No	factory-default (Control domain only)	Not required	Not required
No	With logical domains other than control domain	Required (XML file)	Required (*1)
Yes	factory-default (Control domain only)	Not required	Not required
Yes	With logical domains other than control domain	Required (XML file)	Required (*1)

*1 Not required for XCP 2230 or later.

[Table 2-5](#) indicates the information that may need to be saved/restored when

adding/removing/replacing a PCI expansion unit in a PCI slot of a SPARC M10 system chassis where the direct I/O function for the PCI expansion unit is enabled. Here, the `setpciboxdio(8)` command has been executed to enable the function.

Note - For PCI expansion unit maintenance using the PCI hot plug (PHP) function, the direct I/O function is disabled, so the above information does not need to be saved/restored.

Table 2-5 Required Operations for the Addition/Removal/Replacement of a PCI Expansion Unit in a PCI Slot of a SPARC M10 System Chassis Where the Direct I/O Function is Enabled

Maintenance Environment	Current Domain Configuration	Rebuilding Oracle VM Server for SPARC Configuration	Setting OpenBoot PROM Environment Variable Again
Addition/Removal with PPAR stopped	factory-default (Control domain only)	Not required	Not required
	With logical domains other than control domain	Required (XML file)	Required (*2)
Replacement of faulty PCI expansion unit (*1) with PPAR stopped	factory-default (Control domain only)	Not required	Not required
	With logical domains other than control domain	Required (XML file)	Required (*2)
Replacement of normal PCI expansion unit (*1) with PPAR stopped	factory-default (Control domain only)	Not required	Not required
	With logical domains other than control domain	Not required	Not required

*1 This also includes the replacement of a link card, link cable, management cable, and link board.

*2 Not required for XCP 2230 or later.

Note - Execute the `ldm list-constraints -x` command to save to an XML file, and then execute the `ldm init-system -i` command to restore from an XML file. To display the OpenBoot PROM environment variables, execute the `printenv` command from the `ok` prompt. For details on this procedure, see "1.7.3 How to Save/Restore the Logical Domain Configuration Information and the OpenBoot PROM Environment Variable" in the *PCI Expansion Unit for Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Service Manual*.

Notes on Dual Power Feed Setting

The power supply unit of the SPARC M10 system is redundantly configured. Enabling or disabling the dual power feed function with the `setdualpowerfeed(8)` command does not affect the behavior of a redundantly configured system. Therefore, when the display results of the `showdualpowerfeed(8)` and `showhardconf(8)` commands, which are dependent on the setting of the `setdualpowerfeed(8)` command, also fall under any of the following conditions, the behavior of the redundantly configured system is not affected.

- The `showhardconf(8)` command displays "Power_Supply_System: Dual;" when the `showdualpowerfeed(8)` command displays "Dual power feed is enabled."
- The `showhardconf(8)` command displays "Power_Supply_System: Single;" when the `showdualpowerfeed(8)` command displays "Dual power feed is disabled."

The system administrator can use this setting function as a memo for determining whether the power supply unit has a dual power feed configuration.

Notes on Active Directory

- If Active Directory is enabled and you try login via telnet, inquiries to the second and subsequent alternative servers may time out, causing the login to fail.
- If the value set by the timeout operand of the `setad(8)` command is small, and you log in to the XSCF, the user privilege may not be assigned to you. In this case, increase the timeout setting value and try again.

Notes on LDAP over SSL

If the value set by the timeout operand of the `setldapssl(8)` command is small, and you log in to the XSCF, the user privilege may not be assigned to you. In this case, increase the timeout setting value and try again.

Notes on the Logical Domain Time

- Notes on the logical domain time (1)
After the firmware update from XCP 2232 or earlier to XCP 2240 or later, and after the system has been running for a long time, an XSCF reboot followed by PPAR startup may cause the Oracle Solaris time to deviate.

To avoid this problem, be sure to perform the following procedure.

[Workaround]

- If the setting specifying the XSCF as an NTP client is disabled (default)
Execute the `setdate` command to set the XSCF time to the current time.

In the following example, the XSCF time is set to 2017/3/25 12:00:00.

```
XSCF> setdate -s 032512002017.00
```

The XSCF automatically reboots when the time is set.

- If the setting specifying the XSCF as an NTP client is enabled

1. **Execute the showlogs event command to check whether the XSCF time and NTP server time are successfully synchronized.**

If multiple event logs are registered, see the latest event log.

```
XSCF> showlogs event -rM
Date           Message
Mar 25 11:45:31 JST 2017  NTP service found NTP server to synchronize.
```

When "NTP service found NTP server to synchronize." appears, the time is correctly synchronized. Then, the following steps are not necessary.

2. **Execute the setntp command to temporarily disable the XSCF NTP client function.**

```
XSCF> setntp -s client -c disable
```

3. **Reboot the XSCF.**

```
XSCF> rebootxscf -a
```

4. **Execute the setdate command to set the XSCF time to the current time.**

In the following example, the XSCF time is set to 2017/3/25 12:00:00.

```
XSCF> setdate -s 032512002017.00
```

The XSCF automatically reboots when the time is set.

5. **Execute the setntp command to re-enable the XSCF NTP client function.**

```
XSCF> setntp -s client -c enable
```

6. **Reboot the XSCF.**

```
XSCF> rebootxscf -a
```

Note - Even if the XSCF time and NTP server time fail to synchronize and the following message appears, this workaround has been completed.

```
XSCF> showlogs event -rM
```

Date Message
Mar 25 12:15:31 JST 2017 NTP service failed to reach appropriate NTP server.

■ Notes on the logical domain time (2)

Note - If the firmware is updated from XCP 2232 or earlier to XCP 2240 or later, perform "- Notes on the logical domain time (1)" first.

Due to the problem reported in RTIF2-140711-001 and RTIF2-141204-001, the Oracle Solaris time may deviate at PPAR startup.

To avoid this problem, be sure to perform one of the following procedures.

Note - An XCP with version number XCP 2240 or later fixes the problem reported in RTIF2-140711-001. However, if the firmware update is from XCP 2232 or earlier to XCP 2240 or later, it is necessary to resolve the time deviation problem that has been caused in XCP 2232 or earlier. Perform [Workaround] for "- XCP 2232 or earlier."

- XCP 2240 or later

[Workaround]

Save the logical domain configuration information before stopping or restarting the PPAR.

Example: Saving logical domain configuration information under the name of "config_A"

```
primary# ldm list-domain -l
Omitted
primary# ldm list-spconfig
Omitted
primary# ldm remove-spconfig config_A
primary# ldm add-spconfig config_A
```

- XCP 2232 or earlier

[Workaround]

1. **Specify false for the OpenBoot PROM environment variable auto-boot?, and execute the setpparparam(8) command to disable the autoboot function of the control domain. Also execute the setpparmode(8) command to disable startup of guest domains.**

Example: Physical partition is PPAR#0

```
XSCF> setpparparam -p 0 -s bootscript "setenv auto-boot? false" -y
XSCF> setpparmode -p 0 -m guestboot=off
```

2. **Execute the poweron(8) command to power on the target physical partition.**

```
XSCF> poweron -p 0 -y
```

3. **Execute showdomainstatus -p 0 -g primary, and confirm that "OpenBoot**

Running" appears under Status.

4. Execute the console(8) command to switch to the control domain console.

```
XSCF> console -p 0 -y
:
Omitted
{0} ok
```

5. Execute the boot -s command to start Oracle Solaris in single user mode.

```
{0} ok boot -s
```

6. Execute the date command of Oracle Solaris to set the control domain time.

Example: Setting for 12:20:00 on March 25, 2017

```
primary# date 0325122017.00
```

Note - Execute the showdate(8) command to display the XSCF time, and confirm that the control domain time of the target physical partition is the same as the XSCF time.

7. Switch to multi-user mode.

```
primary# exit
```

8. For a logical domain configuration, set the correct time for guest domains.

For details on how to check for a logical domain configuration, see "Checking between a logical domain configuration and the factory-default configuration." Perform the following procedure on every guest domain to set the time.

a. Check the CONS value of the guest domain.

```
primary# ldm list-domain
NAME          STATE      FLAGS    CONS  VCPU  MEMORY  UTIL  NORM  UPTIME
primary      active    -n-cv-   SP    8     3968M  0.2%  0.1%  18h 37m
guest        bound     -----  5000  8     2G
```

b. Disable the autoboot function of the guest domain.

```
primary# ldm set-variable auto-boot?=false guest
```

c. Start the guest domain.

```
primary# ldm start-domain guest
```

d. Open the guest domain console.

```
primary# telnet localhost 5000
:
{0} ok
```

- e. Execute the boot -s command to start Oracle Solaris in single user mode.

```
{0} ok boot -s
```

- f. Execute the date command of Oracle Solaris to set the guest domain time.
Example: Setting for 12:30:00 on March 25, 2017

```
guest# date 0325123017.00
```

- g. Switch to multi-user mode.

```
guest# exit
```

- h. If the guest domain is an NTP client, execute the ntpq -np command, and confirm synchronization with the NTP server.
The asterisk ("*") at the beginning of a line below indicates that the NTP server in the "remote" column is synchronized with the guest domain.

```
guest# while [ 1 ]; do ntpq -np; sleep 60; done
  remote          refid          st t when poll reach  delay  offset  jitter
=====
*203.0.113.156  203.0.113.70    4 u  46  64   3  0.458  0.754  7.243
  remote          refid          st t when poll reach  delay  offset  jitter
=====
*203.0.113.156  203.0.113.70    4 u  39  64   7  0.478  1.487  5.664
  remote          refid          st t when poll reach  delay  offset  jitter
=====
*203.0.113.156  203.0.113.70    4 u  35  64  17  0.450  2.164  5.077
^C
guest#
```

- i. If necessary, re-enable the autoboot function of the guest domain.

```
primary# ldm set-variable auto-boot?=true guest
```

9. If necessary, re-enable the functions that were disabled in step 1.

For the autoboot function of the control domain, specify true for the OpenBoot PROM environment variable auto-boot?, and execute the setpparam(8) command to re-enable the function. For startup of guest domains, execute the setpparmode(8) command to re-enable it.

Example: Physical partition is PPAR#0

```
XSCF> setpparparam -p 0 -s bootscript "setenv auto-boot? true" -y
XSCF> setpparmode -p 0 -m guestboot=on
```

10. For a logical domain configuration, save logical domain configuration information.

For details on how to check for a logical domain configuration, see "Checking between a logical domain configuration and the factory-default configuration." Example: Saving logical domain configuration information under the name of "config_A"

```
primary# ldm ls -l
Omitted
primary# ldm ls-config
Omitted
primary# ldm remove-spconfig config_A
primary# ldm add-spconfig config_A
```

Checking between a logical domain configuration and the factory-default configuration

You can check whether the configuration is a logical domain configuration by using the showdomainconfig command.

(1) Logical domain configuration

If a value other than factory-default is set, as indicated by "^" below, it is a logical domain configuration.

```
XSCF> showdomainconfig -p 0
PPAR-ID      :0
Booting config
(Current)    :config_A
            ^^^^^^^^
(Next)       :config_A
            ^^^^^^^^
-----
Index        :1
config_name  :factory-default
domains      :1
date_created:-
-----
Index        :2
config_name  :config_A
domains      :1
date_created:'2014-03-05 17:33:17'
```

(2) factory-default configuration

If factory-default is set, as indicated by "" below, it is not a logical domain configuration but the factory-default configuration.

```

XSCF> showdomainconfig -p 0
PPAR-ID      :0
Booting config
(Current)    :factory-default
             ^^^^^^^^^^^^^^^^^
(Next)       :factory-default
             ^^^^^^^^^^^^^^^^^
-----
Index        :1
config_name  :factory-default
domains      :1
date_created:-

```

Notes on the Remote Power Management Function (RCIL)

- To use the remote power management function on XCP 2290 onwards, execute the `setpacketfilters -c ipmi_port` command to set the IPMI service. The SPARC M10 IPMI service is used only with the remote power management function.

From XCP 2290 onwards, the IPMI service is disabled by default. To use the remote power management function, enable the IPMI service.

For XCP 2280 or earlier, the IPMI service cannot be disabled. It is always enabled. If the firmware is updated from XCP 2280 or earlier to XCP 2290 or later, the IPMI service will remain enabled. Therefore, if the remote power management function is not used, disable the IPMI service. If the remote power management has been used, the function will still be available.

The IPMI service can be set by executing the `setpacketfilters -c ipmi_port` command. For details on the `setpacketfilters(8)` command, see the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual*.

- Do not register the same node with multiple remote power management groups. If remote power management is performed by registering a single node with multiple remote power management groups, the operation may not be as intended. With the `setremotepwrmgmt(8)` command, you cannot confirm whether a single node is registered with multiple remote power management groups. When creating or modifying management information files of remote power management groups, take care not to cause overlaps.
- Do not register an I/O node with multiple power control groups. If the same I/O node is set with multiple remote power management groups, and both of the conditions below are satisfied, the I/O node is turned on and off alternately.
 - `setremotepwrmgmt -c enable` is executed, and the remote power management function is enabled.
 - There is a power control group in which one or more host nodes are on, and there is a power control group in which all the host nodes are off.

If you accidentally register an I/O node with multiple power control groups, use

setremotepwrmgmt -c disable to disable remote power management first, and then use clearremotepwrmgmt to delete the power control group setting. After deletion, create a management information file of a remote power management group so that the I/O node is not registered with multiple groups, and then use setremotepwrmgmt -c config to register it again.

- If XCP 2080 or earlier is used together with a single SPARC M10 system that is connected to an external I/O device, do not use the remote power management function. The external I/O device may be erroneously powered off when the XSCF is reset.
- For a management file to set the remote power management function, use CR and LF, or LF as linefeed code.
- For a management file to set the remote power management function, you need to specify the MAC address regardless of the node type. Otherwise, the following error occurs when you execute the setremotepwrmgmt(8) command.
[Example]

```
XSCF> setremotepwrmgmt -c config -u guest ftp://xx.xx.xx.xx/rpmgroup.csv
Password:
Download successful: 213Byte at 2103.000KB/s
Checking file...
The definition of [MACAddress] in [ftp://xx.xx.xx.xx/rpmgroup.csv] is invalid.
XSCF>
```

You need to set the MAC address regardless of the node type of the I/O node, master host node, host node, or remote power supply control unit. This is required even though the MAC addresses of some nodes are not set and left "blank" as described in "Chapter 3 Examples of Remote Power Management Configuration" in the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 RCIL User Guide*.

Notes on Remote Storage

- These notes apply when Java Runtime Environment 8 Update 45 is used and, as described below, an automatic proxy configuration script (PAC file) is used in the Java network settings or by the browser being used. In this case, "The application cannot be run.", "Failed to validate certificate. The application will not be executed.", or another message appears, and the XSCF Remote Storage Server may fail to start.
 - When [Use browser settings] is selected in the Java network settings.
 - For Internet Explorer
The [Use automatic configuration script] check box is selected in [Tools] - [Internet Options] - [Connections] - [LAN settings].
 - For Firefox
[Automatic proxy configuration URL] is selected in [Tools] - [Options] - [Advanced] - [Network] - [Settings].
 - [Use automatic proxy configuration script] is selected in the Java network settings.

To work around this problem, go to the [Java Control Panel] screen, select [General] - [Network Settings] - [Use proxy server], and change the Java network settings.

- Remote storage cannot be connected even when a terminal has more than one network connection.
In this case, a terminal can be connected to the remote storage if you disable network devices on the terminal that are not used for the XSCF-LAN connection.
- If remote storage is connected through a VPN or other network that performs address translation, the error message "iscsiadm: no records found!" is output and the connection is disabled.
Do not connect remote storage through a VPN or other network that performs address translation.

Notes on Rebooting the XSCF

- The following warning message may be output to the control domain console when executing `rebootxscf(8)`.

```
PICL snmpplugin: cannot fetch object value (err=5, OID=<1.3.6.1.2.1.47.1.4.1>, row=0)
```

- If the cluster software is in use when executing `rebootxscf(8)`, the following warning message may be output to the logical domain console.

```
SA SA_xscf***,so to test host *** failed  
7240 Connection to the XSCF is refused. (node:*** ipaddress:*** detail:***)
```

- If the `rebootxscf(8)` command is executed while remote storage is connected, access to the media encounters an error.

```
FRU: /BB#0/REMOTE_STORAGE  
Msg: I/O device error detected  
  
FRU: /BB#0/REMOTE_STORAGE  
Msg: Boot process failed  
  
FRU: /REMOTE_STORAGE  
Msg: FCode informed error  
  
FRU: /BB#1/REMOTE_STORAGE  
Msg: A:sdl:sd:SCSI transport failed (DRIVE vendor=Fujitsu, product=Remote Storage)  
(compdev=c8t0d0)  
  
FRU: /REMOTE_STORAGE  
Msg: A:sd0:sd:Device Error(Media Error) (DRIVE vendor=Fujitsu, product=Remote Storage)
```

```
(compdev=c5t0d0)
```

```
FRU: /BB#0/REMOTE_STORAGE
```

```
Msg: The Machine Administration detected a other hard disk failure.
```

Notes and Restrictions on iSCSI Booting of FUJITSU Storage ETERNUS Disk Storage Systems as the iSCSI Targets

Notes

These notes apply to firmware version V10L50 on FUJITSU Storage ETERNUS Disk Storage Systems. This setting is unnecessary for V10L60 and later.

To perform iSCSI boot on FUJITSU Storage ETERNUS Disk Storage Systems as the iSCSI targets, apply XCP 2280 or later for the XCP firmware, and perform the following procedure in advance.

1. **Perform the following steps to change the OpenBoot PROM environment variable `nvrarc`.**

- a. Execute `nvedit`, and write `nvrarc` as shown below.

```
{0} ok nvedit
0: set-random-tcp#
```

- b. Press the [Ctrl] + [C] keys to exit `nvedit` and return to the `ok` prompt.
- c. Execute `nvstore` to save the edited contents.

```
{0} ok nvstore
```

- d. Check whether `nvrarc` was written correctly.

```
{0} ok printenv nvrarc
nvrarc = set-random-tcp#
{0} ok
```

2. **Set the OpenBoot environment variable `use-nvrarc?` to `true`.**

```
{0} ok setenv use-nvrarc? true
use-nvrarc? = true
```

3. **Execute the `reset-all` command to restart the OpenBoot PROM.**

```
{0} ok reset-all
```

To return the above setting to its original state, perform the following steps.

4. **Delete the set contents from the OpenBoot PROM environment variable nvramrc.**

- a. Execute nvedit, and delete "set-random-tcp#" from nvramrc.

```
{0} ok nvedit
0:
```

- b. Press the [Ctrl] + [C] keys to exit nvedit and return to the ok prompt.
- c. Execute nvstore to save the edited contents.

```
{0} ok nvstore
```

- d. Check whether the nvramrc setting was deleted correctly.

```
{0} ok printenv nvramrc
nvramrc =
{0} ok
```

5. **Set the OpenBoot environment variable use-nvramrc? to false.**

If it has been set to true before the above step 2 was performed, this step is unnecessary.

```
{0} ok setenv use-nvramrc? false
use-nvramrc? = false
{0} ok
```

6. **Execute the reset-all command to restart the OpenBoot PROM.**

```
{0} ok reset-all
```

Restrictions on Deferred Dump

If the memory fails, the following warning message is output, and the panic may persist, preventing deferred dump from being performed.

```
WARNING: Retained memory segment overlapped with degraded
memory.
```

[How to restore]

If this phenomenon persists, perform the following procedure.

- For the control domain
 1. **Execute the sendbreak command to stop the panic.**

```
XSCF> sendbreak -y -p [ppar_id]
```

2. After stopping at the ok prompt, execute the clear-retained-memory command.

```
{0} ok clear-retained-memory
```

3. Execute the reset-all command to restart the OpenBoot PROM.

```
{0} ok reset-all
```

- For the guest domain

After stopping the guest domain with the ldm stop-domain command for Oracle VM Server for SPARC, execute the ldm start-domain command to start the guest domain.

Other Notes and Restrictions

Notes

- It may take a while for the service domain to start the virtual disk server (vds) or virtual switch (vsw). For this reason, at the time of guest domain boot, the wait time from the virtual disk/virtual network to vds or vsw is set as follows.
 - XCP 2221 or earlier: Maximum of 5 minutes
 - XCP 2230 or later: Maximum of 15 minutes

If this wait time is exceeded, boot from the virtual disk/virtual network may fail.

For example, with XCP 2230 or later, suppose multiple disks are configured for the OpenBoot PROM environment variable boot-device. Under this condition, it may take up to 15 minutes to switch the boot path from the virtual disk to the next disk, or the boot may fail if it takes more than 15 minutes.

- When the mode switch on the operation panel is set to Service mode, the power cannot be turned on by using the power switch on the operation panel. To turn on the power using the power switch on the operation panel, set the mode switch on the operation panel to the Locked mode position.
- To add the 64 GB memory, apply XCP 2090 or later.
- The maximum number of users who can concurrently connect to the XSCF via Telnet and SSH is as follows:
 - SPARC M10-1: 20 users
 - SPARC M10-4: 40 users
 - SPARC M10-4S (without crossbar box): 40 users
 - SPARC M10-4S (with crossbar box): 70 users

If the maximum allowable number of users is exceeded, access is denied.

- XSCF-LAN is compliant with auto-negotiation. If you connect XSCF-LAN to a network device that is fixed to full-duplex mode, the XSCF-LAN communicates in

half-duplex mode according to the IEEE 802.3 protocol. This may slow down the speed of network communication or cause communication error. Be sure to set auto-negotiation for network devices to which you connect XSCF-LAN.

- Configuring a memory mirror setting by using the `setupfru(8)` command should be done when the physical partition (PPAR) to which the target system board (PSB) belongs is powered off.
- To display a man page, set `TERM=vt100` for the terminal software.
- When configuring a physical partition (PPAR), do not set a BB-ID that does not exist in the system as the PPAR-ID.
For example, if BB-IDs 00 and 01 exist in the system, you can set 00 or 01 as the PPAR-ID. If you set 02 as the PPAR-ID, the PPAR with PPAR-ID 02 will not be able to start.
- Among the information displayed by executing the `showhardconf(8)` command, the PCI Express (PCIe) card information of the guest domain will be reflected after Oracle Solaris of the corresponding guest domain has started.
- As of XCP 2032, the default value for the power save operation set by the `setpparmode(8)` command is changed from "enabled" to "disabled."
- When you execute the `testsb(8)` or `diagxbu(8)` command, a PPAR-ID of "PPAR#30" that does not exist may appear in an error log suspected area. This indicates that an error was detected on the system board (PSB) while diagnosing. The output PPAR-ID does not have any meaning.
- We recommend to connect the control domain console via the XSCF-LAN port. When a large amount of data is output by connecting to the control domain console via a serial port, the data may not be displayed properly.
- [SPARC M10-4S]
The time taken for processing a command that displays the system configuration or status (such as the `showhardconf(8)` command, `showboards(8)` command, and `showpparstatus(8)` command) depends on the number of SPARC M10-4S chassis or crossbar boxes that compose the system.
- [SPARC M10-4S]
For a system configured with multiple chassis, chassis BB#01 or XBBOX#81 may become the master XSCF first.
- [SPARC M10-4S]
It may take more time to restart the physical partition when hardware fails, depending on the system configuration.
- If you add a SPARC M10-4S to the SPARC M10-4S on which XCP 2032 is installed, update to the latest firmware in advance.
- When you execute the `setsnmpvacm(8)` command with `createview` specified as the operand, you cannot set an access restriction using the OID mask of MIB. When you execute the `setsnmpvacm(8)` command with `createview` specified as the operand, do not use the OID mask of MIB.
- If a takeover IP address has been set, the UDP packet sender IP address and Agent Address for the SNMP Trap of SNMPv1 are different. The IP address assigned to each XSCF-LAN (physical IP address) is stored as the UDP packet sender IP address, while the takeover IP address (virtual IP address) is stored as the Agent

Address.

- In the SPARC M10-4 or SPARC M10-4S with two CPUs installed, when I/O bus reconfiguration is enabled, the addition of a CPU memory unit upper (CMUU) to add two CPUs will change the device paths of the I/O devices used up until this time.

Normally, disable the I/O bus reconfiguration function by leaving the default, false, specified for `ioreconfigure`. You can set `ioreconfigure` in the `-m` function option of the `setpparmode(8)` command.

If you enable the I/O bus reconfiguration function by specifying true for `ioreconfigure`, it may be necessary to reinstall Oracle Solaris.

- With a logical domain running Oracle Solaris 11, updating to XCP 2050 or later allows you to have an increased maximum number of PCIe cards on one root complex (RC).
To add a PCIe card after updating to XCP 2050 or later, perform the steps below in the control domain.

This example assumes that two sets of configuration information currently in use, `ldm-set1` and `factory-default`, are stored.

1. **Confirm the logical domain configuration information saved to the XSCF.**

```
primary# ldm list-spconfig
ldm-set1 [current]
factory-default
```

2. **Execute the following commands on all of the logical domains, including the control domain.**

Specify the name of the logical domain for `ldom`.

```
primary# ldm set-variable fix_atu=true ldom
primary# ldm remove-variable fix_atu ldom
```

3. **Store the updated configuration information for the logical domain again.**

Delete the stored `ldm-set1` and then store the information again.

```
primary# ldm rm-spconfig ldm-set1
primary# ldm add-spconfig ldm-set1
```

4. **Restart all logical domains.**

- An XSCF user account name cannot be the same as an LDAP user name. In addition, an XSCF user account UID cannot be the same as an LDAP UID.
- When starting the OpenBoot PROM, even if an error is detected with the GFX 550e graphics card (TechSource, PCI Express Graphics Board), nothing may be output to the console.
The problem can be confirmed from the following error log output with the `showlogs error` command, which registers the faulty component (suspect

component) as "UNSPECIFIED".

[Error message example]

```
XSCF> showlogs error
Date: MMM DD hh:mm:ss JST 2014
Code: 80000000-00ffff0000ff0000ff-030000010000000000000000
Status: Alarm Occurred: MMM DD hh:mm:ss.sss JST 2014
FRU: /UNSPECIFIED
Msg: I/O device error detected
```

■ [SPARC M10-4S]

If cold replacement is performed on a system with several SPARC M10-4S and crossbar boxes, the XSCF may not start. Perform one of the following procedures, according to the target components for cold replacement.

- When replacing crossbar boxes (XBBOX)

Perform the following procedure.

1. **Turn off the input power (AC OFF) of the target XBBOX.**
2. **Replace the XSCF unit (XSCFU).**

When performing this procedure, swap the microSD card in the old XSCF unit to the new XSCF unit, before replacing the XSCF unit itself.

3. **Turn on the input power (AC ON) of the XBBOX.**

- Perform the following procedure when replacing the following components that are mounted on the crossbar box (XBBOX):

In the following cases, replacement of the microSD card is unnecessary. For the maintenance procedure, see the *Fujitsu M10-4/Fujitsu M10-4S/SPARC M10-4/SPARC M10-4S Service Manual*.

- XSCF unit (XSCFU), crossbar unit (XBU), XSCF interface unit (XSCFIFU), crossbar backplane unit (XBBP), fan backplane unit (FANBP), operation panel (OPNL), power supply unit (PSU), fan (FAN)
- Perform the following procedure when replacing SPARC M10-4S or CPU memory unit lower (CMUL):

Execute the `replacefru(8)` command and then follow the maintenance menu to perform the replacement.

■ [SPARC M10-4S]

When maintenance is performed using either the `replacefru(8)` or `addfru(8)` command, terminate the command by entering the "c" option. In a system consisting of multiple SPARC M10-4S chassis, if the `flashupdate(8)` command is executed before the above command is terminated, automatic switchover to the master XSCF may not be performed after the firmware update is complete.

[How to restore]

After maintenance is completed using either the `replacefru(8)` or `addfru(8)` command, terminate the command by entering the "c" option. If switchover to the master XSCF is not performed after the completion of the firmware update, execute the `switchscf(8)` command on the master XSCF to switch over to the master XSCF.

- If an XSCF reset or switchover is performed while you are performing a setting-related operation from the XSCF shell or XSCF Web, the setting may not be saved on the XSCF. Use the XSCF shell or XSCF Web to confirm that the setting is saved after the XSCF reset or switchover has completed. If the setting is not saved, perform the setting again.

Restrictions

- The `-c wait` option of the `setpowerupdelay(8)` command is not supported.
- At this point in time, the `no-mem` setting in the `-s` option of the `setpcl(8)` command is not supported.
- Alive check function between the XSCF and a Hypervisor is not supported. Alive check function between the XSCF and a Hypervisor cannot be enabled/disabled even by using the `setpparmode(8)` command.

- [SPARC M10-4S]

Suppose that the XSCF was switched while connecting to the control domain console in a SPARC M10-4S that is composed of more than two chassis. In that case, if an attempt has been made to reconnect the control domain console to the switched master XSCF, a connection to the control domain console may not be established until all the SPARC M10-4S chassis have been restarted.

If all XSCFs have not been rebooted due to an error in the XSCF, establish a connection to the control domain console again. Replace a failed XSCF or CPU memory unit lower (CMUL) if a connection cannot be established to the control domain console.

- [SPARC M10-4S]

In a SPARC M10-4S that is composed of more than two chassis, if all XSCFs have not been restarted due to an error in an XSCF, the `poweroff(8)` and `reset(8)` commands may not be executed normally.

To disconnect the power of a physical partition (PPAR), login to the control domain of the PPAR and execute the shutdown command on Oracle Solaris. Then, disconnect the power of the PPAR using the `poweroff -f` command on the XSCF firmware. The `reset(8)` command cannot be used in this state.

- Replacement of a crossbar box using the `replacefru(8)` command is not supported at present. For details on how to replace a crossbar box, see "[Restrictions on Replacing a Crossbar Box](#)" in "Chapter 6 Information on SPARC M10-4S Hardware."
- Addition of a crossbar box using the `addfru(8)` command is not supported at present. For details on how to add a crossbar box, see "[Restrictions on Adding Expansion Rack 2](#)" in "Chapter 6 Information on SPARC M10-4S Hardware."
- [SPARC M10-4S]
When the `poweron(8)` command powers on all the physical partitions (PPARs) in a system that has more than one PPAR, the startup time taken by power-on of a specified PPAR is even longer than the time taken by batch power-on, which is specified by `-a`.
- [SPARC M10-4S]
If `showhardconf -M` is executed, it may not be possible to display one screen at a time if both of the conditions below are met. Do not execute `showhardconf -M`.

- System configured with 2BB or more
- One or more SPARC M10-4S units are subject to an XSCF reset by the `rebootxscf(8)` command, so they cannot communicate.
- [SPARC M10-1]
If XCP 2070 or earlier firmware is updated while the physical partition (PPAR) is powered on, the logical domains may stop.
Be sure to execute firmware update when the physical partition (PPAR) is powered off. For details on this problem, see "RTIF2-131004-001" in "[Problems Resolved in XCP 2070](#)."
- The user privileges of XSCF user accounts are managed with the LDAP server specified in the `setldap(8)` command or in [XSCF] - [Settings] - [User Manager] - [LDAP] on XSCF Web. If the version being used is XCP 2271 or earlier, the privileges cannot be configured from [XSCF] - [Settings] - [User Manager] - [Account] on XSCF Web.
Set user privileges with the `setprivileges(8)` command from the XSCF shell prompt.
- Neither the `deleteuser(8)` command nor the [Settings] - [User Manager] - [Account] menu on XSCF Web can delete the user account of a user who is logged into the XSCF via SSH, Telnet, or XSCF Web. Any attempt to do so will result in an error. To delete the target user account, use the `who(1)` command or the [Settings] - [User Manager] - [Account] menu on XSCF Web to confirm that the user account is not logged in to the XSCF, and then delete it.
Likewise, neither the `deleteuser(8)` command nor the [Settings] - [User Manager] - [Account] menu on XSCF Web can delete a user account for which the SNMP setting has been made with the `setsnmp(8)`, `setsnmpusm(8)`, or `setsnmpvacm(8)` command or the [SNMP] or [SNMP Security] menu on XSCF Web. Any attempt to do so will result in an error. Before deleting the user account, execute the `rebootxscf -a` command to reset all the XSCFs.

XCP 2352 Problems and Workarounds

See "[Problems That Might Occur With XCP 2352 and Workarounds](#)" for information on problems that can occur with XCP 2352. See "[Problems Resolved in XCP 2352](#)" for information on problems resolved in XCP 2352.

Chapter 3

Information on Software

This chapter describes special instructions and problems concerning the SPARC M10 Systems software.

- [Notes and Restrictions](#)
- [Problems With XCP and Workarounds](#)
- [Problems With Oracle Solaris and Workarounds](#)

Notes and Restrictions

Notes on Internal SAS Disk Drives and Oracle Solaris MPxIO Settings

We recommend using an internal SAS disk of SPARC M10 systems with the Oracle Solaris MPxIO setting enabled.

Reason

The recommendation will enhance redundancy because disk access continues even when the communication path between an internal SAS controller and an internal SAS disk is degraded due to a failure, etc.

Note that you can enable MPxIO by using the bundled tool from Enhanced Support Facility 5.0 or later.

Note - Only SPARC M10 systems sold by Fujitsu in Japan support Enhanced Support Facility (ESF).

Target Drives

Whether you need to perform the Oracle Solaris MPxIO setting depends on the product name of the internal SAS disk or the SSD mounted in the SPARC M10 system, and the Oracle Solaris version, as shown in [Table 3-1](#).

Table 3-1 Necessity of MPxIO Setting for an Internal SAS Disk or SSD

Product Name Fujitsu/Oracle Model Name	Vendor Name Product Name	Necessity of MPxIO Setting		
		Oracle Solaris 10 1/13	Oracle Solaris 11.1	Oracle Solaris 11.2 or Later
600GB SAS disk drive SPME3A11*/ 7105508, 7106318 (*3) 7105571, 7106322 (*4)	TOSHIBA MBF2600RC (*5) AL13SEB600 (*5) AL13SEB600AL14SE AL13SEB600AL15SE	Required	Required	Not required(*1)
900GB SAS disk drive SPME3B11*/ 7111257, 7111260 (*3) 7111261, 7111262 (*4)	TOSHIBA AL13SEB900 (*5) AL13SEB900AL14SE AL13SEB900AL15SE	Required	Required	Required
1.2TB SAS disk drive SPME3C11*/ 7120206, 7120207 (*3) 7120208, 7120209 (*4)	TOSHIBA AL15SE12NFUJ1.2T (*5)	Required	Required	Required
100GB SAS SSD SPME3Y11*/ 7105509, 7106319 (*3) 7105572, 7106323 (*4)	TOSHIBA MK1001GRZB (*5)	Required	Required	Not required(*1)
200GB SAS SSD SPME3Y21*/ 7105510, 7106320 (*3) 7105573, 7106324 (*4)	TOSHIBA MK2001GRZB (*5)	Required	Required	Not required(*1)
200GB SAS SSD SPME3Y22*/ 7107810, 7107814 (*3) 7107811, 7107815 (*4)	TOSHIBA PX02SMF020 (*5)	Required	Required	Required
400GB SAS SSD SPME3Y31*/ 7107812, 7107816 (*3) 7107813, 7107817 (*4)	TOSHIBA PX02SMF040 (*5)	Required	Required	Required

Table 3-1 Necessity of MPxIO Setting for an Internal SAS Disk or SSD (continued)

Product Name Fujitsu/Oracle Model Name	Vendor Name Product Name	Necessity of MPxIO Setting		
		Oracle Solaris 10 1/13	Oracle Solaris 11.1	Oracle Solaris 11.2 or Later
400GB SAS SSD SPME3Y32*/ 7115093, 7115094 (*3) 7115095, 7115096 (*4)	TOSHIBA PX04SMB04FUJ400G (*5)	Required	Required	Required
800GB SAS SSD SPME3Y41*/ 7119191, 7119192 (*3) 7119187, 7119188 (*4)	TOSHIBA PX04SMB08FUJ800G (*5)	Required	Required	Required
When the internal hardware RAID is specified		Not required(*2)	Not required(*2)	Not required(*2)

*1 MPxIO is enabled by default of Oracle Solaris. So, you need not to perform the setting.

*2 Since the internal hardware RAID is not subject to the MPxIO setting, it does not require the setting.

*3 These are factory default drives.

*4 These are used for expansion in the field.

*5 This is a product name set to the scsi_vhci.conf. Both of these 600 GB SAS disk drives need to be set.

Confirming the Product Name, Vendor Name, and MPxIO State of the Internal SAS Disk or the SSD

Confirm the product name, the vendor name, and the device path name of the internal SAS disk or the SSD of the SPARC M10 system, and see [Table 3-1](#) to determine whether you need to make the MPxIO setting.

If MPxIO is already enabled, the character string "scsi_vhci" appears in the device path name of the internal SAS disk or the SSD.

■ When the MPxIO setting is enabled

In the following example, "scsi_vhci" appears in the device path name. So, the MPxIO setting is enabled.

```
# format
Searching for disks...done
AVAILABLE DISK SELECTIONS:
0. c0t50000394281b5310d0 <TOSHIBA-MBF2600RC-3706 cyl 64986 alt 2 hd 27 sec 668>
   Logical device path name          Vendor name  Product name
   /scsi_vhci/disk@g50000394281b5310
   Physical device path name
   /dev/chassis/FUJITSU-BBEXP. 500000e0e06d31bf/03N0_HDD00/disk
.....
```

▪ **When the MPxIO setting is disabled**

In the following example, "scsi_vhci" does not appear in the device path name. So, the MPxIO setting is disabled.

```
# format
Searching for disks...done
AVAILABLE DISK SELECTIONS:
0. c2t50000394281B5312d0 <TOSHIBA-MBF2600RC-3706 cyl 64986 alt 2 hd 27 sec 668>
   Logical device path name      Vendor name  Product name
   /pci@8800/pci@4/pci@0/pci@0/scsi@0/ipropt@f/disk@w50000394281b5312,0
   Physical device path name
   .....
```

Procedure for Enabling MPxIO

Log in as a user with administrator privileges to make this setting.

1. **Add the information (the vendor name and product name) of a disabled internal SAS disk or SSD.**

- **For Oracle Solaris 11 and later**

Add the information (the vendor name and the product name) for the disabled internal SAS disk or SSD to the scsi_vhci.conf file in the /etc/driver/drv folder.

If the /etc/driver/drv folder does not have the scsi_vhci.conf file, copy and use /kernel/drv/scsi_vhci.conf.

For details on how to set the scsi_vhci.conf file, see the online manual.

```
scsi-vhci-failover-override ="TOSHIBA MBF2600RC", "f_sym",
                             Vendor name Product name
                             "TOSHIBA AL13SEB600", "f_sym";
                             Vendor name Product name
```

- **For Oracle Solaris 10**

Add the information (the vendor name and product name) of the disabled internal SAS disk or SSD to the /kernel/drv/scsi_vhci.conf file.

```
device-type-scsi-options-list ="TOSHIBA MBF2600RC", "sym-opt",
                               Vendor name Product name
                               "TOSHIBA AL13SEB600", "sym-opt";
                               Vendor name Product name

sym-opt = 0x1000000;
```

2. **Enable MPxIO with the stmsboot command.**

For details on the stmsboot command, see the online manual.

```
# stmsboot -D mpt_sas -e
```

If the following message is output, continue by executing the `stmsboot -u` command.

```
# stmsboot -D mpt_sas -e
STMS is already enabled. No changes or reboots needed
# stmsboot -u
```

Executing the `stmsboot` command restarts the system of the domain, and enables MPxIO of the internal SAS disk or the SSD. After the restart, the target device path name is changed.

You need to change the device path name in application programs, etc., if they specify the name directly.

3. **Confirm that MPxIO is enabled.**

If MPxIO is enabled, the character string "scsi_vhci" appears in the device path name.

```
# format
Searching for disks...done
AVAILABLE DISK SELECTIONS:
0. c0t50000394281b5310d0 <TOSHIBA-MBF2600RC-...>
   Logical device path name      Vendor name  Product name
   /scsi_vhci/disk@g50000394281b5310
   Physical device path name
```

If the ZFS system volume is installed in an internal SAS disk or SSD of a multipath type (MPxIO), confirm that the volume status is normal.

```
# /usr/sbin/zpool status
...
```

Notes on Updating a SPARC M10 System to Oracle Solaris 11.2 or Later

Notes on Enabling MPxIO

The device path name of an internal SAS disk or an SSD may be changed when Oracle Solaris 11.1 is updated to Oracle Solaris 11.2 or later. You can boot Oracle Solaris even when the device path name is changed. However, if any application or backup software specifies the device path name of the internal SAS disk or the SSD directly, the device path may not be available for reference.

Cause

The cause of this is that updating from Oracle Solaris 11.1 to Oracle Solaris 11.2

changes the default MPxIO setting of the internal SAS disk or the SSD from disabled to enabled. For the target disks, see [Table 3-2](#). For an example of a device path change, see [Table 3-3](#).

Table 3-2 Default MPxIO Setting on an Internal SAS Disk and SSD

Product Name Fujitsu/Oracle Model Name	Vendor Name Product Name	Default MPxIO Setting		
		Oracle Solaris 10 1/13	Oracle Solaris 11.1	Oracle Solaris 11.2 or Later
600GB SAS disk drive	TOSHIBA	Disabled	Disabled	Enabled
SPME3A11*/ 7105508, 7106318 (*1) 7105571, 7106322 (*2)	MBF2600RC AL13SEB600 AL13SEB600AL14SE AL13SEB600AL15SE			
900GB SAS disk drive	TOSHIBA	Disabled	Disabled	Disabled
SPME3B11*/ 7111257, 7111260 (*1) 7111261, 7111262 (*2)	AL13SEB900 AL13SEB900AL14SE AL13SEB900AL15SE			
1.2TB SAS disk drive	TOSHIBA	Disabled	Disabled	Disabled
SPME3C11*/ 7120206, 7120207 (*1) 7120208, 7120209 (*2)	AL15SE12NFUJ1.2T			
100GB SAS SSD	TOSHIBA	Disabled	Disabled	Enabled
SPME3Y11*/ 7105509, 7106319 (*1) 7105572, 7106323 (*2)	MK1001GRZB			
200GB SAS SSD	TOSHIBA	Disabled	Disabled	Enabled
SPME3Y21*/ 7105510, 7106320 (*1) 7105573, 7106324 (*2)	MK2001GRZB			
200GB SAS SSD	TOSHIBA	Disabled	Disabled	Disabled
SPME3Y22*/ 7107810, 7107814 (*1) 7107811, 7107815 (*2)	PX02SMF020			
400GB SAS SSD	TOSHIBA	Disabled	Disabled	Disabled
SPME3Y31*/ 7107812, 7107816 (*1) 7107813, 7107817 (*2)	PX02SMF040			
400GB SAS SSD	TOSHIBA	Disabled	Disabled	Disabled
SPME3Y32*/ 7115093, 7115094 (*1) 7115095, 7115096 (*2)	PX04SMB04FUJ400G			

Table 3-2 Default MPxIO Setting on an Internal SAS Disk and SSD (continued)

Product Name Fujitsu/Oracle Model Name	Vendor Name	Default MPxIO Setting		
	Product Name	Oracle Solaris 10 1/13	Oracle Solaris 11.1	Oracle Solaris 11.2 or Later
800GB SAS SSD	TOSHIBA	Disabled	Disabled	Disabled
SPME3Y41*/ 7119191, 7119192 (*1) 7119187, 7119188 (*2)	PX04SMB08FUJ800G			

*1 These are factory default drives.

*2 These are used for expansion in the field.

Table 3-3 Examples of the Device Path Change Before and After MPxIO Enabling

	Device Path Name Before MPxIO is Enabled	Device Path Name After MPxIO is Enabled
Physical device path name	/pci@8800/pci@4/pci@0/pci@0/scsi@0/iport@f/disk@w50000394281b5312,0	/scsi_vhci/disk@g50000394281b5310
Logical device path name	/dev/rdisk/c2t50000394281B5312d0	/dev/rdisk/c0t50000394281b5310d0

Note that a device path name is not changed if MPxIO of an internal SAS disk or an SSD of the SPARC M10 system is enabled manually before updating to Oracle Solaris 11.2.

Corrective Action

Take the following actions after updating to Oracle Solaris 11.2.

- **When the physical device path name can be changed**

Confirm the physical device path name by using the procedure shown in [Procedure for Confirming the Device Path](#). Change the device path of the internal SAS disk or SSD directly specified in backup software or application programs to the confirmed device path name.

- **When the physical device path name cannot be changed**

Execute the following command, and forcibly disable the MPxIO setting of the internal SAS disk and SSD.

```
# stmsboot -D mpt_sas -d
```

Procedure for Confirming the Device Path

In the following procedure, you can confirm the device path name, vendor name, product name, and enabled/disabled state of MPxIO of the internal SAS disk or SSD in the SPARC M10. If MPxIO is already enabled, the character string "scsi_vhci" appears in the physical device path name of the internal SAS disk or SSD.

- **Display example when MPxIO for an internal disk is enabled**

The physical device path name includes the character string "scsi_vhci". So, MPxIO is enabled.

The format command omits a part of the logical device path name to display. The actual name of the logical device path is the path name with "/dev/rdisk" added.

```
# format
Searching for disks...done
AVAILABLE DISK SELECTIONS:
0. c0t50000394281b5310d0 <TOSHIBA-MBF2600RC- 3706 cyl 64986 alt 2 hd 27 sec 668>
   Logical device path name      Vendor name  Product name
/scsi_vhci/disk@g50000394281b5310
Physical device path name
/dev/chassis/FUJITSU-BBEXP. 500000e0e06d31bf/03N0_HDD00/disk
```

- **Display example when MPxIO for an internal disk is not enabled**

The physical device path name does not include the character string "scsi_vhci". So, MPxIO is disabled.

The format command omits a part of the logical device path name to display. The actual name of the logical device path is the path name with "/dev/rdisk" added.

```
# format
Searching for disks...done
AVAILABLE DISK SELECTIONS:
0. c2t50000394281b5312d0 <TOSHIBA-MBF2600RC-3706 cyl 64986 alt 2 hd 27 sec 668>
   Logical device path name      Vendor name  Product name
/pci@8800/pci@4/pci@0/pci@0/scsi@0/iport@f/disk@w50000394281b5312,0
Physical device path name
/dev/chassis/FUJITSU-BBEXP. 500000e0e06d31bf/03N0_HDD00/disk
```

Notes on Boot Environment Switching by beadm

Boot environment switching with the beadm command or with the pkg command may fail with the output of an error message where Oracle Solaris 11.1 or later is installed on an internal SAS disk or SSD.

- **Message example 1**

```
# beadm activate S11U1SRU20B04-z_stress-2.10-1
Error while accessing "/dev/rdisk/c2t500003942823F352d0s0":
No such file or directory
Unable to activate S11U1SRU20B04-z_stress-2.10-1.
Error installing boot files.
```

- **Message example 2**

```
# beadm activate S11U1SRU20B04-z_stress-2.10-1
....
Error while accessing "/dev/rdisk/c2t500003942823F352d0s0":
No such file or directory
Unable to activate S11U1SRU20B04-z_stress-2.10-1.
Error installing boot files.
....
```

Cause

This occurs because the MPxIO device path is not properly inherited by beadm or pkg when the internal SAS disk or SSD is made into a multipath type (MPxIO).

Corrective Action

Execute the following command, and then execute the boot environment switching command again.

```
# /usr/sbin/zpool status
```

Notes on Support for Disks With an EFI (GPT) Label

- The default disk label that takes effect when Oracle Solaris is installed has changed from VTOC (SMI) to EFI (GPT). If you need a disk with a VTOC (SMI) label, execute the `format -e` command of Oracle Solaris to apply the VTOC (SMI) label, and then install Oracle Solaris. For details on the format command, see the Oracle Solaris Reference Manual.

In addition, be sure to select [Use a slice on the disk] for the [Solaris slice] screen on the Oracle Solaris installation menu to ensure that the disk is VTOC (SMI) labeled.

```
-----
                        Solaris Slices: 6.0GB unknown Boot

Oracle Solaris can be installed on the whole disk or a slice on the disk.

The following slices were found on the disk.

Slice      #    Size (GB) Slice      #    Size (GB)
-----
Unused     0     0.1 Unused     5     0.0
Unused     1     0.1 rpool      6     5.7
Unused     3     0.0 Unused     7     0.0
Unused     4     0.0 backup    2     6.0

                        Use the whole disk
                        Use a slice on the disk

Esc-2_Continue Esc-3_Back Esc-6_Help Esc-9_Quit
-----
```

- If an EFI (GPT) labeled disk is used, Oracle Solaris booting from a disk larger than 2 TiB that is treated as a virtual disk is not supported.
- If an EFI (GPT) labeled disk is used in versions prior to XCP 2330, any second GPT header is not supported on a virtual disk.

Notes on Oracle VM Server for SPARC

- After logical domains are reconfigured by Oracle VM Server for SPARC or after executing the "ldm unbind-domain/ldm bind-domain" command, if the guest domain is operated by the reset(8) command of XSCF firmware before executing the "ldm add-spconfig" command, a guest domain other than what had been specified may be reset. Or, the specified guest domain is not reset. You should save a configuration of the logical domains with the ldm add-spconfig command. Then, to reset the guest domain before saving it, execute the ldm stop command from the control domain, not from the XSCF.
- To specify a logical domain configuration for the next time you start, use the ldm set-config command in place of ldm add-spconfig -r.
If you use the ldm add-spconfig -r command to specify a logical domain configuration for the next time you start and then manipulate a guest domain with the reset(8) command of the XSCF firmware, another guest domain may be reset.
- Be sure to start the ldmd service (svc:/ldoms/ldmd:default) of the control domain.
- A boot disk with an EFI GPT disk label is supported by the following systems: SPARC M10 systems with XCP 2230 or later applied and SPARC T-series or M-series systems with SysFW 8.4 or SysFW 9.1 or later applied. However, SPARC M10 systems with XCP 2221 or earlier applied do not support a boot disk with an EFI GPT disk label. Therefore, do not migrate from a SPARC M10 system with XCP 2230 or later applied or a SPARC T-series or M-series system with SysFW 8.4 or SysFW 9.1 or later applied to a SPARC M10 system with XCP 2221 or earlier applied.
You can determine whether the boot disk is EFI GPT labeled by executing the devinfo command of Oracle Solaris on the raw device of the boot disk.
 - With an EFI GPT disk label

```
# devinfo -i /dev/rdisk/c2d0s0
/dev/rdisk/c2d0s0      0      0      73728      512      2
```

- Without an EFI GPT disk label

```
# devinfo -i /dev/rdisk/c1d0s0
devinfo: /dev/rdisk/c1d0s0: This operation is not supported on EFI labeled
devices
```

- The ldm list-rsrc-group, remove-core -g, and ldm remove-memory -g commands of Oracle VM Server for SPARC 3.2 are not supported. For details, see the *Oracle VM Server for SPARC 3.2 Administration Guide*.
- From Oracle VM Server for SPARC 3.2 onwards, you can allocate memory to a logical domain in units of 256 MB if the dynamic reconfiguration of the physical partitions (PPAR DR) feature is enabled by the XSCF firmware. On the other hand, if the PPAR DR feature is disabled, you can allocate memory in units of 4 MB.
- Suppose that a system disk in a physical partition (PPAR)(PPAR #A) is switched

to a system disk that has been used for another PPAR (PPAR #B), in a system with Oracle VM Server for SPARC 3.1 or later and XCP 2230 or later. The PPAR #A domain configuration information saved in the XSCF may overwrite the PPAR #B domain configuration information.

To switch a system disk to a system disk already used for a different PPAR, uninstall Oracle VM Server for SPARC from the system disk prior to switching the disks. After the switching is completed, reinstall Oracle VM Server for SPARC on the system disk.

The same procedure is needed when using the system disk connected to another unit or when the saved domain configuration information in the XSCF is restored using the `restoreconfig(8)` command.

- Suppose that you deleted a system board by executing dynamic reconfiguration of physical partitions (PPAR DR), in a system with Oracle VM Server for SPARC 3.3 or later. Then, a CPU socket constraint in the degraded state may be added to each logical domain. This operation is not a problem; it is an operation for saving the use status of CPU sockets before PPAR DR is executed.

If the CPU socket constraint appears in the degraded state for a logical domain where the CPU socket constraint is not set, ignore this constraint.

For a logical domain that uses the CPU socket constraint, PPAR DR done by the `deleteboard` command places the CPU socket constraint in the degraded state. Also, resources not specified with the CPU socket constraint may be assigned to the logical domain.

When replacing a building block (PSB) in response to a fault and so on, first add the system board. Then, you can execute the `ldm set-socket --restore-degraded` command to restore the CPU socket constraint. If you are only deleting the system board, execute the `ldm set-socket` command as required to set the CPU socket constraint again.

For details on the CPU socket constraint, see "8.14 Managing Logical Domain Resources Associated with CPU Sockets" in the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide*.

- Before system board deletion

```
# ldm list-socket
CONSTRAINTS
SOCKET
  TENANT  VCPUS  CORES  SOCKET_ID  GROUP
  primary 8    4      4          /BB1
  :
```

- After system board deletion

```
# ldm list-socket
-----
Notice: the system is running a degraded configuration because some
required resources were removed by Physical DR.
-----
CONSTRAINTS
  DOMAIN                SOCKET_ID  STATE
```

```

primary* (degraded) 4 active
dom00* (degraded) 0, 3, 4, 5, 6 active
SOCKET
TENANT VCPUS CORES SOCKET_ID GROUP
primary 8 4 4 /BB1
:
-----

```

- In Oracle VM Server for SPARC 3.3 or later, when the logical domain configuration has been restored by the `ldm init-system` command, resource binding may fail even if the CPU socket constraint is not set. Then, the following message is output. [Example]

```

# ldm bind-domain XXXX
Not enough free memory in specified FJ sockets to meet the request.
Domain XXXX has FJ socket resource constraints for recovery.
Use 'ldm set-socket socket_id= XXXX' to clear.

```

If the above message has been output, clear the CPU socket constraint with the `ldm set-socket` command as instructed in the message. Then, try again to bind the resources.

[Example]

```

# ldm set-socket socket_id= XXXX
# ldm bind-domain XXXX

```

- When using the vHBA (Virtual SCSI Host Bus Adapters) function assigned to a guest domain, set the guest domain auto-boot to false, and then save the updated logical domain configuration information. [How to set]

```

primary# ldm set-variable auto-boot\?=false domain-name
primary# ldm add-spconfig config-name

```

Notes on Live Migration of Oracle VM Server for SPARC

- If you execute the `ldm migrate-domain` command in Oracle VM Server for SPARC to perform a live migration, the following problems occur with the XSCF.
 - If you execute the `showdomainstatus(8)` command, "Unknown" is the status displayed for the migrated guest domain.
 - If you execute the `ldm add-spconfig` command from the migrated control domain to save configuration information, the status will be displayed normally by the `showdomainstatus(8)` command.
 - After a live migration is performed, if `showdomainstatus(8)` is then executed at

the migration source, "Host stopped" is the status displayed for the migrated and non-existent guest domain.

- When a physical partition (PPAR) is powered off by the `poweroff(8)` command, not all of the guest domains may be shut down correctly.
- When a guest domain is reset by the `reset(8)` command, a guest domain other than the specified one may be reset. To reset a guest domain, do so from the guest domain, not the XSCF.
- If SNMP is set, the name of the guest domain under trap notification may be incorrect.
- The migration with the `ldm migrate-domain` command is not supported if the logical domain at the migration source is in the OpenBoot PROM state. Perform migration with the `ldm migrate-domain` command after changing the logical domain at the migration source to either of the following states (CR 15858731):
 - Stopped state (bound state)
 - State where Oracle Solaris is running
- Before performing a live migration, check the physical partition containing the logical domains targeted by the live migration. Check whether Oracle Solaris 11.3 or later is installed on any logical domains (except the control domain) and Oracle Solaris kernel zones in the physical partition. For every logical domain and Oracle Solaris kernel zone that has Oracle Solaris 11.3 or later installed, perform the following steps before performing a live migration.
 1. **Add the following line to the `/etc/system` file.**

```
set uhrt_enable = 0x0
```

2. Reboot the logical domain or Oracle Solaris kernel zone.

- The "sparc64-class1" value of the `cpu-arch` property is being supported from Oracle VM Server for SPARC 3.1.1 onwards. For details on the `cpu-arch` property and its values, see the *Oracle VM Server for SPARC Reference Manual* for the version used.
- The value of the `cpu-arch` property, which supports live migration, differs according to the type of CPU operation on the physical partition and the version of Oracle VM Server for SPARC. Refer to the following table. For details on types of CPU operation on physical partitions, see "7.2.1 CPU Mounted on a Physical Partition and CPU Operational Mode" in the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide*.

Table 3-4 cpu-arch Property Values That Support Live Migration (on Oracle VM Server for SPARC 3.1.1 or Later)

Migrate From	Migrate To	Run on SPARC64 X+	Run on SPARC64 X Compatible Run on SPARC64 X
Run on SPARC64 X+		generic, native, sparc64-class1	generic, sparc64-class1
Run on SPARC64 X compatible Run on SPARC64 X		generic, sparc64-class1	generic, native, sparc64-class1

Table 3-5 cpu-arch Property Values That Support Live Migration (on Oracle VM Server for SPARC 3.1 or 3.1.0.1)

Migrate From	Migrate To	Run on SPARC64 X+	Run on SPARC64 X Compatible Run on SPARC64 X
Run on SPARC64 X+		generic, native	generic
Run on SPARC64 X compatible Run on SPARC64 X		generic	generic, native

Table 3-6 cpu-arch Property Values That Support Live Migration (on Oracle VM Server for SPARC 3.0)

Migrate From	Migrate To	Run on SPARC64 X+	Run on SPARC64 X Compatible Run on SPARC64 X
Run on SPARC64 X+		native	none
Run on SPARC64 X compatible Run on SPARC64 X		none	generic, native

- For the cpu-arch property values that enable live migration between SPARC M12 and SPARC M10 systems, see [Table 3-7](#).

Table 3-7 cpu-arch Property Values That Enable Live Migration Between SPARC M12 and SPARC M10 Systems

Migrate To	SPARC M12	SPARC M10
SPARC M12	generic sparc64-class1 native	generic sparc64-class1
SPARC M10	generic sparc64-class1	generic sparc64-class1 native(*1)

*1 For details, see [Table 3-4](#), [Table 3-5](#), and [Table 3-6](#).

To perform a live migration of a logical domain with `cpu-arch=sparc64-class1` between a SPARC M12 system and a SPARC M10 system, perform the following procedure.

1. **Add the following lines to the `/etc/system` file on the logical domain.**

```
set enable_lghz_stick = 1
set uhrt_enable=0x0
```

2. Reboot the logical domain with the above settings.

Note - If both of the following conditions are met, you do not need to perform the procedure.

- Oracle VM Server for SPARC 3.5 or later is installed on the control domain of the SPARC M10.
 - SRU 11.3.23.5.0 or later is installed on the logical domain.
-

- If you perform a live migration of a domain with a kernel zone running, from a SPARC M10 system with XCP 2230 or later, the following output is displayed, and the live migration fails.

```
# ldm migrate-domain ldg1 root@target-name
Target Password:
Failure occurred while preparing domain ldg1 for suspend
operation
Live migration failed because Kernel Zones are active.
Stop Kernel Zones and retry.
Timeout waiting for domain ldg1 to suspend
Domain Migration of domain ldg1 failed, domain suspend failure.
Domain Migration of LDom ldg1 failed
```

To perform a live migration of a domain with a kernel zone running, stop the kernel zone beforehand.

- Live migration from a SPARC M10 system with XCP 2210 or newer, to another SPARC M10 system with XCP 2092 or older fails and produces the following error message:

```
primary# ldm migrate ldg1 root@target-name
Target Password:
Domain ldg1 is using features of the system firmware that are not supported in
the version of the firmware running on the target machine.
Domain Migration of LDom ldg1 failed.
```

When performing a live migration from a SPARC M10 system with XCP 2210 or later, to another SPARC M10 system, make sure to update the XCP firmware of the target system to XCP 2210 or later.

- Refer to the following table to determine the feasibility of performing a live migration, based on the version of the firmware.

Table 3-8 Versions of the Firmware That Support Live Migration

Migrate To	SPARC M12	SPARC M10	SPARC M10	SPARC M10	SPARC T-series M-series	SPARC T-series M-series
Migrate From		XCP 2230 or Later	XCP 2210 or Later and Earlier Than XCP 2230	XCP 2092 or Earlier	SysFW 8.4 or Later	SysFW 8.3 or Earlier
SPARC M12	Available	Available(*2)	Available(*2)	Not available	Available(*2)	Not available
SPARC M10 (XCP 2230 or later)	Available(*2)	Available	Available(*3)	Not available	Available(*3)	Not available
SPARC M10 (XCP 2210 or later and earlier than XCP 2230)	Available(*2)	Available	Available	Not available	Available(*3)	Available(*3)
SPARC M10 (XCP 2092 or earlier)	Available(*2)	Available	Available	Available	Available(*3)	Available(*3)
SPARC T-series M-series(*1) (SysFW 8.4 or later)	Available(*2)	Available(*3)	Available(*3)	Not available	Available	Not available
SPARC T-series M-series(*1) (SysFW 8.3 or earlier)	Available(*2)	Available(*3)	Available(*3)	Not available	Available	Available

*1 It is an Oracle system, such as SPARC M5/M6, which supports Oracle VM Server for SPARC.

*2 For details, see "Notes on Live Migration of Oracle VM Server for SPARC" in the *Fujitsu SPARC M12 Product Notes*.

*3 Live migration is possible only for cpu-arch=generic.

- When a live migration is performed between a SPARC M12 system and a SPARC M10 system, the uptime of a logical domain may not be displayed correctly.

[Example]

UPTIME of a logical domain displays 6m for a SPARC M12 system.

M12#	ldm list-domain	NAME	STATE	FLAGS	CONS	VCPU	MEMORY	UTIL	NORM	UPTIME
	primary	primary	active	-n-cv-	UART	24	8G	0.8%	0.2%	21d 5h 22m
	guest	guest	active	-n----	5000	8	8G	0.6%	0.6%	6m

After a live migration is performed, UPTIME of the logical domain displays 1h11m for the SPARC M10 system.

```
M10# ldm list-domain
NAME          STATE      FLAGS    CONS    VCPU    MEMORY  UTIL   NORM   UPTIME
primary      active    -n-cv-   UART     8       8G      0.1%  0.1%  4h 18m
guest        active    -n----   5000     8       8G      0.0%  0.0%  1h 11m
```

Notes for When Recovery Mode of Oracle VM Server for SPARC is Enabled

- Suppose that you add a system board using dynamic reconfiguration of physical partitions in the condition where the domain configuration has been recovered in the degraded configuration. The added resource is not allocated automatically to any logical domain. Allocate the added resource manually. Alternatively, execute the `ldm set-spconfig` command to select the original domain configuration and then reboot the physical partition using the `poweron(8)` and `poweroff(8)` commands.
- Suppose that you dynamically delete a system board (PSB) using the `deleteboard(8)` command where the version of Oracle VM Server for SPARC is earlier than 3.2, after the domain configuration is recovered in the degraded configuration. This `deleteboard(8)` command may fail. After a domain configuration is recovered in the degraded configuration, do not delete a system board using dynamic reconfiguration of physical partitions.
- Suppose the firmware is updated to XCP 2230 or later and the recovery mode supported in Oracle VM Server for SPARC 3.1 or later is enabled. If the physical partition is started with the logical domain configuration information created with XCP 2221 or earlier after those operations, the following message may be output to the console of the physical partition.

```
warning: Configuration 'xxxx' could not be imported from the
system controller.
```

This is because the recovery mode of the logical domain configuration information created with XCP 2221 or earlier is not enabled. Execute the `ldm add-spconfig` command of Oracle VM for SPARC on the control domain and save the logical domain configuration information.

Support for the Acceleration of Encryption Methods by SPARC M10 Systems

SPARC M10 systems provide acceleration for some of the encryption methods supported by Oracle Solaris 11.

The following table lists the encryption methods for which SPARC M10 systems support acceleration.

Table 3-9 Support for the Acceleration of Encryption Methods

Encryption Method	Providing OS
RSA	Oracle Solaris 11.2 (*1)
DSA	Oracle Solaris 11.2 (*1)
DES	Oracle Solaris 11.1
3DES	Oracle Solaris 11.1
AES	Oracle Solaris 11.1
DH	Oracle Solaris 11.2 (*1)
SHA1	Oracle Solaris 11.1
SHA256	Oracle Solaris 11.1
SHA384	Oracle Solaris 11.1
SHA512	Oracle Solaris 11.1
SHA224	Oracle Solaris 11.1

*1 These methods are further improved in function in SPARC64 X+.

Notes on Using OpenSSL

Oracle Solaris provides accelerated cryptographic libraries for SPARC M10 systems. These libraries can be used by using the PKCS11 engine of OpenSSL. For details, see the man pages `openssl(5)`, `engine(3openssl)`, and `evp(3openssl)`.

Please note:

- The PKCS11 engine is the only way in OpenSSL to obtain the acceleration of cryptographic functions from the encryption arithmetic unit of the SPARC64 X+/SPARC64 X processor.
- The implementation of the PKCS11 engine for OpenSSL in Oracle Solaris requires the enabling of the EVP model for digest and encryption methods supported by the engine.
 - The following digest methods have been optimized for the SPARC64 X+/SPARC64 X processor:
SHA1, SHA224, SHA256, SHA384, SHA512
 - The following encryption methods have been optimized for the SPARC64 X+/SPARC64 X processor:
DES-CBC, DES-EDE3-CBC, DES-ECB, DES-EDE3
AES-128-CBC, AES-192-CBC, AES-256-CBC
AES-128-ECB, AES-192-ECB, AES-256-ECB
AES-128-CTR, AES-192-CTR, AES-256-CTR

Here is an example of calling the accelerated version of the AES-256-CBC method on the SPARC64 X+/SPARC64 X processor.

```
# openssl speed -engine pkcs11 -evp AES-256-CBC
```

- The following public key encryption methods have been optimized for the SPARC64 X+/SPARC64 X processor from Oracle Solaris 11.2.
RSA512, RSA1024, RSA2048
DSA512, DSA1024, DSA2048
The command in the following example invokes the RSA2048 method optimized for the SPARC64 X+/SPARC64 X processor.

```
# openssl speed -engine pkcs11 rsa2048
```

- To use the optimized digest method or encryption method in the PKCS11 engine with an application using the OpenSSL library (libssl, libcrypto), enable the EVP interface explained in `evp(3openssl)`.

Notes and Restrictions on Oracle Solaris 11.4

Notes

- There are notes on live migration of Oracle VM Server for SPARC. See "[Notes on Live Migration of Oracle VM Server for SPARC.](#)"
- There are notes on dynamic reconfiguration of physical partitions. See "2.5.2 Considerations in System Operation for Dynamic Reconfiguration" in the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Domain Configuration Guide*.
- There are notes on migration of Oracle Solaris kernel zones. See "8.2.3 Notes on Oracle Solaris Kernel Zones" in the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide*.

Restrictions

- The Boot Pools function is not supported at present.
- The automated installer with HMAC-SHA256 is not supported at present.

Notes and Restrictions on Oracle Solaris 11.3

Notes

- There are notes on live migration of Oracle VM Server for SPARC. See "[Notes on Live Migration of Oracle VM Server for SPARC.](#)"
- There are notes on dynamic reconfiguration of physical partitions. See "2.5.2 Considerations in System Operation for Dynamic Reconfiguration" in the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Domain Configuration Guide*.

- There are notes on migration of Oracle Solaris kernel zones. See "8.2.3 Notes on Oracle Solaris Kernel Zones" in the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide*.
- Live migration of an Oracle Solaris kernel zone configured with ZOSS NFS may cause bug 20697332 in Oracle Solaris to occur. For details on bug 20697332, see the *Oracle Solaris 11.3 Release Notes*. This issue has been resolved in SRU 11.3.7.5.0 and later.

Restrictions

The Boot Pools function is not supported at present.

Notes on the Remote Maintenance Service

Note - Enhanced Support Facility (ESF) and Remote Customer Support System (REMCS) are supported only for SPARC M10 systems sold within Japan by Fujitsu.

This section describes notes when using the remote maintenance service. See the *Enhanced Support Facility User's Guide for REMCS* for how to set and use REMCS.

Before Setting the Remote Maintenance Service

To use the remote maintenance service with SPARC M10 Systems, you must perform settings for the REMCS agent function by using XSCF Web. In addition, the REMCS agent uses XSCF time zone information. Perform the following settings in advance with the XSCF shell:

- Necessary settings for using XSCF Web, such as enabling HTTPS
- XSCF time zone setting

After completing the above settings, perform settings for the REMCS agent function by using XSCF Web.

For details on the XSCF Web settings and time zone settings, see the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide*.

Time Zone for the REMCS Agent Function

The REMCS agent uses the time zone currently set for the system. For this reason, when you change the system time zone with XSCF, set the periodical connection schedule again to update the information of REMCS center.

Notes on SNMP

- When using the SNMPv3 agent, after setting up the authentication protocol and the encryption protocol using the `setsnmp(8)` command, be sure to set up User-based Security Model (USM) management information using the `setsnmpm(8)` command and View-based Access Control Model (VACM)

management information using the `setsnmpvacm(8)` command. Specification of the authentication protocol and the encryption protocol is required in the SNMPv3 agent setup process. Moreover, a password must be entered to use the `setsnmp(8)` or `setsnmpusm(8)` command.

- If a server, on which the SNMP manager is not running, is registered as the inform trap host of SNMPv3, execution of `setsnmp(8)`, `setsnmpusm(8)` or `setsnmpvacm(8)` commands may output the "Agent restart failed" message. This message is output when there is an abnormality in the restarting of the SNMP agent, but as the SNMP agent works properly even if the message is output, it has no effect on the system. Register the trap host after the SNMP manager had been started.
- If the `setsnmp(8)` command is executed with the "addtraphost" or "addv3traphost" operand and a trap host is registered with a host name consisting of 16 or more characters, the UDP address of the trap reported to the trap host becomes the IP address assigned to the XSCF-LAN (physical IP address), instead of the takeover IP address (virtual IP address). This symptom occurs when a takeover IP address is set up.
If the host name of the trap host consists more than 16 characters, register the trap host with its IP address, not its host name.

[Workaround]

If a host name with more than 16 characters has already been registered, execute the `setsnmp(8)` command with either the "remtraphost" or "remv3traphost" operands to remove the trap host and register the trap host again with the IP address.

- When the trap host is registered with the `setsnmp(8)` command, the following message may be output.

```
iptables v1.4.7: host/network 'example.com' not found
Try 'iptables -h' or 'iptables --help' for more information.
```

This message indicates that the name resolution has not been executed for the host name of the registered host.

Although the trap host has been properly registered, traps are not reported to the trap host because the name of the trap host could not be resolved.

Set up the name server by executing the `setnameserver(8)` command and conduct name resolution for the target host.

Notes on Using the SPARC M12/M10 Plug-in for Oracle Enterprise Manager Cloud Control

The SPARC M12/M10 plug-in for Oracle Enterprise Manager Cloud Control requires Oracle Enterprise Manager Cloud Control 13.1 or later.

The SPARC M12/M10 plug-in for Oracle Enterprise Manager Cloud Control provides monitoring functionality for SPARC M12/M10 server hardware.

You can obtain the SPARC M12/M10 plug-in for Oracle Enterprise Manager Cloud Control and find the information including installation instructions at the sites listed

below. For SPARC M12/M10 systems supported by Oracle, please open an SR in My Oracle Support to access support for this plug-in.

- Global site
https://updatesite.jp.fujitsu.com/unix/en/download/software/unix_soft/emcc_plug-in/download/index.html
- Japanese site
https://updatesite.jp.fujitsu.com/unix/jp/download/software/unix_soft/emcc_plug-in/download/index.html

Problems With XCP and Workarounds

This section describes problems with XCP and workarounds for each version.

Problems That Might Occur With XCP 2352 and Workarounds

The following table lists the problems that might occur with XCP 2352 and workarounds for them.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds

RTI No.	RTIF2-170424-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	Suppose that the PCIe root complex connecting the PCI card is dynamically added to a logical domain with the <code>ldm add-io</code> command. In this case, there may be PCIe lane degradation on the PCI card. To check whether there is lane degradation, execute the <code>prtdiag</code> command of Oracle Solaris and check "Cur Speed/Width."
Workaround	There is no effective workaround. [How to restore] Restart the logical domain connected to this PCI card.
RTI No.	RTIF2-170405-001
Model	SPARC M10-4S
Description	After completing the master XSCF switchover and executing the <code>rebootxscf -a</code> command or the <code>setdate</code> command to reboot all XSCFs, hardware errors may not be detectable.
Workaround	After the master XSCF switchover has completed, wait about 20 minutes and then execute the <code>rebootxscf</code> command or the <code>setdate</code> command. [How to restore] Reboot all the XSCFs by executing the <code>rebootxscf -a</code> command.

RTI No. **RTIF2-170405-003**

Model **SPARC M10-1, SPARC M10-4, SPARC M10-4S**

Description If the poweroff -f command is executed while the physical partition (PPAR) is restarting, the next execution of the poweron command may cause an error that does not allow the PPAR to be powered on.

[Example] The PPAR cannot be powered on.

```
XSCF> poweron -p 0
```

```
PPAR-IDs to power on :00
```

```
Continue? [y|n] :y
```

```
00 : Not powering on : An internal error has occurred. Please contact your system administrator.
```

Workaround Before executing the poweroff -f command, use the showpparprogress command to check the status of the target PPAR.

If "PPAR reset" is displayed at this point, it means that the target PPAR is restarting.

The PPAR restart is completed by the time that "The sequence of power control is completed." appears.

Until this message appears, do not execute the poweroff -f command.

If "This PPAR is powered on." or "This PPAR is powered off." appears, the PPAR has not been restarted.

[Example] The PPAR restart has completed.

```
XSCF> showpparprogress -p 0
```

```
PPAR reset                    PPAR#0 [ 1/13]
```

```
:
```

```
CPU Start                    PPAR#0 [13/13]
```

```
The sequence of power control is completed.
```

```
XSCF>
```

[How to restore]

Turn off the input power to the system, wait 30 seconds, and then turn it on again.

(AC OFF/ON)

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-170405-005
Model	SPARC M10-4S
Description	<p>If you execute a command listed in [Command list] during XSCF master/standby switching, the following problems occur.</p> <ul style="list-style-type: none"> - The XSCF that executes the command stops due to "BOARD ERROR." - Moreover, you cannot power on the physical partition containing the PSB (BB) of the stopped XSCF. <p>You can confirm that the XSCF master/standby switching is completed by executing the showhardconf command to check that [Status] of XBBOX or BB is "Normal."</p> <p>[Command list] restoreconfig(8) rebootxscf(8) flashupdate(8) setdate(8)</p>
Workaround	<p>Do not execute any of the commands listed in [Command list] above during XSCF master/standby switching.</p> <p>[How to restore] Recover the system by performing the following procedure.</p> <ol style="list-style-type: none"> 1. Shut down Oracle Solaris on all logical domains. 2. Execute the poweroff -f command to forcibly power off all PPARs. 3. Turn off (AC OFF) the input power to every SPARC M10. 4. Turn on (AC ON) the input power to every SPARC M10.
RTI No.	RTIF2-170405-007
Model	SPARC M10-4S
Description	<p>If the "SCF process down detected" error occurs on the standby XSCF while the XCP firmware is being updated, the "SCF panic detected" error may occur on the master XSCF.</p>
Workaround	<p>There is no effective workaround.</p> <p>After the reboot of every XSCF has completed, execute the flashupdate command with the -f option specified to update the XCP firmware again.</p>
RTI No.	RTIF2-170405-008
Model	SPARC M10-4S
Description	<p>If a low-voltage problem in the XSCF unit occurs on the master XSCF, automatic master/standby switching may not occur.</p>
Workaround	<p>There is no effective workaround.</p> <p>If the master XSCF does not respond, execute the switchscf command with the -f option specified from the standby XSCF to forcibly switch the master/standby XSCF.</p> <p>[Example] XSCF> switchscf -t Master -f The XSCF unit switch between the Master and Standby states. Continue? [y n]:y After the master/standby switching, replace the non-responsive XSCF unit.</p>

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-170405-009
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	When executed while a POST diagnosis is in progress on a PPAR consisting of one system board (PSB), the console command may not display the console screen.
Workaround	Execute the console command before the PPAR starts or after the POST diagnosis is completed. [How to restore] For a building block configuration, restart the PPAR (power), switch the master/standby XSCF, or reboot the master XSCF. For the SPARC M10-1, the SPARC M10-4, or the 1BB configuration with the SPARC M10-4S, power off the PPAR and then power it on again.
RTI No.	RTIF2-170405-010
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	Normally, the occurrence of a memory configuration error is supposed to degrade all memory under the CPU. However, some memory has not been degraded. Powering on the PPAR at this time may register the non-degraded memory in an error log. [Example] The MEM#02B memory has been unmounted but the MEM#xxA memory is not degraded. XSCF> showstatus BB#00 Status:Normal; CMUL Status:Normal; * MEM#00B Status:Deconfigured; * MEM#01B Status:Deconfigured; * MEM#03B Status:Deconfigured; [Example] Example of an error log XSCF> showlogs error Date: Nov 22 10:57:37 JST 2016 Code: 40002000-004b830134110000ff-020014210000000000000000 Status: Warning Occurred: Nov 22 10:57:37.190 JST 2016 FRU: /BB#0/CMUL/MEM#03A,/BB#0/CMUL Msg: Failed to find write cycle adjustment value Diagnostic Code: 00000300 00000000 0000 00000001 00000000 0000
Workaround	Before powering on the PPAR, execute the showhardconf command and confirm that the memory is correctly mounted in accordance with memory mounting rules. Remount any memory that is not correctly mounted. [How to restore] Remount the memory correctly in accordance with memory mounting rules. With the mode switch in the Service mode position on the operation panel, turn on (AC ON) the input power to cancel the degradation of the memory registered with an error. Execute the showstatus command to confirm that the memory is not degraded.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-161116-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>If available CPU resources in a physical partition (PPAR) have not been assigned when a CPU Activation Interim Permit expires, powering on the PPAR causes the PPAR to be reset repeatedly without power-on processing being suppressed.</p> <p>At this time, the following event log is registered repeatedly.</p> <p>SCF:PPAR-ID x: Reset SCF:SP-Config falling back to factory-default (PPARID 0 factor:0x1010000) SCF:PPAR-ID x: Reset released</p>
Workaround	<p>After a CPU Activation Interim Permit expires, execute the <code>setinterimpermit disable</code> command to disable the CPU Activation Interim Permit.</p> <p>To power on a PPAR, assign available CPU core resources in the PPAR.</p> <p>[How to restore]</p> <p>Perform the following procedure.</p> <ol style="list-style-type: none"> 1. Execute the <code>poweroff -f</code> command to forcibly power off the PPAR that is being reset repeatedly. 2. Execute the <code>poweroff</code> command (without <code>-f</code>) to power off all PPARs other than that described above. 3. Turn off/on (AC OFF/ON) the input power to every SPARC M10. 4. Execute the <code>setinterimpermit disable</code> command to disable the CPU Activation Interim Permit.
RTI No.	RTIF2-150730-001
Model	SPARC M10-4S
Description	<p>The message "Cannot communicate with BB#xx. Please check BB#xx's state." is output when the <code>setprivileges(8)</code> command is executed.</p> <p>[Example]</p> <pre>XSCF> setprivileges jsmith fieldeng platadm useradm auditadm Cannot communicate with BB#01. Please check BB#01's state. XSCF></pre>
Workaround	<p>There is no effective workaround.</p> <p>User privileges are normally set. This message does not affect system operation.</p>
RTI No.	RTIF2-150728-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>When the <code>ioxadm(8)</code> command is executed for the first firmware update of the PCI expansion unit after shipment, the event log will display an incorrect firmware version: "last version=0000".</p> <p>[Example of the event log]</p> <pre>May 28 11:27:40 Event: SCF:LINKCARD update is started (LINKCARD=0, bank=1, PCIBOX version=1200: last version=0000)</pre>
Workaround	<p>There is no effective workaround.</p> <p>Ignore this version in the event log. This problem does not affect system operation.</p>

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-150629-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>When using the Africa/Casablanca time zone, executing showtimezone -c dst-m standard does not display daylight saving time but outputs the following message: An internal error has occurred. Please contact your system administrator. This is a problem with the command output only. Daylight saving time is set as follows twice a year. Starts March 29, 2015 and ends June 13, 2015 Starts July 18, 2015 and ends October 25, 2015</p>
Workaround	There is no effective workaround.
RTI No.	RTIF2-150629-002
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>The log of "Event: SCF:XCP update has been completed" may be registered at the XCP firmware update time, indicating a successful firmware update. However, the firmware may not actually have been updated in some SPARC M10 system chassis or crossbar boxes.</p>
Workaround	<p>There is no effective workaround.</p> <p>In either of the following cases about the registered logs at the XCP firmware update time (logs from "SCF:XCP update is started (XCP version=xxxx:last version=yyyy)" to "SCF:XCP update has been completed (XCP version=xxxx:last version=yyyy)"), update the XCP firmware again.</p> <ul style="list-style-type: none"> - "SCF:XSCF update is started (BBID=x, bank=y)" is not logged twice for each SPARC M10 chassis. - Either of the following logs is registered, indicating a connected SPARC M10 system chassis has an error. <p>[Example 1] XSCF> showlogs monitor -r Alarm: /XBBOX#81/XSCFU:SCF:XSCF hang-up is detected</p> <p>[Example 2] XSCF> showlogs monitor -r Notice: /FIRMWARE,/BB#0/CMUL:SCF:SCF panic detected</p>
RTI No.	RTIF2-150226-002
Model	SPARC M10-4S
Description	CHECK LED of the old master XSCF blinks during the master/standby switchover of XSCFs.
Workaround	<p>There is no effective workaround.</p> <p>Ignore the blinking CHECK LED of the old master XSCF during the master/standby switchover of XSCFs.</p>

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-150218-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>Suppose you execute any of the following on a system with a PCI expansion unit connected when the power to the physical partition is on. In such a case, an SNMP Trap related to the addition of the PCI expansion unit or the link card is sent by mistake.</p> <ul style="list-style-type: none"> - Reset the XSCF - Switch the master/standby XSCF - Change the SNMP agent from the disabled state to the enabled state - Set the SNMP agent management information when the SNMP agent is enabled <p>In such a case, the following SNMP Traps are sent:</p> <ul style="list-style-type: none"> - PCI expansion unit addition scfPciBoxEvent scfTrapEventType=add(10) - Link card addition scfComponentEvent scfTrapEventType=add(10) <p>Similarly, the following SNMP Trap on the PCIe card addition is sent by mistake in a system with a PCIe card connected.</p> <ul style="list-style-type: none"> scfComponentEvent scfTrapEventType=add(10)
Workaround	<p>There is no effective workaround.</p> <p>This incorrectly sent SNMP Trap does not affect the behavior of the PCI expansion unit or the PCIe card.</p>

RTI No.	RTIF2-141204-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>If the guest domain has been kept active for a long time, powering off and then powering on a physical partition (PPAR) may cause the guest domain time to deviate.</p> <p>This phenomenon occurs under the following conditions.</p> <ul style="list-style-type: none"> - A guest domain is configured (*1), and - a long period of time passes after the ldm add-sponfig command is executed from Oracle VM Server for SPARC (*2), and - a physical partition is powered on or reset <p>*1 Time deviation does not occur on the control domain. *2 Time deviation comes to about 20 seconds per month.</p>
Workaround	<p>Immediately before powering off a physical partition or resetting it, execute the ldm addspconfig from Oracle VM Server for SPARC, and store the latest guest domain configuration information on the XSCF.</p> <p>[How to restore] If the guest domain time deviates, boot Oracle Solaris in single-user mode, and then synchronize the time.</p> <p>[Example] Setting of 18:30:00 on June 27, 2014 # date 0627183014.00 # date 0627183014.00</p>

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140804-002
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>Although the showstatus(8) command is executed when there are no faulty components, the message "No failures found in System Initialization.", indicating that there are no faulty components, does not appear.</p> <p>[Example] XSCF> showstatus XSCF></p>
Workaround	<p>There is no effective workaround.</p> <p>If no message appears, there are no faulty components. You can continue system operation.</p>
RTI No.	RTIF2-140616-001
Model	SPARC M10-1
Description	<p>The showhardconf(8) command executed on a SPARC M10-1 does not display [Type] for the power supply unit (PSU). Either "Type: A" or "Type: B" should be displayed as the PSU type. The meaning of each value of "Type" is as follows:</p> <ul style="list-style-type: none"> - Type: A: PSU for SPARC64 X - Type: B: PSU for SPARC64 X+
Workaround	<p>When you execute the showhardconf(8) command, it displays "FRU-Part-Number: CAXXXXXXXXX-X/xxxxxxx;" as part of the PSU information. You can determine the PSU type by checking the value of "CAXXXXX-XXXX-X" in this information.</p> <ul style="list-style-type: none"> - If the value of CAXXXXX-XXXX-X is "CA01022-0750-M" Type: A: PSU for SPARC64 X - If the value of CAXXXXX-XXXX-X is "CA01022-0751-M" Type: B: PSU for SPARC64 X+ <p>If multiple PSU types are mounted mixed, you can determine the PSU type by checking the value of "XXXXXX" in the error log "Code:80000000-XXXXXX0000ff0000ffxxxxxxxx00000000000000" output by the showlogs error command.</p> <ul style="list-style-type: none"> - If the value of XXXXXX is "002400" Type: A: PSU for SPARC64 X - If the value of XXXXXX is "002401" Type: B: PSU for SPARC64 X+
RTI No.	RTIF2-140616-002
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>When the procedure below is executed, the error message "An internal error has occurred. Please contact your system administrator." is displayed at execution of the prtfru(8) command and the command abnormally terminates.</p> <ol style="list-style-type: none"> 1. Turn on the input power and then execute the rebootxscf(8) or switchscf(8) command to start or reset the XSCF. 2. Execute the snapshot(8) command. 3. Execute the prtfru(8) command.
Workaround	<p>After starting or resetting the XSCF, execute the prtfru(8) command before executing the snapshot(8) command.</p> <p>[How to restore] Execute the rebootxscf -a command to reset all XSCFs.</p>

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140606-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	Suppose that a cluster system is built to fulfill the following condition: it is composed of multiple SPARC M10 system chassis, each of which includes 10 or more guest domains (10 or more cluster nodes) running in 1 physical partition (PPAR). Moreover, PRIMECLUSTER software is installed on each of these guest domains. Alternatively, the cluster system is composed of multiple PPARs within the SPARC M10 system chassis. If you execute the poweroff -f command on one PPAR to forcibly turn off the power to that PPAR, the XSCF may slow down, panic, and then reset.
Workaround	Confirm that less than 10 cluster nodes are included in one PPAR that exists on an instance of the SPARC M10-1, SPARC M10-4, or SPARC M10-4S. [How to restore] The system will be available as is because processing of the poweroff(8) command continues after an XSCF panic and reset.

RTI No.	RTIF2-140606-004
Model	SPARC M10-4S
Description	Suppose that you execute the addboard(8) and reset por commands to add a system board (PSB) to a physical partition (PPAR) that fulfills both of the conditions below. You may see an error message "No analytical target" repeatedly. Also, Oracle Solaris running on this PPAR may hang. <ul style="list-style-type: none"> - The SPARC M10-4S (including a crossbar box) has a building-block configuration. - Power is supplied only to a PPAR made up of one PSB.
Workaround	To add a PSB to a PPAR that fulfills both of the conditions above, use one of the following methods: <ul style="list-style-type: none"> - Turn off the power to the PPAR, and then execute the addboard(8) command to add a PSB. - After executing the addboard(8) command to add a PSB, turn off the power to the PPAR, turn it on again, and then rebuild the PPAR. - Using the DR function, execute the addboard(8) command to add a PSB. [How to restore] <ul style="list-style-type: none"> - If the error message "No analytical target" is not displayed Execute the poweroff(8) command to turn off the power to the PPAR, and then execute the poweron(8) command to turn on the power to the PPAR. - If the error message "No analytical target" is displayed Execute the poweroff -f command to forcibly turn off the power to the PPAR and then execute the poweron(8) command to turn on the power to the PPAR.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140606-008
Model	SPARC M10-4S
Description	<p>Suppose you attempt to use dynamic reconfiguration of physical partitions (PPARs) to add a system board (PSB) with the addboard -c configure command or disconnect a PSB with the deleteboard -c unassign or deleteboard -c disconnect command. If your attempt satisfies any of the following conditions, switching between master and standby XSCFs occurs.</p> <p>If the rebooting XSCF in the following conditions is the standby XSCF, it becomes the master XSCF after the switching. The previous master XSCF is reset and deactivated.</p> <p>[Condition]</p> <ul style="list-style-type: none"> - In the case of addboard <ul style="list-style-type: none"> - The XSCF for the PSB to be added is rebooting. - The PSB is to be deleted from a PPAR, and the XSCF for any of the PSBs making up the PPAR is rebooting. - In the case of deleteboard <ul style="list-style-type: none"> - The XSCF for one of the PSBs making up the PPAR from which the PSB is to be deleted is rebooting.
Workaround	<p>If there is a standby XSCF that fulfills one of the conditions above, execute the addboard(8) or deleteboard(8) command after the standby XSCF has rebooted.</p> <p>Check whether the XSCF has rebooted, by executing the showhardconf(8) command to confirm that the [Status] of the SPARC M10-4S chassis (BB#xx) that includes the XSCF is "Normal".</p> <p>[Example]</p> <ul style="list-style-type: none"> - The XSCF at BB#02 is running. <pre>XSCF> showhardconf SPARC M10-4S; --- Omitted --- BB#02 Status:Normal; Role:Slave; Ver:2220h; Serial:1234567890;</pre> <ul style="list-style-type: none"> - The XSCF at BB#02 is rebooting. <pre>XSCF> showhardconf SPARC M10-4S; --- Omitted --- BB#02 Status:Cannot communicate;</pre> <p>[How to restore]</p> <p>Even if switching between master and standby XSCFs occurs, you can continue using the system because the addboard(8) or deleteboard(8) command has been executed correctly.</p> <p>If the previous master XSCF has stopped, execute the following procedure:</p> <ol style="list-style-type: none"> 1. Execute the poweroff -a command to turn off the power to all PPARs. 2. Turn off the input power to the SPARC M10-4S chassis configured on the master and standby XSCFs, and then turn it on again. 3. Log in to the master XSCF and execute the showhardconf(8) command to confirm that the [Status] of the SPARC M10-4S chassis for the standby XSCF is "Normal".

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No. **RTIF2-140605-001**

Model **SPARC M10-4S**

Description Suppose the following scenario. You turn on the power to a physical partition (PPAR) that consists of multiple SPARC M10-4S chassis. The PPAR/domain enters a state that is between self-diagnosis test (POST) completion (Initialization Complete) and OpenBoot PROM start completion (OpenBoot Running). If the XSCF in a SPARC M10-4S in the PPAR resets in this state, you may be unable to switch to the control domain console.

You can confirm that this event has occurred by executing the showlogs event command and confirming that an event log for console path switching was registered during the time between POST diagnosis completion and OpenBoot PROM start completion.

[Example] Event log message issued when PPAR_ID is 0
POST Diag complete from PPAR (PPAR ID 0)
--- Omitted ---
PPAR ID 0: Console path is switched
--- Omitted ---
PPARID 0 GID 00000000 state change (OpenBoot Running)

Workaround There is no effective workaround.

[How to restore]
Perform one of the following operations:

- Execute the rebootxscf -a command.
- From XSCF Web, reset all XSCFs.
- Turn off the power to the PPAR, and then turn it on again.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140605-002
Model	SPARC M10-4S
Description	<p>Suppose that all but one of the system boards (PSBs) that make up a physical partition (PPAR) are faulty. If you attempt to use the DR function to disconnect the normal PSB by executing the deleteboard(8) command, this command times out and terminates abnormally. In addition, if you execute the deleteboard(8) command using the DR function to disconnect another PSB in the same PPAR, the command always times out. Furthermore, you can no longer connect to the control domain console by executing the console(8) command.</p> <p>[Example] deleteboard(8) command timeout</p> <pre>XSCF> deleteboard -c disconnect 00-0 PSB#00-0 will be unconfigured from PPAR immediately. Continue?[y n] :y All domains are temporarily suspended, proceed?[y n] :y Start unconfigure preparation of PSB. [1200sec] 0..... 30..... 60..... 90.....120.....150.....180.....210.....240..... \ 270.....300.....330.....360.....390.....420.....450.....480.....510..... \ 540.....570.....600.....630.....660.....690.....720.....750.....780..... \ 810.....840.....870.....900.....930.....960.....990.....1020.....1050.....- 1080.....1110.....1140.....1170.....end Timeout detected during unconfiguration of PSB#00-0. XSCF></pre>
Workaround	<p>If all but one of the PSBs are faulty, first turn off the power to the PPAR, and then execute the deleteboard(8) command to disconnect the PSB.</p> <p>[How to restore]</p> <p>Perform the following procedure.</p> <ol style="list-style-type: none"> 1. Execute the rebootxscf -a command to reset the XSCF. 2. Check the error logs and messages to identify the cause of the error. 3. Eliminate the cause of the error. 4. Perform the operation described in "6.3.1 Example of Operations for Deleting a System Board Assignment" or the operation described in "6.3.3 Example of Operations for Reserving a System Board Unassignment" in the <i>Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 Domain Configuration Guide</i>.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140605-006
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>When the OS panics, a large volume of panic messages may be sent to the XSCF. In this case, the XSCF cannot handle the large volume of panic messages. As a result, the codd process fails and OS-panic error logs are registered in large quantities as shown below.</p> <p>[Example] OS panic and process failure error logs</p> <pre>XSCF> showlogs error -v Date: Dec 20 14:44:26 JST 2013 Code: 40000000-00ffff0000ff0000ff-01b900060000000000000000 Status: Warning Occurred: Dec 20 14:44:26.513 JST 2013 FRU: /UNSPECIFIED Msg: XSCF command: System status change (OS panic) (PPARID#00, path: 00) Diagnostic Code: 00000000 00000000 0000 00000000 00000000 0000 00000000 00000000 0000 00000000 00000000 00000000 00000000 00000000 00000000 0000 Date: Dec 20 15:00:01 JST 2013 Code: 20000000-00fcff00b0000000ff-010400010000000000000000 Status: Notice Occurred: Dec 20 14:59:56.838 JST 2013 FRU: /FIRMWARE,/XBBOX#81/XSCFU Msg: SCF process down detected Diagnostic Code: 00000000 00000000 0000 51000000 00000000 0000 00000000 00000000 0000 636f6464 2e323537 382e627a 32000000 00000000 00000000 0000 You can confirm this codd condition by checking for "636f6464" in the first four bytes on the fourth line of [Diagnostic Code:].</pre>
Workaround	<p>There is no effective workaround.</p> <p>[How to restore]</p> <p>The system is restored when the XSCF is reset by codd process failure.</p>

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140602-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>When the input power of a PCI expansion unit is turned off (AC OFF), an error log is registered, but notification through SNMP trap or REMCS is not performed. Confirmation that the input power of a PCI expansion unit had been turned off can be seen in the output of the showlogs error command, like the following:</p> <p>Date: May 29 20:03:05 JST 2014 Code: 10000400-00d4000000ff0000ff-1100002a0000000000000000 Status: Information Occurred: May 29 20:03:00.905 JST 2014 FRU: /BB#0/PCI#8/PCIBOX#2003/PSU#0 Msg: AC FAIL Diagnostic Code: 00083230 30330000 0000 00080000 00000000 0000 00080000 00000000 0000 00000000 00000000 00000000 00000000 00000000 00000000 0000</p>
Workaround	There is no effective workaround.
RTI No.	RTIF2-140507-003
Model	SPARC M10-4S
Description	After adding or removing a system board with the dynamic reconfiguration of physical partitions (PPAR), if the PPAR is powered off, the output of the showpparprogress(8) command mistakenly shows the status of the procedure of powering on the system.
Workaround	<p>There is no effective workaround.</p> <p>[How to restore] After the "PPAR-ID x: Reset" message is output to the event log, re-execute the showpparprogress(8) command for the right display.</p>
RTI No.	RTIF2-140507-005
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	If a failure is detected in DC-DC converter (DDC), "Power subsystem failure" may be registered twice in the error log.
Workaround	<p>There is no effective workaround.</p> <p>Ignore the second of these entries in the error log. Moreover, replace the faulty FRU as soon as possible.</p>
RTI No.	RTIF2-140507-006
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	When error logs (Critical ultrahigh temperature at XXX) at the time of component abnormalities due to high temperature (ultrahigh temperature level) are registered and the system is forcibly stopped, if the showlogs power command is executed, the "Cause" column to the power log may show "-".
Workaround	There is no effective workaround.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140507-007
Model	SPARC M10-4, SPARC M10-4S
Description	After a DC-DC converter (DDC) fault has been detected, if the system is run continuously, without replacing the FRU that appears in the error logs, the following error is mistakenly detected every time the input power is turned off. Msg: Power-off failure
Workaround	Replace the faulty FRU. Moreover, as this error message has no effect on the functioning of the system, ignore it.
RTI No.	RTIF2-140507-009
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	Even when rotation abnormality is found on both fans at the same time in the same fan unit, only one fan is registered in the error log and thus the system is not shut down. As a result, the temperature of components, like CPUs, may rise.
Workaround	There is no effective workaround. [How to restore] If the system is not shut down when fan rotation abnormality is detected, if any of the following symptom is confirmed in the two fans which comprises the faulty fan unit, change the following fan unit as soon as possible: - The showenvironment fan command shows that the number of rotations has decreased. - A visual check reveals that both fans stopped at the same time.
RTI No.	RTIF2-140507-014
Model	SPARC M10-4S
Description	When the replacefru(8), addfru(8), or rebootxscf(8) command is executed, the "XSCF hang-up is detected" error may be detected by the slave XSCF.
Workaround	There is no effective workaround. Ignore this error log message since the command in question ended normally and thus has no effect on the system.
RTI No.	RTIF2-140507-016
Model	SPARC M10-4S
Description	After configuring the XSCF network with the setnetwork(8) command and applying the configuration by the applynetwork(8) command, if you execute the shownetwork(8) command without executing the rebootxscf(8) command, the displayed IP address may not match the IP address that is being used on the present system.
Workaround	After executing the applynetwork(8) command to apply network configurations of the XSCF, execute the rebootxscf(8) command to reset the XSCF.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140507-021
Model	SPARC M10-4S
Description	In the middle of a diagnosis performed by the diagxbu(8) command with the -p and -b options specified, if a failure occurs in the physical partition specified by the -p option, the diagxbu(8) command may terminate abnormally, and power-off of the SPARC M10 chassis specified by the -b option may fail.
Workaround	There is no effective workaround. [How to restore] Perform the following procedure. 1. Turn off the input power (AC OFF) of the SPARC M10 chassis specified by the -b option of the diagxbu(8) command. 2. Select the SPARC M10 chassis by the "-b" option of the replacefru(8) command. 3. When the message for the replacement of the SPARC M10 chassis is output in the maintenance menu, turn on the input power (AC ON) of the SPARC M10 chassis without replacing it.
RTI No.	RTIF2-140507-022
Model	SPARC M10-4S
Description	When the crossbar cable of the crossbar unit (XBU) in the SPARC M10-4S has a bad electrical contact, the crossbar cable components are depicted with a number of "#"s by the showhardconf(8) command. [Example] When the crossbar cable is loose: + FRU-Part-Number:#####; Ver:####h; + Type:#####; Length: #;
Workaround	There is no effective workaround. [How to restore] Perform the following procedure. 1. Power off the physical partition (PPAR) by the poweroff(8) command. 2. Check the physical state of the crossbar cable (loose connection, disconnection, etc.) whose component information is depicted by a number of "#"s. 3. Execute the poweron(8) command to power on the PPAR. 4. Check the component information of the target crossbar cable with the showhardconf(8) command. [Example] When the crossbar cable is properly connected: + FRU-Part-Number:2123628-2 ; Ver:3920h; + Type:Optic; Length: 2;
RTI No.	RTIF2-140410-002
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	If, during execution of the restoreconfig(8) command, you power on or off the physical partition (PPAR) or execute diagnosis of the system board, the operations in progress are interrupted.
Workaround	During execution of the restoreconfig(8) command, do not power on or off the physical partition (PPAR) or execute diagnosis of the system board. [How to restore] - If the PPAR power on process is interrupted Forcibly power off the PPAR using the poweroff -f command. - If the PPAR power off process or system board diagnosis is interrupted Turn off the input power to all SPARC M10 system chassis or crossbar box chassis, and then turn it on again (AC-OFF/ON).

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140410-003
Model	SPARC M10-4S
Description	<p>With the system input power turned off, if the CPU memory unit lower (CMUL) is replaced or the SPARC M10-4S is added without using the maintenance menu, the following error log may be registered when automatic synchronization of XCP firmware versions is enabled.</p> <p>Alarm: :SCF:Gaps between XBBOX-ID Or Information: :SCF:Gaps between BB-ID</p>
Workaround	<p>There is no effective workaround. Ignore this error log entry.</p>
RTI No.	RTIF2-140410-008
Model	SPARC M10-4S
Description	<p>After the input power is turned on with the XSCF DUAL control cable disconnected or faulty, data between the master and standby XSCFs is not synchronized even if the XSCF DUAL control cable is restored.</p> <p>System operation can continue. However, after master/standby XSCF switching, normal system operation is not guaranteed. This is because information in the old master XSCF is not reflected in the new XSCF.</p> <p>You can check, with the following error log messages, whether the XSCF DUAL control cable is disconnected or faulty:</p> <ul style="list-style-type: none"> - XSCF DUAL control cable is disconnected <p>Msg: BB control cable detected unexpected</p> <ul style="list-style-type: none"> - XSCF DUAL control cable is faulty <p>Msg: Cannot communicate with the other XSCF</p>
Workaround	<p>Before turning on the input power, confirm that the XSCF DUAL control cable is correctly inserted.</p> <p>Also, use the showlogs error command to confirm that the error logs shown in [Description] are not registered.</p> <p>[How to restore]</p> <p>If the XSCF DUAL control cable is disconnected, make sure that it is properly connected. Then, execute the rebootxscf -a command to reboot all XSCFs.</p> <p>If the XSCF DUAL control cable is faulty, replace the cable.</p>
RTI No.	RTIF2-140410-009
Model	SPARC M10-4S
Description	<p>If the input power to the standby or slave chassis is turned off, a "Board control error (MBC link error)" error log may be registered.</p>
Workaround	<p>There is no effective workaround. Ignore this error log entry.</p>
RTI No.	RTIF2-140409-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>An error may be detected in the Realtime Clock (RTC) built into the SPARC M10 system and XSCF may not start.</p>
Workaround	<p>There is no effective workaround. [How to restore] Turn off the input power to the system and then turn it on again (AC OFF/ON).</p>

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140407-005																
Model	SPARC M10-4, SPARC M10-4S																
Description	<p>Suppose that the CPU memory unit lower (CMUL) or CPU memory unit upper (CMUU) has a faulty DC-DC converter (DDC) and the system operates continuously without the CMUL/CMUU being replaced. In this case, CPU-related failures may not be detected, even if an error occurs on a CPU mounted in the CMUL/CMUU with the faulty DDC, and the physical partition (PPAR) may hang.</p> <p>For a DDC failure, check whether any of the following messages are registered in the error log.</p> <p>Msg: Critical low voltage error</p> <p>Or</p> <p>Msg: Critical high voltage error</p> <p>Or</p> <p>Msg: Power subsystem failure</p>																
Workaround	Immediately replace the CMUU or CMUL where the DDC failure has occurred.																
RTI No.	RTIF2-140407-006																
Model	SPARC M10-4S																
Description	<p>For a SPARC M10-4S chassis with the XSCF stopped, or with the input power off (AC OFF), even if you execute the <code>initbb -f</code> command to forcibly disconnect the chassis from the system, the chassis remains assigned to the physical partition (PPAR).</p> <p>To check this phenomenon, execute the <code>showboards(8)</code> command to check whether Assignment of the target SPARC M10-4S chassis (PSB) remains "Assigned".</p> <p>XSCF> showboards -av</p> <pre>PSB R PPAR-ID(LSB) Assignment Pwr Conn Conf Test Fault -----</pre> <table border="0"> <tbody> <tr> <td>00-0</td> <td>00(00)</td> <td>Assigned</td> <td>y</td> <td>y</td> <td>y</td> <td>Passed</td> <td>Normal</td> </tr> <tr> <td>01-0</td> <td>* 00(01)</td> <td>Assigned</td> <td>n</td> <td>n</td> <td>n</td> <td>Unmount</td> <td>Normal</td> </tr> </tbody> </table>	00-0	00(00)	Assigned	y	y	y	Passed	Normal	01-0	* 00(01)	Assigned	n	n	n	Unmount	Normal
00-0	00(00)	Assigned	y	y	y	Passed	Normal										
01-0	* 00(01)	Assigned	n	n	n	Unmount	Normal										
Workaround	<p>There is no effective workaround.</p> <p>[How to restore]</p> <p>Perform the following on the disconnected SPARC M10-4S chassis.</p> <ol style="list-style-type: none"> Execute the <code>replacefru(8)</code> command to replace the chassis. Power on (AC ON) the replacement chassis. <ul style="list-style-type: none"> If the READY LED of the XSCF or XSCF unit is lit <ol style="list-style-type: none"> Complete <code>replacefru(8)</code> command execution by following the maintenance menu. Execute the <code>initbb(8)</code> command to disconnect the chassis from the system. If the READY LED of the XSCF or XSCF unit is not lit <ul style="list-style-type: none"> Remove the chassis and replace the XSCF unit or CPU memory unit lower (CMUL) where a failure is suspected. 																
RTI No.	RTIF2-140407-008																
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S																
Description	If the <code>poweroff(8)</code> command is executed and the master chassis XSCF is reset during the time that it takes for a prompt to be returned, the following power on/power off operation cannot be executed.																
Workaround	<p>There is no effective workaround.</p> <p>If this phenomenon occurs, turn off the input power to all chassis and then turn it on again.</p>																

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140304-001
Model	SPARC M10-4S
Description	When performing AC OFF/ON, in some very rare cases, the "SCF process down detected" error message is registered and XSCF fails to start.
Workaround	There is no effective workaround. If this symptom has occurred, contact a field engineer.
RTI No.	RTIF2-140304-002
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	The "SCF process down detected" error log may be registered when the flashupdate(8) command is running.
Workaround	There is no effective workaround. [When restoration is not required] If both of the following conditions are met, there is no need for restoration. It may be assumed that the firmware update has completed properly. <ul style="list-style-type: none"> - The showlogs error -rv command reveals that the following diagnostic message along with the "Msg: SCF process down detected" error message had been registered when the flashupdate(8) command was running. <ul style="list-style-type: none"> [Example] Diagnostic Code: 00000000 00000000 0000 00000000 00000000 0000 00000000 00000000 0000 666c6173 68757064 6174652e xxxxxxxx 00000000 00000000 0000 where, xxxxxxxx is indefinite - The showlogs event command displays the "XCP update has been completed" event log. [How to restore] Re-execute the flashupdate(8) command.
RTI No.	RTIF2-140304-003
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	With SNMPv3, if the trap host name registered by the setsnmp(8) command contains a colon (:), it does not show up properly with the showsnmp(8) command. [Example] In case the trap host name is "test:example.com", the host name is shown as "test" and port number is shown as "0".
Workaround	With SNMPv3, do not execute the setsnmp(8) command to register a trap host name that contains a colon (:). If such a trap host name has already been registered, use the following command to remove the trap host name: <pre>setsnmp remv3traphost -u 'user_name' -p 'port_number' 'trap_host_name'</pre> In such a case, be sure to specify a port number. If the port number is not specified when removing a trap host name that includes a colon(:), the "Entry does not exist" message is displayed and the trap host name is not removed. The port number specified at the time of removal should be the one specified at the time of registration, and not the wrong one displayed by the showsnmp(8) command.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140304-004
Model	SPARC M10-4S
Description	<p>In the case of a SPARC M10-4S that is attached to a crossbar box, if an XSCF failure occurs on a portion of the chassis when the physical partition (PPAR) is in the powered on state, the following symptom may occur.</p> <ul style="list-style-type: none">- Symptom 1 When the poweroff(8) command is executed, the power on the PPAR is turned off, but the command prompt does not respond for about 20 minutes.- Symptom 2 When the PPAR is powered on, the "XB-XB interface fatal error" error occurs, the power-on process is repeated, and it does not end normally.
Workaround	<p>If an XSCF failure has occurred, replace the CPU memory unit lower (CMUL) or the XSCF unit. [How to restore]</p> <ul style="list-style-type: none">- Case of symptom 1 After about 20 minutes, the poweroff(8) command ends properly and the prompt comes back.- Case of symptom 2 Forcibly power off the PPAR using the poweroff -f command.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140304-005
Model	SPARC M10-4S
Description	<p>On a physical partition (PPAR) that is composed of several SPARC M10-4S chassis (system boards), after a system board has been degraded due to failure, if you execute the setpciboxdio(8) command to disable/enable the direct I/O feature of the PCI cards mounted in the PCI expansion unit attached to the degraded chassis without powering off the PPAR first, the following message is output and the command fails:</p> <p>This operation cannot be done because the PPAR including a PSB of the target BB is powered on.</p> <p>This symptom occurs when a system board is in a state like the following, which is shown by either the showhardconf(8) or showboards(8) command.</p> <p>[Example] When PSB#01-0 (BB#01) has been degraded.</p> <pre>XSCF> showhardconf ... * BB#01 Status:Deconfigured; ... XSCF> showboards -a PSB PPAR-ID(LSB) Assignment Pwr Conn Conf Test Fault --- ----- 01-0 00(01) Assigned n n n Passed Faulted</pre>
Workaround	<p>Change the configuration of the direct I/O feature with the setpciboxdio(8) command only after removing the degraded system board from the physical partition with the deleteboard(8) command.</p> <p>[Example]</p> <pre>XSCF> deleteboard -c unassign 01-0</pre> <p>After changing the configuration of the direct I/O feature, assign the system board to the PPAR with the addboard(8) command, and then incorporate the system board into the PPAR by following the maintenance procedure.</p> <p>[Example]</p> <pre>XSCF> addboard -c assign -p 0 01-0</pre> <p>Meanwhile, from XCP 2210 onwards, this problem can be solved by performing maintenance on the degraded chassis by the replacefru(8) command. In such a case, the above procedure is unnecessary.</p>
RTI No.	RTIF2-140304-006
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>When the power supply unit (PSU) fails due to any of the following errors, and after active replacement or hot replacement of the PSU by the replacefru(8) command, if any of the following errors re-occur on the PSU of the same chassis, no error log is registered.</p> <ul style="list-style-type: none"> - PSU shortage (power off started) - PSU shortage - Wrong PSU is installed
Workaround	<p>Execute the replacefru(8) command to perform cold replacement if a PSU is replaced for the first time after any of the error logs shown in [Description] is registered. In such a case, do not perform active replacement or hot replacement.</p>

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140304-007
Model	SPARC M10-4S
Description	When replacing power supply units (PSUs) with the <code>replacefru(8)</code> command, if the [f] key in the menu of the <code>replacefru(8)</code> command is pressed within 30 seconds after mounting the new PSU, any problem in the new PSU cannot be detected.
Workaround	See " Workaround for RTIF2-140304-007. "
RTI No.	RTIF2-140227-005
Model	SPARC M10-4S
Description	At the time of cold replacement or cold addition of the CPU memory unit lower (CMUL) or a SPARC M10 chassis, if the following conditions are met, the "XCP firmware version synchronization failed" message may be registered in the event log, and maintenance or addition fails. <ul style="list-style-type: none"> - Two or more CMUL or SPARC M10 chassis are cold replaced or cold added at one time. - The XCP versions of replacement components do not match the version of the master XCP.
Workaround	When cold replacing or cold adding two or more CMUL or SPARC M10 chassis, use the <code>replacefru(8)</code> or <code>addfru(8)</code> command to perform the operations one by one. [How to restore] Execute any of the following procedures. <ul style="list-style-type: none"> - Procedure 1 <ol style="list-style-type: none"> 1. Perform AC OFF/ON. 2. Execute the <code>flashupdate(8)</code> command, specifying the XCP version. <pre>XSCF> flashupdate -c update -m xcp -s xxxx -f</pre> <i>xxxx</i> is the XCP version of the master XSCF. - Procedure 2 <p>Execute the <code>replacefru(8)</code> command to perform a pseudo replacement of the CMUL or SPARC M10 chassis that failed to be cold replaced.</p>
RTI No.	RTIF2-140227-009
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	If XSCF login is performed with an XSCF user account whose privileges are managed by the LDAP server specified in the <code>setldap(8)</code> command, execution of commands in the XSCF shell or operations on XSCF Web may take a while.
Workaround	In the case of an LDAP server specified by the <code>setldap(8)</code> command, there is no effective workaround. Specify the LDAP server with the <code>setldapssl(8)</code> command.
RTI No.	RTIF2-140227-010
Model	SPARC M10-4S
Description	The following improper error message is displayed when the <code>addboard -c</code> configure command is executed, specifying a non-existing system board (PSB): PPAR is currently unavailable for DR, because XBU status has failed. Or The current configuration does not support this operation.
Workaround	There is no effective workaround. Execute the <code>addboard -c</code> configure command, specifying existent PSBs.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140212-003
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	The OID information of scfPsbInfo in the definition file of the XSCF extension MIB is not updated even when the system board (PSB) information is updated.
Workaround	Restart the snmp daemon by using any of the setsnmp(8), setsnmpusm(8), and setsnmpvacm(8) commands, or reset the XSCF.
RTI No.	RTIF2-140212-005
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	In the OID information of scfComponentStatusEvent in the XSCF extended MIB definition file, the paths of suspected components may be reported as "unspecified" information in trap notifications. This symptom occurs when the OID "FaultEventCode" information is any of the following: 05018113 05018123 05018133 05018211 05018221 05018231
Workaround	There is no effective workaround. Execute the showlogs error command to confirm the suspected location.
RTI No.	RTIF2-140212-007
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	When an attempt is made to register a password containing 256 or more characters, in the password input prompt of either the setsnmp(8) or the setsnmpusm(8) command, only 255 characters are registered.
Workaround	When setting up a password equal to or longer than 256 letters, use the password option of either the setsnmp(8) or the setsnmpusm(8) command, instead of using the password input prompt. [How to restore] If a password equal to or longer than 256 characters has been registered using the password input prompt, remove the target user data with either the setsnmp remv3traphost or setsnmpusm delete command and then register the user again.
RTI No.	RTIF2-140212-011
Model	SPARC M10-4S
Description	If the XSCF master/standby switchover occurs when the physical partition (PPAR) is running, in some very rare cases, a "process down" may occur on the XSCF and the XSCF may be reset.
Workaround	There is no effective workaround. The system can be operated continuously as it will be restored after being reset.
RTI No.	RTIF2-140212-014
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	Even if the "CPU cache degraded (CPU #xx)" error is detected, the CHECK LED of the operation panel may not light up.
Workaround	There is no effective workaround.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-140212-016
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	When resetting the XSCF, "the XSCF serial terminal may display the SNMP-related "snmpd [XXXXX] svrSP: error doAction ACTION_CONTROL_LED" error message.
Workaround	There is no effective workaround. Ignore this message.
RTI No.	RTIF2-140212-021
Model	SPARC M10-4S
Description	If any of the physical partitions is reset due to some hardware failure, in the middle of the execution of the testsb(8) command, the testsb(8) command may terminate abnormally. The following is an example of such an error. [Example] XSCF> testsb PSB_NO Initial diagnosis is about to start, Continue?[y n] : y SB power on sequence started. 0.....30.....end Initial diagnosis started. [7200sec] 0..... 30..... 60..... 90.....120.....150.....180.....210.....240..... 270.....300.....330.. Hardware error occurred by initial diagnosis. SB power off sequence started. [1200sec] 0..... 30..... 60..... 90.....120.....150.....180.....210.....240..... 270.....300.....330.....360.....390.....420.....450.....480.....510..... 540.....570.....600.....630.....660.....690.....720.....750.....780..... 810.....840.....870.....900.....930.....960.....990.....1020.....1050..... 1080.....1110.....1140.....1170.....1200end Failed to power off. Please check the FRU. An internal error has occurred. Please contact your system administrator. done.
Workaround	There is no effective workaround. [How to restore] After confirming the faulty hardware with the showlogs(8) command, perform maintenance on the hardware. After that, execute the testsb(8) command again.
RTI No.	RTIF2-140121-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	If the input power is turned off (AC OFF) while the XSCF is processing large amounts of data, only occasionally, the XSCF may not start up when the input power is turned on (AC ON) again.
Workaround	There is no effective workaround. Contact a field engineer if this problem occurs.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-131213-002
Model	SPARC M10-4S
Description	While in the process of adding or removing a system board (PSB) by using the addboard(8) or deleteboard(8) command of the DR feature, if another physical partition is rebooted due to a hardware failure or execution of the poweroff(8)/poweron(8)/reset(8) command on it, the executed addboard(8) or deleteboard(8) command may detect a timeout and terminate abnormally.
Workaround	Do not execute the poweroff(8)/poweron(8)/reset(8) command while the addboard(8) or deleteboard(8) command is being executed. There is no effective workaround if any hardware failure occurs while executing DR. [How to restore] Check the status of the system board (PSB) using the showboards(8) command. Execute the addboard(8) or deleteboard(8) command after that.

RTI No.	RTIF2-131213-003
Model	SPARC M10-4S
Description	While executing the poweroff(8)/poweron(8)/reset(8) command on a physical partition, if the addboard(8) or the deleteboard(8) command of the DR feature is executed on another physical partition to add or remove a system board (PSB), the addboard(8) or deleteboard(8) command may detect timeout and terminate abnormally.
Workaround	Do not execute the addboard(8) or deleteboard(8) command while the poweroff(8)/poweron(8)/reset(8) command is being executed elsewhere. There is no effective workaround if DR is executed while also executing power supply operations on another physical partition. [How to restore] Perform the following procedure. <ol style="list-style-type: none"> 1. Execute the showboards(8) command. 2. Check the status of /Pwr/Conn/Conf/Test of the system board (PSB) to confirm the end of power operations. <ul style="list-style-type: none"> - Power-on/Reset completed The Pwr/Conn/Conf/Test status is "y y y passed" respectively. - End of power-off: The status of Pwr/Conn/Conf is "n n n" respectively. 3. Re-execute the addboard(8) or the deleteboard(8) command.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-131213-011
Model	SPARC M10-4S
Description	<p>When a SPARC M10-4S or a crossbar box is added using the <code>addfru(8)</code> command, the following message is output and the addition may fail.</p> <p>[Warning:036] Failed to find BB#x. The BB-ID setting and/or the cable connections of the BB#x will be wrong. Please confirm the BB-ID setting and the cable connections. Do you want to try to add BB#x again? [a:add c:cancel] :</p>
Workaround	<p>When the following maintenance menu is output after executing the <code>addfru(8)</code> command, turn on the input power to the SPARC M10-4S or the crossbar box to be added, and execute the next operation (step 2) after 20 minutes.</p> <p>Please execute the following steps:</p> <ol style="list-style-type: none">1) After the added device is connected with the system, please turn on the breaker of the BB#x.2) Please select [f:finish]: <p>[How to restore] Input "a" in response to the "[a:add c:cancel] :" message and re-execute the <code>addfru(8)</code> command.</p>

RTI No.	RTIF2-131213-019
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>While the XSCF is starting after being powered on, a watchdog timeout may occur and the XSCF is rebooted. After this reboot is completed, the configuration information of the components mounted on the system cannot be checked by the <code>showhardconf(8)</code> command. Moreover, the following configuration-related messages may be registered in the error log:</p> <p>Msg:Indispensable parts are not installed (PSU). Msg:Indispensable parts are not installed (FAN). Msg:Indispensable parts are not installed (OPNL). Msg:PSU shortage Msg:FAN shortage In this case, the physical partition may not be started.</p>
Workaround	<p>There is no effective workaround.</p> <p>[How to restore] Re-execute power off and on.</p>

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-131213-022
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>After a firmware update, when the XCP version is checked with the version(8) command or the XSCF Web interface, the displayed XCP version of a SPARC M10-1, SPARC M10-4, crossbar box (XBBOX), or SPARC M10-4S (BB) may not be the same as the updated XCP version. The following shows an example of firmware update from XCP 2042 to XCP 2052. The XCP version for "XCP0 (Reserve):" of BB#00 is not updated.</p> <pre>XSCF> version -c xcp -v XBBOX#80-XSCF#0 (Master) XCP0 (Reserve): 2052 XSCF : 02.05.0002 XCP1 (Current): 2052 XSCF : 02.05.0002 XBBOX#81-XSCF#0 (Standby) XCP0 (Current): 2052 XSCF : 02.05.0002 XCP1 (Reserve): 2052 XSCF : 02.05.0002 BB#00-XSCF#0 XCP0 (Reserve): 2042 ~~~~~ CMU : 02.05.0002 POST : 1.43.0 OpenBoot PROM : 4.34.0+1.22.0 Hypervisor : 0.27.8 XSCF : 02.04.0000 XCP1 (Current): 2052 CMU : 02.05.0002 POST : 1.43.0 OpenBoot PROM : 4.34.0+1.22.0 Hypervisor : 0.27.8 XSCF : 02.05.0002</pre>
Workaround	<p>There is no effective workaround.</p> <p>[How to restore]</p> <p>For the SPARC M10-1, the SPARC M10-4, or the 1BB configuration with the SPARC M10-4S, execute the rebootxscf command to reset the XSCF.</p> <p>For the building block configuration, specify the BB-ID of the crossbar box (XBBOX) or the SPARC M10-4S (BB), which was not updated, in the rebootxscf -b BB-ID command, and execute it to reset the XSCF of the specified chassis.</p>
RTI No.	RTIF2-131126-003
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>Update of the PCI expansion unit firmware may fail. If it fails, "LINKCARD update is failed" appears in the event log.</p> <p>[Example]</p> <pre>SCF:LINKCARD update is failed (LINKCARD=1, bank=0, PCIBOX version=1130: last version=1120)</pre>
Workaround	<p>There is no effective workaround.</p> <p>Execute the ioxadm -c update command to update the PCI expansion unit firmware again.</p>

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-131112-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	If data is transmitted via SSH by the snapshot(8) -t command, this transmission may be delayed by about 10 to 30 minutes, in comparison with transfer using USB devices and XSCF Web.
Workaround	There is no effective workaround. Even if transfer is delayed, there is no problem with the collected data.
RTI No.	RTIF2-131112-013
Model	SPARC M10-4S
Description	Suppose that multiple physical partitions (PPARs) are simultaneously started by the poweron -a command. The error message "Error storing configuration variable. LDC is not up Configuration variable setting will not persist after a reset or power cycle" may be output to the OS console after the message "Unable to connect to Domain Service providers." Oracle Solaris may be started without applying the OpenBoot PROM environment variable specified with the setpparparam(8) command.
Workaround	There is no effective workaround. Restart the control domain for the physical partition (PPAR) for which the error message was output.
RTI No.	RTIF2-131001-002
Model	SPARC M10-4S
Description	While a physical partition (PPAR) is operating or when a PPAR is turned off, master/standby XSCF switching may occur. If master/standby switching occurs, the following message is displayed on the console of the master XSCF. Kernel panic - not syncing: MBC Dual ifcut interrupt.
Workaround	There is no effective workaround. As a result of this defect, master/standby switching occurs, but this does not affect the system, and the system can be operated continuously.
RTI No.	RTIF2-130919-001
Model	SPARC M10-4S
Description	In a system composed of several SPARC M10-4S chassis, if input power is turned off and on (AC OFF/ON) on a number of the SPARC M10-4S chassis while some other slave chassis are running, the physical partitions may not start.
Workaround	When performing AC OFF/ON, do so on all of the chassis comprising the system. [How to restore] Turn off the input power to all of the chassis comprising the system, and then turn on the input power.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-130710-001
Model	SPARC M10-4S
Description	If XSCF switching is performed using the switchscf(8) command, in rare cases, the standby XSCF may not start.
Workaround	There is no effective workaround. [How to restore] Turn off and then turn on the input power supply (AC OFF/ON) to every SPARC M10-4S chassis or execute the replacefru(8) command to perform pseudo replacement (replacement work without replacing any parts) of the SPARC M10-4S that does not start.
RTI No.	RTIF2-130516-002
Model	SPARC M10-4S
Description	If XSCF switching or an XSCF reboot occurred while the physical partition (PPAR) was being powered off, it may be impossible to turn off the power.
Workaround	There is no effective workaround. While powering off the PPAR, do not use the switchscf(8) command for the switching or the rebootxscf(8) command for the XSCF reboot. [How to restore] Turn off the input power, and then turn it on again. (AC-OFF/ON)
RTI No.	RTIF2-130516-004
Model	SPARC M10-4S
Description	If a hardware failure occurs in a 4BB or greater configuration, automatic cluster switching may fail. If 16 or more guest nodes are incorporated into a single cluster, the following warning message may be output to the console of the control domain. SA SA_xscf?????.so to test host ??? failed
Workaround	If automatic cluster switching fails, follow the procedure in the manual of the cluster software to perform switching manually.
RTI No.	RTIF2-130516-006
Model	SPARC M10-4S
Description	If XSCF switching occurs while multiple physical partitions (PPARs) are being powered on at the same time, it may take more time than usual to power them on.
Workaround	There is no effective workaround. Do not switch an XSCF by using the switchscf(8) command while PPARs are being powered on.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-130410-001
Model	SPARC M10-4S
Description	<p>Suppose that for the system connected to a crossbar box, you are powering on or off the physical partition (PPAR) that is not assigned to the maintenance-target FRU, and you execute the <code>diagxbu(8)</code> or <code>testsb(8)</code> command. In this case, the diagnosis of a system board (PSB) may fail during the PSB power-off, and the following message may be output.</p> <pre>[Warning:010] An internal error has occurred.</pre>
Workaround	<p>There is no effective workaround.</p> <p>Execute the <code>showboards(8)</code> command to check that the [Pwr] field of the relevant PSB is set to "n".</p> <p>If the field is set to "y", execute the <code>showboards(8)</code> command every few minutes to check that the field changes to "n".</p>
RTI No.	RTIF2-130410-002
Model	SPARC M10-4S
Description	<p>The switching of an XSCF may fail if the XSCF is switched by executing the <code>switchscf(8)</code> command while a physical partition (PPAR) is being powered on.</p>
Workaround	<p>Do not switch an XSCF by using the <code>switchscf(8)</code> command while a PPAR is being powered on.</p>
RTI No.	RTIF2-130410-003
Model	SPARC M10-4S
Description	<p>If a physical partition (PPAR) is powered on in a system that satisfies all the following conditions, another PPAR may also be powered on.</p> <ul style="list-style-type: none"> - The remote power management is enabled with the <code>setremotepwrmgmt(8)</code> command. - When created, a node does not have a set SubNodeID in the management items for remote power management. - Multiple PPARs are configured.
Workaround	<ul style="list-style-type: none"> - When remote power management is not necessary <ul style="list-style-type: none"> Disable remote power management with <code>setremotepwrmgmt -c disable</code> and then delete the remote power management setting with the <code>clearremotepwrmgmt(8)</code> command. - When remote power management is necessary <ul style="list-style-type: none"> If the system has multiple PPARs, create a management file for remote power management by specifying a PPAR-ID as a SubNodeID, and then register the remote power management setting with <code>setremotepwrmgmt -c config</code>.
RTI No.	RTIF2-130329-005
Model	SPARC M10-1
Description	<p>When you turn on the input power supply in the SPARC M10-1, the XSCF READY LED remains blinking and the system may not start.</p>
Workaround	<p>There is no effective workaround.</p> <p>Power off the input power supply and then power it on again.</p>

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-130305-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	"The limit of power has been exceeded" is registered in the event log if you perform the following. Execute the <code>setpowercapping(8)</code> command to set the power consumption limiting function to "Enable", the power consumption maximum allowable value to "powerlimit_p" (percentage), and the time extension when exceeding the maximum allowable power consumption to "none." Then, turn on the input power supply or turn off the power to the physical partition (PPAR).
Workaround	There is no effective workaround. Ignore this event log.
RTI No.	RTIF2-130305-002
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	If an error in the CPU or memory is detected and if the XSCF is switched while the XSCF is reporting error information to the control domain, the error information may not be reported again to the control domain. Because of this, the error information displayed with the <code>showlogs</code> error command is not displayed in the Fault Report output by the <code>fmdump</code> command.
Workaround	There is no effective workaround. Perform maintenance according to the FRU displayed by the <code>showlogs</code> error command.
RTI No.	RTIF2-130305-007
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	A FRU registered in the error log is displayed as "PPAR#30" if the configuration error of the system board (PSB) is detected when executing the <code>testsb(8)</code> command or <code>diagxbu(8)</code> command.
Workaround	There is no effective workaround. Maintain the applicable PSB of the SPARC M10 system chassis.
RTI No.	RTIF2-130305-013
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	While the XSCF is starting, watchdog timeout may occur, which may cause the XSCF to be reset.
Workaround	Perform the following procedure. <ol style="list-style-type: none"> 1. If Oracle Solaris is running, shut it down. 2. Confirm that Oracle Solaris has been shut down, and then turn off and on (AC OFF/ON) the input power of the system. When turning off/on the input power, power off the system, wait at least 30 seconds, and then power on the system. <p>If the XSCF does not start even after the system input power is cycled, replace the CPU memory unit lower (CMUL) or the motherboard unit (MBU).</p>

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-130305-020
Model	SPARC M10-4S
Description	If an XSCF panic or hang occurs, "XSCF hang-up is detected" may be posted many times in the error log.
Workaround	There is no effective workaround. Ignore duplicate notifications with the same time in the log, considering that they have the same cause.
RTI No.	RTIF2-130305-022
Model	SPARC M10-4S
Description	The poweron(8) command may fail with the output of the following message if "XSCF hang-up is detected" was detected in the error log and the showboards(8) command found an "unknown" system board (PSB). The same happens even with a power-on instruction from XSCF Web, APCS, or remote power management. Not powering on : An internal error has occurred. Please contact your system administrator.
Workaround	There is no effective workaround. Use the showboards(8) command to replace the XSCF of the "unknown" PSB.
RTI No.	RTIF2-130305-025
Model	SPARC M10-4S
Description	If the poweroff(8) command is executed on a system that has more than one physical partition (PPAR), the processing takes time because the PPARs are powered off one by one. Depending on the system configuration, it may take about one hour.
Workaround	There is no effective workaround.
RTI No.	RTIF2-130219-004
Model	SPARC M10-4S
Description	If an error occurs with a crossbar unit mounted on a crossbar box, many instances of "failed to read/write interrupt mask register" may be registered in the error log.
Workaround	There is no effective workaround. Power off the physical partition (PPAR) and replace the crossbar unit in the crossbar box.
RTI No.	RTIF2-130219-006
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	If you reboot an XSCF by using the flashupdate(8) or rebootxscf(8) command while a physical partition (PPAR) is being powered on, the POST may stop in a state where the diagnosis is completed (Initialization Complete).
Workaround	There is no effective workaround. [How to restore] Execute the reset por command, or power off the PPAR by using the poweroff -f command, and then power it on again.

Table 3-10 Problems That Might Occur With XCP 2352 and Workarounds (*continued*)

RTI No.	RTIF2-130109-003
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	If you use the <code>setpctl(8)</code> command to change the LSB number of a SPARC M10 system connected to a PCI expansion unit, and then you start Oracle Solaris in a logical domain configuration, you cannot display the configuration information for the PCI expansion unit by executing the <code>showhardconf(8)</code> command.
Workaround	Use the <code>setdomainconfig(8)</code> command to set the logical domain configuration to the factory-default, and power on the physical partition (PPAR). Then, configure the logical domain again.

Workaround for RTIF2-140304-007

If a PSU is replaced with the `replacefru(8)` command, after mounting the new PSU, wait at least 30 seconds before pressing the [f] key for the menu of the `replacefru(8)` command.

```
Do you want to continue?[r:replace|c:cancel] :r
Please execute the following steps:
1) Remove PSU#n.
2) Execute either the following:
   2-1) After installing the exchanged device, please select 'finish'.
   2-2) If you want to suspend the maintenance without exchanging device,
        please select 'cancel'.
[f:finish|c:cancel] :f
```

[How to restore]

If no workaround is implemented and the status of both PSUs is "Deconfigured," active replacement of the PSUs cannot be done with the `replacefru(8)` command.

```
Maintenance/Replacement Menu
Please select a FRU to be replaced.
No.  FRU                Status
-----
1    /PSU#0              Deconfigured
2    /PSU#1              Deconfigured
-----
Select [1,2|b:back] :2
[Warning:307]
PSU#1 cannot be replaced. Please verify the configuration.
Select [1,2|b:back] :2
[Warning:307]
PSU#1 cannot be replaced.
```

To restore the system, first remove each replaced PSU without using the `replacefru(8)` command. Then, use the `replacefru(8)` command to replace the PSUs.

Problems Resolved in XCP 2352

The following table lists the problems resolved in XCP 2352.

Table 3-11 Problems Resolved in XCP 2352

RTI No.	RTIF2-180704-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	Security fixes. (CVE-2018-3639)
Workaround	Update the XCP firmware to XCP 2352 or later. If the firmware is updated while the power to the physical partition is on, power off and then power on the physical partition.

Problems Resolved in XCP 2351

The following table lists the problems resolved in XCP 2351.

Table 3-12 Problems Resolved in XCP 2351

RTI No.	RTIF2-180403-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	Security fixes. (CVE-2017-5753) For details, see the Oracle website relating to the following: <i>Critical Patch Updates</i>
Workaround	Update the XCP firmware to XCP 2351 or later.

Problems Resolved in XCP 2350

The following table lists the problems resolved in XCP 2350.

Table 3-13 Problems Resolved in XCP 2350

RTI No.	RTIF2-171011-001
Model	SPARC M10-4S
Description	The dumpconfig(8) command may fail when the firmware has been updated from XCP 2250 or earlier to XCP 2260 or later.
Workaround	Execute one of the following XSCF commands: <ul style="list-style-type: none">- showremotestorage(8)- snapshot(8)- rebootxscf -a

Table 3-13 Problems Resolved in XCP 2350 (*continued*)

RTI No.	RTIF2-171011-002
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	If the XSCF shell is forcibly disconnected during execution of the showsnmp command, executed XSCF commands may in rare cases not respond. After that, if XSCF commands do not respond many times, an XSCF panic and reboot occurs.
Workaround	There is no effective workaround. [How to restore] Reboot all the XSCFs by executing the rebootxscf -a command. If the executed rebootxscf -a command fails to reboot an XSCF, press the RESET switch of the XSCF, or shut down every Oracle Solaris and then turn off the input power (AC OFF).
RTI No.	RTIF2-171011-003
Model	SPARC M10-1
Description	If the DC-DC converter (DDC) mounted on the motherboard unit (MBU) fails, a power failure may be erroneously detected while the DCC failure is undetected.
Workaround	There is no effective workaround. Replace the MBU.
RTI No.	RTIF2-171011-004
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<ul style="list-style-type: none"> - If the reporting function by REMCS is used, any of the following symptoms may very rarely occur if the XSCF is not rebooted for a long time. - The power of the physical partition is not turned on. - Reporting by REMCS is not conducted. - A REMCS periodical connection timeout occurs. - The error message "An internal error has occurred." appears when an XSCF command is executed, and the command fails.
Workaround	Reboot all of the XSCFs by executing the "rebootxscf -a" command. At that time, it is not necessary to power off the physical partition.
RTI No.	RTIF2-171011-005
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	If the motherboard unit (MBU), the CPU memory unit lower (CMUL), or a microSD card in the XSCFU fails, the CPU Activation key backup information on the PSU backplane (PSUBP) may also be deleted. This may make it impossible to register the CPU Activation key again.
Workaround	There is no effective workaround. [How to restore] If the CPU Activation key cannot be registered again, execute the restoredefaults -c xscf command. Then, after the XSCF stops, turn off the input power (AC OFF). After that, turn on the input power (AC ON) again, and register the CPU Activation key again.

Table 3-13 Problems Resolved in XCP 2350 (*continued*)

RTI No.	RTIF2-170626-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>If only the host node (SPARC M10) loses power, the power of the ETERNUS is turned off when the power of the host node recovers. At this time, the power of the ETERNUS is not turned on even when the power of the physical partition (PPAR) is turned on.</p> <p>This may occur when the remote power management function (RCIL) is enabled on the SPARC M10 system with the ETERNUS connected.</p>
Workaround	<p>There is no effective workaround.</p> <p>If the power of the ETERNUS is not turned on, turn off and on the PPAR again to recover.</p>
RTI No.	RTIF2-170428-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>Even when XCP firmware has been updated, the old XCP firmware version before the update may be displayed under the following cases:</p> <ul style="list-style-type: none"> - The [VERSION] button on XSCF Web is selected. - The prtdiag -v command is executed on Oracle Solaris. <p>In this case, the correct XCP firmware version is displayed after the XSCF is rebooted, however, the following message appears at each XSCF login.</p> <p>"XCP version of XSCF and Back-Panel mismatched!"</p>
Workaround	<p>There is no effective workaround.</p> <p>[How to restore]</p> <p>Update the firmware again.</p> <p>To update the firmware with the flashupdate command, specify the -f option.</p>
RTI No.	RTIF2-170405-002
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>Suppose you close the browser before finishing the collection of a snapshot from XSCF Web and then you execute the snapshot command to collect a snapshot on the XSCF shell. The newly executed command ends normally but no log was collected in the snapshot.</p>
Workaround	<p>To collect a snapshot after you close the browser while collecting a snapshot from XSCF Web, collect it from XSCF Web again or wait about an hour after closing the browser to execute the snapshot command.</p>
RTI No.	RTIF2-161205-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>During firmware update for a PCI expansion unit, the "LINKCARD I2C error" error log may inadvertently be registered immediately after the start of the update.</p> <p>Nov 27 19:28:26 M10-1 Event: SCF:LINKCARD update is started (LINKCARD=9, bank=1, PCIBOX version=1210: last version=1180)</p> <p>Nov 27 19:28:29 M10-1 Warning: /BB#0/PCI#9/LINK:SCF:LINKCARD I2C error</p>
Workaround	<p>There is no effective workaround.</p> <p>Ignore the "LINKCARD I2C error" error log that is registered immediately after the start of the update.</p> <p>The firmware update for the PCI expansion unit will succeed.</p>

Table 3-13 Problems Resolved in XCP 2350 (continued)

RTI No.	RTIF2-150702-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	<p>Suppose you attempt to execute the <code>dumpconfig(8)</code> command to save XSCF setting information, and then execute the <code>restoreconfig(8)</code> command to restore the XSCF setting information in the same chassis or other chassis. In this case, some settings may not be saved/restored or may be excessively restored on other chassis.</p> <p>The following settings are not saved/restored in the same chassis or other chassis.</p> <ul style="list-style-type: none"> - Setting values for power operation at power recovery, set with the <code>setpowerschedule -c recover</code> command - Setting values for enabling/disabling ASR function, set with the <code>setservicetag(8)</code> command - Setting values for configuring a remote power management group, changing its settings, and enabling/disabling the remote power management function for the group, set with the <code>setremotepwrmgmt(8)</code> command <p>The following settings are excessively restored on the same or other chassis.</p> <ul style="list-style-type: none"> - Information on the time difference between the XSCF and the hypervisor of each physical partition (PPAR)
Workaround	<p>There is no effective workaround.</p> <p>[How to restore]</p> <p>Perform the following procedure.</p> <ol style="list-style-type: none"> 1. Before saving the XSCF setting information with the <code>dumpconfig(8)</code> command, confirm the following settings. <ul style="list-style-type: none"> - Setting for power operation at power recovery(recover mode) <p>[Example] Not powering on at power recovery</p> <pre>XSCF> showpowerschedule -a -m state PPAR-ID schedule member recover mode ----- 0 enable 4 off - ASR function (Service Tag) enable/disable setting</pre> <p>[Example] Enabled</p> <pre>XSCF> showservicetag Enabled - Remote power management group settings - Saving configuration and setting changes</pre> <p>[Example] Saving management file to ftp server when registering one remote power management group</p> <pre>XSCF> getremotepwrmgmt -G 1 ftp://server[:port]/path/file - Remote power management function enable/disable setting</pre> <p>[Example] Enabled</p> <pre>XSCF> showremotepwrmgmt [Remote Power Management Group#01 Information] Remote Power Management Status :[Enable] --- Omitted ---</pre> 2. Execute the <code>dumpconfig(8)</code> command to save the XSCF setting information. 3. Execute the <code>restoreconfig(8)</code> command to restore the XSCF setting information. 4. After restoring the XSCF setting information with the <code>restoreconfig(8)</code> command, execute the command below to implement a reset using the setting values confirmed in step 1. <ul style="list-style-type: none"> - Restoring the setting for power operation at power recovery <p>[Example] Not powering on at power recovery</p>

Table 3-13 Problems Resolved in XCP 2350 (*continued*)

XSCF>	setpowerschedule -a -c recover=off
	- Restoring the ASR function (Service Tag) enable/disable setting
	[Example] Setting enable
XSCF>	setservicetag -c enable
	- Restoring configuration and setting changes of the remote power management group and the enable/disable setting of the remote power management function
	[Example] Restoring setting information based on management file saved to FTP server
	(1) If there are other hosts included in the remote power management group, disable the remote power management function for the hosts.
XSCF>	setremotepwrmgmt -c disable
	(2) Restore the management file of the remote power management group.
XSCF>	setremotepwrmgmt -c config "ftp://server[:port]/path/file"
	(3) Enable the remote power management function. If you have disabled other hosts, enable them, too.
XSCF>	setremotepwrmgmt -c enable
	5. Power on the physical partition, synchronize the Oracle Solaris time with the NTP server, or adjust the time with the date(1) command.

Problems Resolved in XCP 2340

The following table lists the problems resolved in XCP 2340.

Table 3-14 Problems Resolved in XCP 2340

RTI No.	RTIF2-171013-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	Security fixes. (CVE-2016-6304, CVE-2016-2182, CVE-2016-2183, CVE-2016-6302, CVE-2016-6306, CVE-2016-6515, CVE-2017-3731, CVE-2016-7431, CVE-2016-7429, CVE-2016-7433) For details, see the Oracle website relating to the following: <i>Critical Patch Updates</i>
Workaround	Update the XCP firmware to XCP 2340 or later.

RTI No.	RTIF2-170421-001
Model	SPARC M10-1, SPARC M10-4, SPARC M10-4S
Description	Booting from an external DVD drive may fail. [Example of output message] {0} ok boot /pci@8000/pci@4/pci@0/pci@2/pci@0/usb@4,1/cdrom@2/disk Boot device: /pci@8000/pci@4/pci@0/pci@2/pci@0/usb@4,1/cdrom@2/disk File and args: Can't open boot device
Workaround	1. Stop OpenBoot PROM at the ok prompt. 2. Perform the following steps to change the OpenBoot PROM environment variable nvramrc. a. Execute the nvedit command, and write nvramrc as shown below. {0} ok nvedit

-
- ```

0: : patch-zero 0 to my-adr0 0 ;
1: ' patch-zero ' 0 ' new-instance d# 28 + token@ (patch

```
- b. Press the [Ctrl] + [C] keys to exit nvedit and return to the ok prompt.
  - c. Execute nvstore to save the edited contents.

```
{0} ok nvstore
```
  - d. Check whether nvramrc was written correctly.

```
{0} ok printenv nvramrc
nvramrc = : patch-zero 0 to my-adr0 0 ;
 ' patch-zero ' 0 ' new-instance d# 28 + token@ (patch
{0} ok
```
3. Set the OpenBoot PROM environment variable use-nvramrc? to true.

```
{0} ok setenv use-nvramrc? true
use-nvramrc? = true
```
  4. Execute the reset-all command to restart the OpenBoot PROM.

```
{0} ok reset-all
```

After executing the reset-all command and the system stops at the ok prompt, boot from a DVD drive.

Example: For booting from a DVD drive connected to a front USB port

```
{0} ok boot /pci@8000/pci@4/pci@0/pci@2/pci@0/usb@4,1/cdrom@2/disk
```

To restore the original settings from the above settings in [Workaround], perform the following steps.

1. Stop OpenBoot PROM at the ok prompt.
  2. Delete the set values from the OpenBoot PROM environment variable nvramrc.
    - a. Execute nvedit to delete ":patch=zero 0 to my-adr0 0;" and

```
" patch-zero ' 0 ' new-instance d# 28 + token@ (patch"
```

from nvramrc. You can delete lines one by one by pressing the [Ctrl] + [K] keys twice at the beginning of a line.

```
{0} ok nvedit
0:

```
    - b. Press the [Ctrl] + [C] keys to exit nvedit and return to the ok prompt.
    - c. Execute nvstore to save the edited contents.

```
{0} ok nvstore
```
    - d. Check whether the nvramrc setting was deleted correctly.

```
{0} ok printenv nvramrc
nvramrc =
{0} ok
```
  3. Set the OpenBoot PROM environment variable use-nvramrc? to false. If it has been set to true before the workaround was performed, this step is unnecessary.

```
{0} ok setenv use-nvramrc? false
use-nvramrc? = false
{0} ok
```
  4. Execute the reset-all command to restart OpenBoot PROM.

```
{0} ok reset-all
```
-

Table 3-14 Problems Resolved in XCP 2340 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-170407-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>The ttydm process may fail and the following logs may be registered if you reboot the XSCF by executing the rebootxscf(8), switchscf(8), or flashupdate(8) command while console messages are continuously output from the control domain.</p> <p>[Example]<br/> XSCF&gt; <b>showlogs error -vr</b><br/> Date: Mar 02 22:23:00 JST 2015<br/> Code: 20000000-00fcff000e020000ff-010400010000000000000000<br/> Status: Notice Occurred: Mar 02 22:22:54.396 JST 2015<br/> FRU: /FIRMWARE,/MBU<br/> Msg: SCF process down detected<br/> Diagnostic Code:<br/> 00000000 00000000 0000<br/> 00000000 00000000 0000<br/> 00000000 00000000 0000<br/> 74747964 6d2e3237 36342e62 7a320000<br/> 00000000 00000000 0000</p> <p>You can check the ttydm process by confirming that the first four bytes on the fourth line of "Diagnostic Code" have the value "747479646d".</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/> Logs are collected. The XSCF is rebooted.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-170406-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>From XCP 2230 onwards, a process may produce a core dump if Dynamic Resource Management (DRM) of Oracle VM Server for SPARC is used or a virtual CPU is deleted by executing the ldm remove-vcpu command.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Workaround</b>  | <p>There is no effective workaround.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-170406-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>When you select the [XSCF] - [Logs] - [Console Log] menu from XSCF Web and display log information, the following error appears in a pop-up window and the screen returns to the login window.</p> <p>Description Server Internal error or the page you visit does not exist or has been deleted! Sorry for any inconvenience!</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/> Execute the showlogs command in the XSCF shell to check the console log.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>RTI No.</b>     | <b>RTIF2-170405-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>If a security key is used for booting Oracle Solaris on the control domain and you save the domain configuration information by using the following procedure, the next boot fails.</p> <ol style="list-style-type: none"> <li>1. Execute the set-security-key command or the set-ascii-security-key command at the ok prompt of the OpenBoot PROM on the control domain to set a security key.</li> </ol>                                                                                                                                                                                                                                                                                                                                                                                                                                                             |

Table 3-14 Problems Resolved in XCP 2340 (continued)

---

```
2. Save the domain configuration information to the XSCF by executing the ldm add-spconfig
 command of Oracle VM Server for SPARC.
3. Boot Oracle Solaris on the control domain.
This problem does not occur on a guest domain.
[Example] Booting Oracle Solaris fails after a security key is set
SPARC M10-4S, No Keyboard
Copyright (c) 1998, 2016, Oracle and/or its affiliates. All rights reserved.
OpenBoot 4.38.5, 126.0000 GB memory available, Serial #*****.
[2.21.0]
Ethernet address *.*.*.*.*.*.*, Host ID: *****.
-- Omitted --
Boot device: /pci@8000/pci@4/pci@0/pci@9/network@0 File and args:
100 Mbps full duplex Link up
100 Mbps full duplex Link up
100 Mbps full duplex Link up
SunOS Release 5.11 Version 11.3 64-bit
Copyright (c) 1983, 2015, Oracle and/or its affiliates. All rights reserved.
NOTICE: Configuring iSCSI boot session...
Hostname: solaris

solaris console login: root
Password:
Last login: Wed Jan ** *.*.*.* 2017 on console
Jan ** *.*.*.* solaris login: ROOT LOGIN /dev/console^M
Oracle Corporation SunOS 5.11 11.3 September 2015
You have mail.
root@solaris:~# ldm ls-spconfig
factory-default [current]
root@solaris:~# ldm add-spconfig ldm-cfg1
root@solaris:~# ldm ls-spconfig
factory-default
ldm-cfg1 [current]
root@solaris:~# init 6
-- Omitted --
SPARC M10-4S, No Keyboard
Copyright (c) 1998, 2016, Oracle and/or its affiliates. All rights reserved.
OpenBoot 4.38.5, 126.0000 GB memory available, Serial #*****.
[2.21.0]
Ethernet address *.*.*.*.*.*.*, Host ID: *****.

100 Mbps full duplex Link up
Boot device: net File and args:
100 Mbps full duplex Link up
ERROR: /iscsi-hba: iSCSI login failed

ERROR: /packages/obp-tftp: Could not open /iscsi-hba/disk

Boot load failed
```

Table 3-14 Problems Resolved in XCP 2340 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                               |
|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
|                    | Evaluating:<br><br>{0} ok                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>After executing the ldm add-sconfig command from Oracle VM Server for SPARC to store the domain configuration information in the XSCF, set the security key again by executing the set-ascii-security-key command or set-security-key command at the ok prompt of the OpenBoot PROM. |
| <b>RTI No.</b>     | <b>RTIF2-170405-006</b>                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | In a system with a building block configuration, the following error log may be registered when the input power is turned on (AC ON) or the rebootxscf -a command is used.<br>XSCF data synchronization failed<br>At this time, the physical partition cannot be powered on.                                                                  |
| <b>Workaround</b>  | There is no effective workaround.<br>Reboot all the XSCFs by using the rebootxscf -a command.                                                                                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-170405-011</b>                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | The showinterimpermit command cannot be executed with the fieldeng privilege.                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | Execute the command with any of the following user privileges.<br>platadm, platop, pparadm, pparmgr, pparop                                                                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-170331-001</b>                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | On a SPARC M10 system with SPARC64 X processors, Hypervisor may abort after performing a live migration on a guest domain.                                                                                                                                                                                                                    |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                             |
| <b>RTI No.</b>     | <b>RTIF2-161215-001</b>                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | If a live migration of a guest domain fails, logical domains on the physical partition as the migration source may panic or Hypervisor may abort.                                                                                                                                                                                             |
| <b>Workaround</b>  | There is no effective workaround.<br>If a live migration fails, reboot the guest domain as soon as possible.                                                                                                                                                                                                                                  |

Table 3-14 Problems Resolved in XCP 2340 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-161117-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | <p>If you boot/reboot logical domains after the elapse of a certain period of time from physical partition (PPAR) power-on, the boot/reboot may fail.</p> <p>This problem occurs in a boot/reboot from an internal LAN.<br/> This problem happens during the time period described below.</p> <ul style="list-style-type: none"> <li>- The first period is the 15-minute period after the elapse of "24 days, 20 hours, 16 minutes, and 23.648 seconds" from physical partition (PPAR) power-on. Each subsequent period is the 15-minute period after the elapse of a multiple of time of "49 days, 17 hours, 02 minutes, and 47.296 seconds."</li> </ul> <p>(15-minute period on the 25th day and then the 75th day, 125th day, 175th day, and so on at an interval of approximately 50 days)</p> <p>[Example]<br/> Period of 15 minutes from the 24th day, 20 hours, 16 minutes, and 23.648 seconds to (same day and hour) 31 minutes and 23.648 seconds<br/> Period of 15 minutes from the 74th day, 13 hours, 19 minutes, and 10.944 seconds to (same day and hour) 34 minutes and 10.944 seconds</p> <p>[Example of failure] Oracle Solaris is booted from internal LAN in control domain on SPARC M10-4<br/> SPARC M10-4S, No Keyboard<br/> Copyright (c) 1998, 2016, Oracle and/or its affiliates. All rights reserved.<br/> OpenBoot 4.38.5, xx.xxxx GB memory available, Serial #xxxxxxx.<br/> [ 2.19.0 ]<br/> Ethernet address xx:xx:xx:xx:xx:xx, Host ID: xxxxxxxx.<br/> 1000 Mbps full duplex Link up<br/> Boot device: net:dhcp File and args:<br/> 1000 Mbps full duplex Link up<br/> Timed out waiting for BOOTP/DHCP reply<br/> :<br/> :</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/> [How to restore]<br/> Execute the sendbreak command from the XSCF. After the system stops at the ok prompt and 15 minutes elapse, execute the boot command.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>RTI No.</b>     | <b>RTIF2-150226-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | CHECK LED of the old master XSCF blinks during the master/standby switchover of XSCFs.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/> Ignore the blinking CHECK LED of the old master XSCF during the master/standby switchover of XSCFs.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |

Table 3-14 Problems Resolved in XCP 2340 (continued)

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|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-141008-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | <p>If the <code>setsnmp</code> command tries to set enabled immediately after disabled was set, the "Agent enable failed" message may appear, and the command may fail. This may cause the following phenomena.</p> <ol style="list-style-type: none"><li>1. If it is left as is, "SCF panic detected" is registered in the error log, and the XSCF is rebooted.</li><li>2. The setting is not reflected, so if the <code>showsnmp</code> command is executed, it displays "Enabled" for "Agent Status" and "None" for "Enabled MIB Modules."</li><li>3. If the <code>showsnmp</code> command is executed, it displays "Enabled" for "Agent Status" and "SP MIB" for "Enabled MIB Modules." However, the setting has not actually been reflected, so rebooting the XSCF causes "None" to be displayed for "Enabled MIB Modules."</li><li>4. The setting has not been reflected, so when the <code>showsnmp</code> command displays "None" for "Enabled MIB Modules," the system using the cluster software detects the following error: "FJSVcluster: Error: DEV: 7240: Cannot connect to the XSCF."</li></ol> <p>[Example] After the <code>setsnmp</code> command fails, the setting is not reflected.</p> <pre>XSCF&gt; <b>setsnmp disable</b> XSCF&gt; <b>setsnmp enable</b> setsnmp: Agent enable failed XSCF&gt; <b>showsnmp</b></pre> <p>Agent Status: Enabled<br/>Agent Port: 161<br/>System Location: System-Location<br/>System Contact: System-Contact<br/>:<br/>Status: Enabled<br/>Community String: public</p> <p>Enabled MIB Modules: None<br/>XSCF&gt;</p> <p>Similarly, this problem also occurs when the <code>setsnmpusm</code> command or <code>setsnmpvacm</code> command is executed to change the setting while the SNMP setting is enabled.</p> |
| <b>Workaround</b>  | <p>To change the setting to enabled after it was changed to disabled, wait at least 30 seconds, the minimum interval.</p> <p>To change the setting with the <code>setsnmpusm</code> command or <code>setsnmpvacm</code> command, temporarily change the SNMP setting to disable, and then wait at least 30 seconds, the minimum interval.</p> <p>[How to restore]<br/>Temporarily set disable with the <code>setsnmp</code> command, and then wait at least 30 seconds, the minimum interval, to set enable.<br/>Next, execute the <code>showsnmp</code>, <code>setsnmpusm</code>, or <code>showsnmpvacm</code> command to confirm that the setting has been reflected as expected.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |

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# Problems Resolved in XCP 2332

The following table lists the problems resolved in XCP 2332.

Table 3-15 Problems Resolved in XCP 2332

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|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-170130-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | <p>When you perform a live migration of a logical domain, the following may not be displayed properly even after you save logical domain configuration information by executing the <code>ldm add-sconfig</code> command:</p> <ul style="list-style-type: none"><li>- The state of the moved logical domain to be displayed by the XSCF <code>showdomainstatus</code> command</li><li>- The state of the logical domain of <code>scfDomainStatus</code> in the XSCF extended MIB definition file</li></ul> <p>At this time, in an environment that uses the cluster software, the software tries to switch clusters if the interconnect of all the clusters fails. In that case, there may be double startup of a user application, and as a result, data inconsistency may occur.</p> <p>[Example] After <code>guest01</code> is moved from another SPARC M10 to the local SPARC M10 through live migration, the <code>showdomainstatus</code> command should display the Status of <code>guest01</code> as Solaris running. However, it actually displays it as Host stopped.</p> <pre>XSCF&gt; showdomainstatus -p 0 -y Logical Domain Name  Status primary              Solaris running guest00              Solaris running guest01              Host stopped</pre> <pre># ldm list NAME  STATE  FLAGS  CONS  VCPU  MEMORY  UTIL  UPTIME primary  active  -t-cv      4     1G    0.5%   3d 21h 7m guest00  active  -t---    5000    8     1G    23%    10h guest01  active  -t---    5001    8     1G    23%    20h</pre> |
| <b>Workaround</b>  | <p>Before moving a guest domain from another SPARC M10 to the local SPARC M10 through live migration, reboot all the XSCFs connected to the local SPARC M10 by using the <code>rebootxscf -a</code> command.</p> <p>The XSCFs must be rebooted every time a PPAR is powered off and on.</p> <p>Reboot the XSCFs after powering off the PPAR but before performing a live migration.</p> <p>If you turn the input power off and on when powering off and on the PPAR, you do not need to execute the <code>rebootxscf -a</code> command.</p> <p>[How to restore]</p> <p>After rebooting all the XSCFs connected to the SPARC M10 by using the <code>rebootxscf -a</code> command, perform a live migration to move the guest domain whose state is not displayed properly, or restart the guest domain.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |

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# Problems Resolved in XCP 2330

The following table lists the problems resolved in XCP 2330.

Table 3-16 Problems Resolved in XCP 2330

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|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-161129-001</b>                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                              |
| <b>Description</b> | With XCP 2290 or later, Hypervisor may abort after perform a live migration on a guest domain running Oracle Solaris 11.2 or later.                                        |
| <b>Workaround</b>  | There is no effective workaround.<br>After a guest domain is moved by a live migration, reboot the guest domain as soon as possible, or restart (stop and start) the PPAR. |

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Table 3-16 Problems Resolved in XCP 2330 (continued)

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-161108-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | <p>If you boot/reboot logical domains after a certain period of time elapsed from physical partition (PPAR) power-on, the boot/reboot fails and the system stops at the ok prompt. This problem does not occur in a boot/reboot from an internal disk or USB device. The problem occurs in a boot/reboot from any of the following devices:</p> <ul style="list-style-type: none"><li>- Virtual disk (*1), virtual network, virtual HBA, and F160/F320 card</li></ul> <p>*1 This also holds true for cases where an internal disk is used as a virtual disk.</p> <p>This problem happens during the time period described below.</p> <ul style="list-style-type: none"><li>- The first period is the 15-minute period after the elapse of "24 days, 20 hours, 16 minutes, and 23.648 seconds" from physical partition (PPAR) power-on. Each subsequent period is the 15-minute period after the elapse of a multiple of time of "49 days, 17 hours, 02 minutes, and 47.296 seconds."</li></ul> <p>(15-minute period on the 25th day and then the 75th day, 125th day, 175th day, and so on at an interval of approximately 50 days)</p> <p>[Example]<br/>Period of 15 minutes from 24th day, 20 hours, 16 minutes, and 23.648 seconds to (same day and hour) 31 minutes and 23.648 seconds<br/>Period of 15 minutes from 74th day, 13 hours, 19 minutes, and 10.944 seconds to (same day and hour) 34 minutes and 10.944 seconds</p> <p>[Example of failure] Oracle Solaris is booted from guest domain virtual network on SPARC M10-1</p> <p>SPARC M10-1, No Keyboard<br/>Copyright (c) 1998, 2016, Oracle and/or its affiliates. All rights reserved.<br/>OpenBoot 4.38.5, 14.0000 GB memory available, Serial #xxxxxxx.<br/>[ 2.19.0 ]<br/>Ethernet address xx:xx:xx:xx:xx:xx, Host ID: xxxxxxxx.</p> <p>WARNING: /virtual-devices@100/channel-devices@200/network@0: Unable to connect to virtual switch<br/>No viable default device found in boot-device variable.</p> <p>Evaluating:</p> <p>{0} ok</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]<br/>After system stops with the ok prompt and the elapse of 15 minutes, execute the boot command.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |

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Table 3-16 Problems Resolved in XCP 2330 (continued)

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-161108-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>In a system configuration with two crossbar boxes, logs may be registered with the wrong BB-ID if an "XB detected fatal clock failure" error or XB internal fatal error" error occurs.<br/>         [Example] BB-ID of "Code:" shows correct value "01", but BB-ID of "FRU" shows incorrect value "0".</p> <pre>XSCF&gt; showlogs error Date: May 16 08:49:52 JST 2016 Code: 80002100-0076110000ff0000ff-050067003013015f0000000000       ^       ^^ Status: Alarm Occurred: May 16 08:49:35.147 JST 2016 FRU: /BB#0/XBU#1      ^ Msg: XB detected fatal clock failure</pre> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>         After executing the showlogs error command, identify the faulty components and replace them based on the value of "Code:" indicating the suspected location.</p>                                                                                                                                                                                                                                                                                                                                                              |
| <b>RTI No.</b>     | <b>RTIF2-161012-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | Virtual function (VF) may be unusable if you forcibly stop (e.g. ldm stop-domain command with -f option) the I/O domain to which the VF of the 10 Gigabit Ethernet card is assigned.                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Workaround</b>  | <p>When stopping the I/O domain to which the VF is assigned, use the shutdown command.<br/>         [How to restore]<br/>         After clearing the Fault Report of the appropriate VF with the fmadm command on the I/O domain with the VF assigned, as well as on the root domain providing physical function (PF) of the VF, stop and start the I/O domain and the root domain with the shutdown command.</p>                                                                                                                                                               |
| <b>RTI No.</b>     | <b>RTIF2-161012-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | In the I/O domain to which the 10 Gigabit Ethernet card and the like are assigned, Oracle Solaris may hang when Oracle Solaris is booting.                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |

Table 3-16 Problems Resolved in XCP 2330 (continued)

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|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-160912-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | When a root complex other than BB#0/CMUL fails, the FRU of the PCI access error, which is displayed by the XSCF command showlogs error, may not be displayed properly.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>When a root complex fails, identify the failure location and replace the failure part according to the result displayed by the showstatus(8) command of the XSCF as shown below.</p> <p><b>Note</b> - Do not identify the failure location and perform maintenance according to the FRU of PCI access error displayed by the showlogs error command of the XSCF and the output result of the fmdump/fmadm command of Oracle Solaris.</p> <p>[Example] When the root complex of BB#01/CMUU/CPU#1 fails</p> <pre>XSCF&gt; showstatus BB#01 Status:Normal; *   CMUU Status:Degraded; *   CPU#1 Status:Degraded;</pre>                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-160616-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>In a system with two PCI expansion units connected, you may be unable to identify the connection of a PCI expansion unit from the XSCF by executing the ioxadm -v list command after the physical partition (PPAR) is powered on. However, you may be able to identify it from Oracle Solaris or the OpenBoot PROM on the physical partition.</p> <p>In this case, with the connection unidentified, it is not possible to perform active replacement of a LINK card or the fan unit or power supply unit of the PCI expansion unit. The settings are reset to the factory default and the logical domain may not be able to start if the system operates with a logical domain configuration.</p> <p>The settings are reset to the factory default and the logical domain may not be able to start if the system operates with a logical domain configuration.</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>Power off the physical partition (PPAR) and power it on again.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>RTI No.</b>     | <b>RTIF2-160606-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | If the reset command is executed while the physical partition (PPAR) is powered off, "no PSB available in PPAR" is registered in the error log at the PPAR power-on time, and PPAR power-on fails.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | <p>Do not execute the reset command while the physical partition (PPAR) is powered off.</p> <p>Execute the rebootxscf command to reboot the XSCF or turn off the input power of the system if the error log of "no PSB available in PPAR" is registered after executing the reset command.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |

# Problems Resolved in XCP 2322

The following table lists the problems resolved in XCP 2322.

Table 3-17 Problems Resolved in XCP 2322

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|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-161013-001</b>                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | If IP addresses are set to both XSCF-LAN#0 and XSCF-LAN#1, the XSCF-LAN#1 IP address may be set as the Agent Address for the SNMP Trap of SNMPv1.<br>Therefore, a retry by resource management software (ServerView Resource Orchestrator) to switch the servers may fail.<br>This symptom occurs when no takeover IP address is set for the SPARC M10-4S. |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                          |
| <hr/>              |                                                                                                                                                                                                                                                                                                                                                            |
| <b>RTI No.</b>     | <b>RTIF2-161004-001</b>                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | The OID information of scPPARStatus (PPAR status information) in the definition file of the XSCF extension MIB is not updated even when the control domain is rebooted.<br>Therefore, a retry by resource management software (ServerView Resource Orchestrator) to switch the servers may fail.                                                           |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                          |
| <hr/>              |                                                                                                                                                                                                                                                                                                                                                            |
| <b>RTI No.</b>     | <b>RTIF2-161004-002</b>                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | If you mount an IHV graphics card on a PCI Express expansion unit from IHV and connect the PCI Express expansion unit to the SPARC M10, the error "IOC register compare error" may be detected.                                                                                                                                                            |
| <b>Workaround</b>  | Set the POST diagnosis level to "off".<br>Perform the following.<br>1. Power off the physical partition.<br>XSCF> <b>poweroff -p x</b><br>2. Set the POST diagnosis level to "off".<br>XSCF> <b>setpparmode -p x -m diag=off</b>                                                                                                                           |

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# Problems Resolved in XCP 2321

The following table lists the problems resolved in XCP 2321.

Table 3-18 Problems Resolved in XCP 2321

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|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-160909-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>After Oracle Solaris startup, the following errors may be detected.</p> <pre><b>XSCF&gt; showlogs error -V</b> Date: xxx xx xx:xx:xx.xxx xxx xxxx Code: 40000000-00a20400480400a204-12bb00000000000000000000 Status: Warning                               Occurred: xxx xx xx:xx:xx.xxx xxx xxxx FRU: xxxxxxxxxxxxxx Msg: PCI access error Diagnostic Code:     00000200 00000000 0000     00000100 00000000 0000     00000200 00000000 0000     00000000 00000000 00000000 00000000     00000000 00000000 0000 Diagnostic Messages IO-FaultReport: TIME                UUID xxx xx xx:xx:xx    xxxxxxxx-xxxx-xxxx-xxxx-xxxxxxxxxxxxx MSG-ID xxxxxxxxxxxxx</pre> <p>If one of the following is indicated in FRU, it corresponds to this problem.</p> <pre>/BB#0/CMUL,/BB#0/CMUU /BB#0/CMUL /BB#0/CMUU /MBU</pre> <p>If one of the following is indicated in MSG-ID, it corresponds to this problem.</p> <pre>PCIEX-8000-YJ PCIEX-8000-KP PCIEX-8000-J5</pre> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>Update the XCP firmware to XCP 2321 or later.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |

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# Problems Resolved in XCP 2320

The following table lists the problems resolved in XCP 2320.

Table 3-19 Problems Resolved in XCP 2320

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| <b>RTI No.</b>     | <b>RTIF2-161108-002</b>                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-4</b>                                                                                                                                                                                                         |
| <b>Description</b> | If the PCI expansion unit is connected to PCI slot #9 of the SPARC M10-4, logical domains using the resources of the PCI expansion unit for PCI slot #9 may take a few more minutes to restart than other logical domains. |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                          |

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| <b>RTI No.</b>     | <b>RTIF2-160613-002</b>                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                         |
| <b>Description</b> | Security fixes.<br>(CVE-2016-0800)<br>For details, see the Oracle website relating to the following:<br><i>Critical Patch Updates</i> |
| <b>Workaround</b>  | Update the XCP firmware to XCP 2320 or later.                                                                                         |

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| <b>RTI No.</b>     | <b>RTIF2-160607-001</b>                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4</b>                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | Suppose that the PCI expansion unit is connected to PCI slot #9 of the SPARC M10-4. It is not possible to execute the <code>ioxadm</code> command to cause a chassis LED (locator) to blink to point to the PCI expansion unit connected to PCI slot #9. Nor is it possible to perform active replacement of the fan unit and power supply unit (PSU) of the PCI expansion unit.              |
| <b>Workaround</b>  | There is no effective workaround. If the <code>ioxadm</code> command was executed for a PCI expansion unit connected to a slot other than PCI slot #9, execute the <code>ioxadm</code> command again. To replace a fan unit or PSU of a PCI expansion unit connected to PCI slot #9, power off the physical partition connecting the PCI expansion unit and then replace the fan unit or PSU. |

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Table 3-19 Problems Resolved in XCP 2320 (continued)

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| <b>RTI No.</b>     | <b>RTIF2-160520-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-4</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | <p>If the firmware of the PCI expansion unit is updated when the unit is connected to PCI slot #9 of the SPARC M10-4, the update may fail with the following message displayed.</p> <p><b>Note</b> - The update will fail even on a PCI expansion unit connected to a slot other than PCI slot #9.</p> <p>[Example of output message]<br/> XSCF&gt; <b>ioxadm -c update PCIBOX#8002 -s 1200</b><br/> Firmware update is started. (version=1200) Operation was not successful.</p> <p>In a PCI expansion unit firmware update to a PCI expansion unit connected to PCI slot #9, it may take two hours or more until the ioxadm command ends with an error. Even though the firmware update of the PCI expansion unit failed, it does not affect the operation of the physical partition.</p> |
| <b>Workaround</b>  | <p>There is no effective workaround. If the ioxadm command was executed for a PCI expansion unit connected to a slot other than PCI slot #9, execute the ioxadm command again. To replace a fan unit or PSU of a PCI expansion unit connected to PCI slot #9, power off the physical partition connecting the PCI expansion unit and then replace the fan unit or PSU.</p>                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-160520-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | <p>The connection of a PCI expansion unit may not be identifiable from the XSCF by executing the ioxadm -v list command after the physical partition (PPAR) is powered on, although it is identifiable from Oracle Solaris or OpenBoot PROM on the physical partition.</p> <p>In this case, it is not possible to perform active replacement of the LINK card used with the XSCF functions, or a fan unit or power supply unit of the PCI expansion unit.</p>                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>Power off the physical partition and power it on again.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |

Table 3-19 Problems Resolved in XCP 2320 (continued)

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| <b>RTI No.</b>     | <b>RTIF2-160520-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | <p>If the graphics card (SP0X7GR1F) is used to start GNOME Display Manager (GDM), nothing may appear on the display unit.</p> <p>This problem occurs when Oracle Solaris is in the factory-default configuration.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | <p>Perform either [Workaround 1] or [Workaround 2] below.</p> <p>[Workaround 1]</p> <p>Comment out the BusID in the desktop environment information used by GNOME. This operation does not affect system performance, etc. Perform the following procedure.</p> <ol style="list-style-type: none"> <li>1. Edit BusID in /etc/X11/xorg.conf to make it a comment line (add "#" to the beginning of the line).<br/>           [Example] Comment out BusID written between the # First card start line and the # First card end line.<br/> <b># First card start</b><br/>           (Omitted)<br/> <b># BusID "PCI:8:0:0"</b><br/>           (Omitted)<br/> <b># First card end</b></li> <li>2. Apply the changed desktop environment information to GNOME.           <ul style="list-style-type: none"> <li>- If you are logged in to GNOME, log off from GNOME, and log in again.</li> <li>- If you are not logged in to GNOME, restart GDM, and log in to GNOME.</li> </ul>           [Example] If you are not logged in to GNOME, restart GDM.<br/> <b># /usr/sbin/svcadm restart gdm</b></li> </ol> <p>[Workaround 2]</p> <p>After saving logical domain configuration information, operate the system. GFX 550e Driver Software must be reinstalled because this problem be the cause of incorrectly configured PCI information used by the graphics card. If the hardware configuration has changed (including a change to the CPU Activation settings), this operation needs to be performed again. Perform the following procedure.</p> <ol style="list-style-type: none"> <li>1. Start Oracle Solaris with the current configuration (factory-default) left as is.</li> <li>2. Execute the ldm add-spconfig command to save the current logical domain configuration information.<br/>           [Example]<br/> <b># /usr/sbin/ldm add-spconfig &lt;config&gt;</b></li> <li>3. Stop Oracle Solaris.<br/>           [Example]<br/> <b># /usr/sbin/shutdown -y -g0 -i5</b></li> <li>4. Start Oracle Solaris, and reinstall the GFX 550e driver software.</li> </ol> <p>After performing the procedure, operate the system with the logical domain configuration information saved in step 2.</p> |

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Table 3-19 Problems Resolved in XCP 2320 (continued)

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| <b>RTI No.</b>     | <b>RTIF2-160520-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | If you execute the reset command for a logical domain while executing the reset command for another logical domain, the later reset command may be blocked. For this reason, if cluster software tries to switch clusters by executing the reset command, the cluster switching may fail. The clusters are switched successfully in the end because the cluster software changes paths in sequential order and executes the reset command to switch the clusters.                                                                                                                                                      |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>RTI No.</b>     | <b>RTIF2-160512-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | The ldm service may enter maintenance mode 734 days after starting the physical partition (PPAR). Consequently, none of the following will be available: Oracle VM Server for SPARC ldm command, logical domain operations (start/stop/change configuration/migration), automatic replacement of CPU cores, dynamic CPU/memory degradation, and monitoring (Host Watchdog) between a hypervisor and logical domain. In addition, when 1,101 days have elapsed after starting the PPAR, the logical domain may panic or a hypervisor may abnormally end (Hypervisor Abort).                                             |
| <b>Workaround</b>  | There is no effective workaround. After starting a physical partition (PPAR), restart (stop/start) the PPAR before 734 days have elapsed.<br>[How to restore]<br>Restart (stop/start) the physical partition (PPAR).                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-150521-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | When the <code>ioxadm(8)</code> command is executed, the message "Operation was not successful." is displayed and the command may fail.<br>[Example]<br><code>XSCF&gt; ioxadm -c update PCIBOX#0000 -s 1234</code><br>Firmware update is started. (version=1234)<br>Operation was not successful.<br><br>When the <code>prtfru(8)</code> command is executed, the message "An internal error has occurred. Please contact your system administrator." is displayed and the command may fail.<br>[Example]<br><code>XSCF&gt; prtfru</code><br>An internal error has occurred. Please contact your system administrator. |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Execute the command again.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |

# Problems Resolved in XCP 2290

The following table lists the problems resolved in XCP 2290.

Table 3-20 Problems Resolved in XCP 2290

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-160401-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>Security fixes.<br/>           (CVE-2015-7547)<br/>           (CVE-2013-4786)<br/>           For details, see the Oracle website relating to the following:<br/> <i>Critical Patch Updates</i></p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Workaround</b>  | <p>Update the XCP firmware to XCP 2290 or later.<br/>           To use the remote power management function, see "<a href="#">Notes on the Remote Power Management Function (RCIL)</a>."</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>RTI No.</b>     | <b>RTIF2-160203-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>When you set a string of 1017 or more characters in the OpenBoot PROM environment variable nvramrc, the following message appears, and the system may stop with the ok prompt displayed.<br/>           FATAL: free-memory: bad address.<br/>           ERROR: Last Trap: Memory Address not Aligned<br/>           TL: 1</p> <pre>%TL:1 %TT:34 %TPC:f0209020 %TnPC:f0209024 %TSTATE:4420001600 %CWP:0   %PSTATE:16 AG:0 IE:1 PRIV:1 AM:0 PEF:1 RED:0 MM:0 TLE:0 CLE:0 MG:0 IG:0   %ASI:20 %CCR:44 XCC:nZvc ICC:nZvc</pre> <pre>%TL:2 %TT:60 %TPC:f0246b54 %TnPC:f0200680 %TSTATE:14420001400 %CWP:0   %PSTATE:14 AG:0 IE:0 PRIV:1 AM:0 PEF:1 RED:0 MM:0 TLE:0 CLE:0 MG:0 IG:0   %ASI:20 %CCR:44 XCC:nZvc ICC:nZvc omitted</pre> <p>{0} ok</p> |
| <b>Workaround</b>  | <p>Confirm that a string of 1016 or less characters is set in nvramrc. Perform the following procedure at the ok prompt.</p> <ol style="list-style-type: none"> <li>1. If the OpenBoot PROM environment variable auto-boot? is true, set it to false, and then execute the reset-all command to restart the OpenBoot PROM.</li> <li>2. Execute the nvedit command.</li> <li>3. Edit the contents of nvramrc so that it contains a string of 1016 or fewer characters.</li> <li>4. Execute the nvstore command to apply the contents of nvramrc.</li> <li>5. Reset the OpenBoot PROM environment variable auto-boot?, and then execute the reset-all command to restart the OpenBoot PROM.</li> </ol>                                              |

Table 3-20 Problems Resolved in XCP 2290 (continued)

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| <b>RTI No.</b>     | <b>RTIF2-160129-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>After you execute the showstatus command, Status displays "Faulted" or "Degraded" for the faulty components of registered hardware failures in the error log. In some very rare cases, Status may incorrectly display "Deconfigured" instead.</p> <p>[Example] After an Alarm-level memory failure is detected, "Deconfigured" instead of "Faulted" is displayed at Status.</p> <pre>XSCF&gt; showlogs_error Date: Jan 1 00:00:00 JST 2016 Code: 80002000-006e070069040000ff-019204110000000000000000 Status: Alarm Occurred: Jan 1 00:00:00.000 UTC 2016 FRU: /BB#0/CMUL/MEM#00A,/BB#0/CMUL Msg: DIMM initialization serious error</pre> <pre>XSCF&gt; showstatus BB#00 Status:Normal; CMUL Status:Normal; * MEM#00A Status:Deconfigured; * MEM#01A Status:Deconfigured; * MEM#02A Status:Deconfigured; * MEM#03A Status:Deconfigured;</pre> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>Status is not correctly displayed with the showstatus command, but the faulty component has been degraded. Therefore, perform maintenance on the component.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |

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| <b>RTI No.</b>     | <b>RTIF2-160129-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>When you are maintaining a fan unit using the replacefru(8) command, the error log may show an incorrectly registered fan configuration error.</p> <p>If this happens, the system or chassis with a fan unit maintained by executing the showhardconf command or showstatus command is displayed as degraded, although it is not degraded.</p> <p>[Error message example]</p> <pre>Msg: FAN shortage</pre> <p>[Example] When the BB#00 fan was maintained</p> <pre>XSCF&gt; showstatus * BB#00 Status:Deconfigured;</pre> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>Ignore this error log as it has no effect on the maintenance work.</p> <p>After you finish maintaining the fan unit using the replacefru command, the chassis is no longer shown as degraded.</p>                                                                                                                                                                                                                                                        |

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Table 3-20 Problems Resolved in XCP 2290 (continued)

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| <b>RTI No.</b>     | <b>RTIF2-160129-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |    |          |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |    |          |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| <b>Description</b> | <p>When the CPU memory unit upper (CMUU) is removed, the number of CPU core resources displayed under "Installed" by the showcodusage command, or at "CPU Cores" and "Cores" by the showpparinfo command, is incorrect. Instead, it shows the number when the CMUU was mounted.</p> <p>[Example] When the CMUU is removed from the SPARC M10-4, the number of CPU core resources is supposed to be 32 but is displayed as 64.</p> <p>XSCF&gt; <b>showcodusage</b></p> <p>Resource In Use Installed CoD Permitted Status</p> <p>-----</p> <p>PROC 0 64 64 OK: 64 cores available ~~</p> <p>PPAR-ID/Resource In Use Installed Assigned</p> <p>-----</p> <table border="0"> <tr> <td>0 - PROC</td> <td>0</td> <td>64</td> <td>64 cores</td> </tr> <tr> <td>~~</td> <td></td> <td></td> <td></td> </tr> <tr> <td>1 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>2 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>3 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>4 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>5 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>6 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>7 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>8 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>9 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>10 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>11 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>12 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>13 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>14 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>15 - PROC</td> <td>0</td> <td>0</td> <td>0 cores</td> </tr> <tr> <td>Unused - PROC</td> <td>0</td> <td>0</td> <td>64 cores</td> </tr> </table> |    |          | 0 - PROC | 0 | 64 | 64 cores | ~~ |  |  |  | 1 - PROC | 0 | 0 | 0 cores | 2 - PROC | 0 | 0 | 0 cores | 3 - PROC | 0 | 0 | 0 cores | 4 - PROC | 0 | 0 | 0 cores | 5 - PROC | 0 | 0 | 0 cores | 6 - PROC | 0 | 0 | 0 cores | 7 - PROC | 0 | 0 | 0 cores | 8 - PROC | 0 | 0 | 0 cores | 9 - PROC | 0 | 0 | 0 cores | 10 - PROC | 0 | 0 | 0 cores | 11 - PROC | 0 | 0 | 0 cores | 12 - PROC | 0 | 0 | 0 cores | 13 - PROC | 0 | 0 | 0 cores | 14 - PROC | 0 | 0 | 0 cores | 15 - PROC | 0 | 0 | 0 cores | Unused - PROC | 0 | 0 | 64 cores |
| 0 - PROC           | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 64 | 64 cores |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| ~~                 |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |    |          |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 1 - PROC           | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 2 - PROC           | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 3 - PROC           | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 4 - PROC           | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 5 - PROC           | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 6 - PROC           | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 7 - PROC           | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 8 - PROC           | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 9 - PROC           | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 10 - PROC          | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 11 - PROC          | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 12 - PROC          | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 13 - PROC          | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 14 - PROC          | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| 15 - PROC          | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 0 cores  |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| Unused - PROC      | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0  | 64 cores |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |    |          |          |   |    |          |    |  |  |  |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |          |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |           |   |   |         |               |   |   |          |

Table 3-20 Problems Resolved in XCP 2290 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-151124-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | <p>When you perform the following procedure, "Hypervisor Abort" occurs, and the logical domain may fail to start. If you try to start the PPAR again at this time, the PPAR is rebooted repeatedly, and the logical domain cannot be started. In addition, the logical domain configuration information cannot be restored.</p> <ol style="list-style-type: none"> <li>1. Execute the dumpconfig command to save the XSCF setting information, including the logical domain configuration information.</li> <li>2. Change the logical domain configuration information.</li> <li>3. Specify the configuration information name before the change. Then, execute the ldm remove-sponconfig and ldm add-sponconfig commands to replace the current logical domain configuration information with the updated one and save it to the XSCF.</li> <li>4. Execute the restoreconfig command to restore the XSCF setting information, including the logical domain configuration information, saved in step 1.</li> <li>5. Start the physical partition (PPAR).</li> </ol> |
| <b>Workaround</b>  | <p>To restore the logical domain configuration information, perform the following procedure after step 4, instead of step 5 in [Description].</p> <ol style="list-style-type: none"> <li>1. Start the PPAR with the factory-default configuration.</li> <li>2. Restore the logical domain configuration information already saved to the XML file.</li> <li>3. Execute the ldm add-sponconfig command to save the logical domain configuration information to the XSCF.</li> </ol>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150914-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | <p>Suppose you use any of the operations below to turn on the power to the physical partition (PPAR) when you use the flashupdate(8) command to update the firmware. An "FMEM serious error" error log may be registered, the motherboard unit (MBU) or CPU memory unit (lower) (CMUL) may be degraded, and the PPAR power-on processing and firmware update may fail.</p> <ul style="list-style-type: none"> <li>- Power on using the operation panel power switch</li> <li>- Issue the power on instruction with the remote power management function (RCIL)</li> <li>- Issue the power on instruction with the auto power control system (APCS)</li> </ul> |
| <b>Workaround</b>  | <p>When you use the flashupdate(8) command to update the firmware, do not turn on the power to the physical partition (PPAR) using the power switch on the operation panel, RCIL, or APCS. Perform these operations after the firmware update is complete.</p> <p>You can use the showlogs event command to check whether the firmware update is complete. If the firmware update is complete, the following message is output.</p> <p>"XCP update has been completed (XCP version=xxxx:last version=yyyy)"</p> <p>[How to restore]</p> <p>Replace the motherboard unit (MBU) or the CPU memory unit (lower) (CMUL).</p>                                      |

Table 3-20 Problems Resolved in XCP 2290 (*continued*)

**RTI No.** RTIF2-150521-001

**Model** SPARC M10-4S

**Description** Suppose you have used the flashupdate(8) command to update the firmware with the power to the physical partition (PPAR) turned on. If you add a system board (PSB) by using dynamic reconfiguration, the following error log is registered, and the PSB may fail to be added.  
 Warning: /BB#x/CMUL:SCF:POST/OBP/HV data write error  
 Notice: /UNSPECIFIED:HYPERVERSOR:DR failed  
 Also, the PPAR at the PSB add destination is reset.

**Workaround** Execute the version -c xcp -v command, and check the [CMU BACKUP] lines.

```
[Example]
XSCF> version -c xcp -v
BB#00-XSCF#0 (Master)
XCP0 (Reserve): 2260
CMU : 02.25.0000
 POST : 3.9.0
 OpenBoot PROM : 4.36.1+2.10.0
 Hypervisor : 1.4.1
XSCF : 02.26.0000
XCP1 (Current): 2260
CMU : 02.25.0000
 POST : 3.9.0
 OpenBoot PROM : 4.36.1+2.10.0
 Hypervisor : 1.4.1
XSCF : 02.26.0000
BB#01-XSCF#0 (Standby)
XCP0 (Reserve): 2260
CMU : 02.25.0000
 POST : 3.9.0
 OpenBoot PROM : 4.36.1+2.10.0
 Hypervisor : 1.4.1
XSCF : 02.26.0000
XCP1 (Current): 2260
CMU : 02.25.0000
 POST : 3.9.0
 OpenBoot PROM : 4.36.1+2.10.0
 Hypervisor : 1.4.1
XSCF : 02.26.0000
CMU BACKUP
#0: 02.26.0000
#1: ..
XSCF>
```

Check the line where Current is displayed either for XCP0 or XCP1 on the PSB (BB#xx) where the PPAR is running. If the CMU firmware version shown on the next line after it ("CMU:02.25.0000" in the above example) is not the same as the CMU firmware version under [CMU BACKUP] ("#0: 02.26.0000" in the above example), update the XCP firmware to the CMU firmware version shown on the next line after Current while the PPAR is running. If the CMU firmware versions are the same, you do not need to update the firmware.

Table 3-20 Problems Resolved in XCP 2290 (continued)

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Afterward, regardless of whether the CMU firmware versions are the same, perform the following procedure in advance of any firmware update using the flashupdate(8) command while the PPAR is running.

1. Execute the switchscf(8) command to switch the master XSCF.
2. Execute the rebootxscf -a command to reset all XSCFs.

The above steps 1. and 2. are not necessary for a firmware update while the PPAR is stopped.

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## Problems Resolved in XCP 2280

The following table lists the problems resolved in XCP 2280.

Table 3-21 Problems Resolved in XCP 2280

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|                    |                                                                                                                                                                                                                                                                                                                                                                                                                            |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-160909-001</b>                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | The XSCF may be unable to detect power supply unit (PSU) failures.                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | Use one of the following methods to determine whether a PSU has failed. If a PSU failure has been determined, replace the PSU. <ul style="list-style-type: none"><li>- The PSU LED goes on (amber) or blinks (green) while the physical partition (PPAR) is running.</li><li>- After the physical partition (PPAR) is powered off, a PSU failure is registered in an XSCF log when the PPAR is powered on again.</li></ul> |
| <b>RTI No.</b>     | <b>RTIF2-160613-001</b>                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | Security fixes.<br>(CVE-2013-2566)<br>For details, see the Oracle website relating to the following:<br><i>Critical Patch Updates</i>                                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | Update the XCP firmware to XCP 2280 or later.                                                                                                                                                                                                                                                                                                                                                                              |

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Table 3-21 Problems Resolved in XCP 2280 (*continued*)

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| <b>RTI No.</b>     | <b>RTIF2-151117-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | <p>If there are multiple iSCSI targets when the show-iscsi command is executed at the ok prompt, one of the iSCSI targets may be unable to log in. The detection of such a target will terminate the command with the "login failed" error displayed. At this time, no information is output about other iSCSI targets that have not tried to log in.</p> <p>[Example] Login failure of the second iSCSI target<br/>         {0} ok <b>show-iscsi /pci@8100/pci@4/pci@0/pci@0/network@0</b><br/>         1000 Mbps full duplex Link up<br/>         Target: iqn.1986-03.com.sun:02:27f6951c-a432-4a86-b6c4-de72743cd25a<br/>         Lun 0-0-0-0<br/>         Disk SUN COMSTAR 1.0 71669440 Blocks, 36 GB<br/>         Lun 1-0-0-0<br/>         Disk SUN COMSTAR 1.0 71669440 Blocks, 36 GB<br/>         Target: iqn.1986-03.com.sun:02:c5662c3b-a6d9-4a69-b0d0-c6445a23383b<br/>         Evaluating:<br/>         login failed<br/>         {0} ok</p> |
| <b>Workaround</b>  | If the output of the show-iscsi command does not show the intended iSCSI target, check the iSCSI target information from the target. For the check procedure from the iSCSI target, see the respective target manual.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-151105-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | <p>Security fixes.<br/>         (CVE-2015-4000)<br/>         For details, see the Oracle website relating to the following:<br/> <i>Critical Patch Updates</i></p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | Update the XCP firmware to XCP 2280 or later.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>RTI No.</b>     | <b>RTIF2-151105-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | Suppose that you select [Verified Boot] from the [PPAR Operation] menu of XSCF Web to register the X.509 public key certificate for the user using the verified boot. In this case, "Space is not allowed." is displayed for the attempt to register the copied X.509 public key certificate, and the certificate cannot be registered.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | To register the copied X.509 public key certificate, use the addvbootcerts(8) command. Use XSCF Web to register the X.509 public key certificate by specifying USB media or an http/https server.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>RTI No.</b>     | <b>RTIF2-151105-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | If a fan failure occurs, the physical partition (PPAR) may be powered off even when it does not need to be powered off.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>         [How to restore]<br/>         Replace the faulty fan unit. After that, power on the PPAR.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |

Table 3-21 Problems Resolved in XCP 2280 (continued)

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-151105-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | If "PSU input power failure / PSU input power recover" is frequently registered in the event log because of power supply unit (PSU) failures, an XSCF panic may occur repeatedly, and the XSCF may stop.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Workaround</b>  | There is no effective workaround.<br>If this is frequently registered in the PSU event log, replace the faulty PSU.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>RTI No.</b>     | <b>RTIF2-151105-005</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | Suppose that a FRU fails when you replace or add a SPARC M10-4S chassis or replace an XSCF unit (XSCFU). In this case, a timeout may occur in 90 minutes even though the timeout is supposed to occur in 45 minutes according to the display of the maintenance menu.<br>[Example] A FRU of the XSCFU is faulty.<br>Waiting for XBBOX#81/XSCFU to enter ready state.<br>[This operation may take up to 45 minute(s)]<br>(progress scale reported in seconds) 0..... 30..... 60..... 90..... 120..... 150..... 180..... 210.....<br>240..... 270..... 300..... 330..... 360..... 390..... 420..... 450.....<br>480..... 510..... 540..... 570..... 600..... 630..... 660..... 690.....<br>--- Omitted ---<br>2400.....2430.....2460.....2490.....2520.....2550.....2580.....2610....<br>2640.....2670.....2700.....Omitted |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Use another FRU.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-151105-006</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | Suppose that you select [PSB Configuration] from the [PPAR Operation] menu of XSCF Web to set memory mirror mode for the CPU of the system board. In this case, "System error: unknown error." is displayed and the setting cannot be made.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | To set memory mirror mode for the SPARC M10-1, use the setupfru(8) command.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |

Table 3-21 Problems Resolved in XCP 2280 (continued)

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|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-151020-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | <p>Suppose that the firmware version is XCP 2250 or later in a system configuration that has at least 3 BBs or a crossbar box. In this system configuration, if Oracle Solaris stays running for a long time after XSCF master/standby switching, the Oracle Solaris time may deviate when the physical partition (PPAR) is powered off and on.</p> <p>Also, in the same system configuration with a similar firmware version, the following error log is registered with the XSCF every 10 days after XSCF master/standby switching.</p> <p>Date: Sep 15 11:42:38 JST 2015<br/> Code: 20000000-00560300fcff0000ff-01e400010000000000000000<br/> Status: Notice Occurred: Sep 15 11:43:08.531 JST 2015<br/> FRU: /BB#2/CMUL,/FIRMWARE<br/> Msg: XSCF self diagnosis warning detection</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | <p>After XSCF master/standby switching, promptly perform any of the following operations.</p> <ul style="list-style-type: none"> <li>- Execute the <code>rebootxscf(8)</code> command to reset all the XSCFs.</li> <li>- Execute the <code>rebootxscf(8)</code> command to reset the XSCFs that are neither the master nor standby.</li> <li>- Stop all the PPARs, and then turn off all the input power supplies. After that, wait at least 30 seconds, and then turn on the input power supplies.</li> </ul> <p>[How to restore]</p> <p>Execute the following procedure to start Oracle Solaris in single-user mode and then set the time.</p> <ol style="list-style-type: none"> <li>1. Execute the <code>poweroff(8)</code> command to power off the PPAR.</li> <li>2. Execute the <code>showpparstatus(8)</code> command to confirm that the status is "Powered off," indicating that the PPAR power is off.</li> <li>3. Set the OpenBoot PROM environment variable <code>auto-boot?</code> to "false."<br/> XSCF&gt; <b>setpparparam -p <i>ppar_id</i> -s bootscript "setenv auto-boot? false"</b></li> <li>4. Execute the <code>poweron(8)</code> command to power on the PPAR.</li> <li>5. Execute the <code>console(8)</code> command to connect to the control domain console and display the ok prompt.</li> <li>6. Boot Oracle Solaris in single-user mode.<br/> {0} ok <b>boot -s</b></li> <li>7. Execute the <code>date(1)</code> command to set the Oracle Solaris time.</li> </ol> <p>[Example] Setting for 18:30:00 on October 20, 2015<br/> # <b>date 1020183015.00</b></p> |

Table 3-21 Problems Resolved in XCP 2280 (continued)

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|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150818-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | <p>When you execute the deleteboard(8) command with the DR feature to remove a system board (PSB) where the XSCF is rebooting, the deleteboard(8) command may end abnormally with the "SCF process down detected" error. If the target system board is the standby XSCF, the master XSCF will become non-responsive while inactive. If the target system board is the slave XSCF, the master XSCF will reboot, with the result of switching between the master XSCF and standby XSCF.</p> <p>In contrast, when the addboard(8) command is executed to add a PSB, and a command timeout is detected, then the command ends abnormally. In this event, the master XSCF and standby XSCF are not switched.</p> <p>The following example shows the deleteboard(8) command terminated due to an internal error:</p> <p>[Example]<br/> XSCF&gt; <b>deleteboard -c disconnect -m unbind=resource 00-0</b><br/> PSB#00-0 will be unconfigured from PPAR immediately. Continue?[y n] :<b>y</b><br/> All domains are temporarily suspended, proceed?[y n] :<b>y</b><br/> Start unconfigure preparation of PSB. [1200sec]<br/> 0 /end<br/> An internal error has occurred. Please contact your system administrator.<br/> XSCF&gt;</p> |
| <b>Workaround</b>  | <p>Execute the showhardconf(8) command to confirm that [Status] of every crossbar box and the SPARC M10-4S is "Normal." Then, execute the deleteboard(8) or addboard(8) command. During the execution of the deleteboard(8) or addboard(8) command, do not execute any command that involves an XSCF reboot by the rebootxscf -a or switchxscf(8) command.</p> <p>[How to restore]</p> <ul style="list-style-type: none"> <li>- If the target system board is the standby XSCF <ul style="list-style-type: none"> <li>Turn off the input power to all SPARC M10 system chassis or crossbar box chassis, and then turn it on again (AC-OFF/ON).</li> </ul> </li> <li>- If the target system board is the slave XSCF <ul style="list-style-type: none"> <li>Follow the instructions in [Workaround] to remove or add the system board using the DR feature.</li> </ul> </li> </ul>                                                                                                                                                                                                                                                                                                                                            |
| <b>RTI No.</b>     | <b>RTIF2-150729-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>When a DIMM failure is detected during the self-diagnosis test (POST) on the physical partition (PPAR), the component is marked as a failure. Even after DIMM replacement, the failure mark is not cleared. In the following example, after replacement, components are marked as failures and their status is "Deconfigured."</p> <p>[Example]<br/> XSCF&gt; <b>showstatus</b><br/> * MBU Status:Deconfigured;<br/> * CPU#0 Status:Deconfigured;<br/> * MEM#00A Status:Deconfigured;<br/> * MEM#01A Status:Deconfigured;<br/> * MEM#02A Status:Deconfigured;<br/> * MEM#03A Status:Deconfigured;</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]<br/> Update the firmware to XCP 2250 or later. Set the mode switch on the operation panel to Service mode, turn off the input power, and then turn it on again (AC OFF/ON).</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |

Table 3-21 Problems Resolved in XCP 2280 (continued)

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| <b>RTI No.</b>     | <b>RTIF2-150708-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>Suppose the motherboard unit (MBU), CPU memory unit lower (CMUL), or CPU memory unit upper (CMUU) is replaced at the same time as a microSD card. Then, the self-diagnosis test (POST) on the physical partition (PPAR) detects and marks the component as a failure. Even after the replacement, the failure mark is not cleared. In the following example, after replacement, components are marked as failures and their status is "Deconfigured."</p> <p>[Example]</p> <pre>XSCF&gt; showstatus * MBU Status:Deconfigured; * CPU#0 Status:Deconfigured; * MEM#00A Status:Deconfigured; * MEM#01A Status:Deconfigured; * MEM#02A Status:Deconfigured; * MEM#03A Status:Deconfigured;</pre> |
| <b>Workaround</b>  | <p>When replacing the motherboard unit (MBU), CPU memory unit lower (CMUL), or CPU memory unit upper (CMUL) and a microSD card, replace them one at a time, not at the same time.</p> <p>[How to restore]</p> <p>Update the firmware to XCP 2250 or later. Set the mode switch on the operation panel to Service mode, turn off the input power, and then turn it on again (AC OFF/ON).</p>                                                                                                                                                                                                                                                                                                      |
| <b>RTI No.</b>     | <b>RTIF2-150626-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>Suppose that you select [PPAR Operation] from the [Verified Boot] menu of XSCF Web and specify a PPAR that is not PPAR#0 to display details of the X.509 public key certificate for a user using the verified boot. In this case, the wrong X.509 public key certificate is displayed for PPAR#0.</p>                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | <p>To check the public key certificate of a PPAR that is not PPAR#0, use the showvbootcerts(8) command.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>RTI No.</b>     | <b>RTIF2-140808-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>After the occurrence of a component failure, you may fail to power off the physical partition (PPAR).</p> <p>This phenomenon may occur when the following procedure is performed:</p> <ol style="list-style-type: none"> <li>1. While the PPAR is being reactivated due to a component failure, the poweroff(8) command is executed without the -f option.</li> <li>2. After the PPAR is powered off upon a PPAR reactivation failure due to a component failure, the poweron(8) command is executed to power on the PPAR.</li> <li>3. The poweroff(8) command is executed without the -f option.</li> </ol>                                                                                  |
| <b>Workaround</b>  | <p>Do not execute the poweroff(8) command while the PPAR is being reactivated due to a component failure.</p> <p>[How to restore]</p> <p>Execute the shutdown command from Oracle Solaris to power off the PPAR.</p> <p>[Example]</p> <pre># shutdown -y -g0 -i5</pre>                                                                                                                                                                                                                                                                                                                                                                                                                           |

Table 3-21 Problems Resolved in XCP 2280 (continued)

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| <b>RTI No.</b>     | <b>RTIF2-140616-005</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | <p>If you turn off the input power of the master XSCF (AC OFF) and then turn it on again (AC ON) when the power to the physical partition (PPAR) is off, error handling may fail due to the temporary absence of the master XSCF. In addition, the PPAR may remain abnormal so that you may be unable to power on the PPAR again.</p> <p>You can determine the occurrence of this phenomenon by executing the showboards(8) command. If the value of "Pwr Conn Conf" displayed as the master XSCF PSB status is "n y n," this phenomenon has occurred.</p> <p>[Example] If the master XSCF is BB#00</p> <pre>XSCF&gt; showboards -a PSB  PPAR-ID Assignment  Pwr  Conn Conf Test Fault ---  ----- 00-0 00(00) Assigned    n   y   n   Passed Normal 01-0 01(00) Assigned    y   y   n   Passed Normal</pre> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>Turn off the input power to all SPARC M10-4S chassis that make up the system and then turn it on again.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |

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| <b>RTI No.</b>     | <b>RTIF2-140507-010</b>                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                       |
| <b>Description</b> | <p>In a system configuration with at least 3 BBs or a crossbar box, if the master/standby switchover occurs more than 256 times, the "SCF process down detected" error is detected by the slave XSCF, and XSCF "coremgrd" may cause a "process down."</p> |
| <b>Workaround</b>  | <p>Do not perform consecutive XSCF master/standby switchover.</p> <p>[How to restore]</p> <p>Reset the XSCF. The system will become usable.</p>                                                                                                           |

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| <b>RTI No.</b>     | <b>RTIF2-140507-012</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | <p>In a system with several SPARC M10-4S, if a crossbar box or the XSCF unit of a crossbar box is replaced after powering off all chassis and then the input power is turned on, the following error log may be registered.</p> <p>Indispensable parts are not installed (OPNL).</p> <p>Indispensable parts are not installed (FAN).</p> <p>Indispensable parts are not installed (PSU).</p>                                                                                                          |
| <b>Workaround</b>  | <p>When replacing a crossbar box, do so after turning off the input power (AC OFF) to only the crossbar box being replaced.</p> <p>When replacing the XSCF unit of a crossbar box, execute the replacefru(8) command to do so. Refer to the <i>Fujitsu M10-4/Fujitsu M10-4S/SPARC M10-4/SPARC M10-4S Service Manual</i> for replacing the crossbar box or the XSCF unit in the crossbar box.</p> <p>[How to restore]</p> <p>Turn off/on the input power (AC OFF/ON) of the replaced crossbar box.</p> |

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Table 3-21 Problems Resolved in XCP 2280 (continued)

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|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140410-005</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | <p>If a hardware error is detected immediately after powering off or restarting the physical partition (PPAR), the PPAR power off or restart process may be stopped, and may not be completed.</p> <p>To check whether this phenomenon has occurred, power off or restart the PPAR and then execute the <code>showpparprogress(8)</code> command. If the PPAR remains in the power on state and the power off process has not completed after at least one minute has passed, this phenomenon has occurred.</p> <p>[Example of unsuccessful power off]<br/> XSCF&gt; <b>showpparprogress -p 0</b><br/> This PPAR is powered on.</p> <p>Normally, when the PPAR power off completes, the power off sequence and PPAR state are as follows.</p> <p>[Example of successful power off]<br/> XSCF&gt; <b>showpparprogress -p 0</b><br/> PPAR Power Off                    PPAR#0 [1/3]<br/> CPU Stop                            PPAR#0 [2/3]<br/> PSU Off                             PPAR#0 [3/3]</p> <p>The sequence of power control is completed.</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]<br/> If the PPAR power supply remains in the power-on state 30 minutes after executing the <code>showpparprogress(8)</code> command, turn off the input power to all chassis and then turn it on again (AC-OFF/ON).</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-140409-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | <p>If you disconnect and then connect USB memory while the executed <code>snapshot(8)</code> command is collecting information into the memory, the following hardware error may be detected:<br/> Msg: DMA timeout error Hard detected</p> <p>At this time, the logical domain is continuously operated but the functions that access hardware (e.g., power on/off, monitoring function) no longer operate.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | <p>Do not connect and then disconnect the USB memory while information is being collected into the memory by the <code>snapshot(8)</code> command.</p> <p>[How to restore]<br/> Turn off the input power to the system and then turn it on again (AC OFF/ON).</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |

Table 3-21 Problems Resolved in XCP 2280 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140409-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | <p>An error log due to hardware is erroneously registered in the following cases.</p> <ul style="list-style-type: none"> <li>- A watchdog timeout occurs due to a software factor</li> <li>- XSCF RESET switch is pressed</li> <li>- XSCF panic occurs due to firmware</li> </ul> <p>[Example]</p> <ul style="list-style-type: none"> <li>- Incorrect</li> </ul> <p>Status: Alarm<br/>FRU: /FIRMWARE,/MBU<br/>Msg: SCF panic detected</p> <ul style="list-style-type: none"> <li>- Correct</li> </ul> <p>Status: Notice<br/>FRU: /FIRMWARE,/MBU<br/>Msg: SCF panic detected</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>Read the "Alarm" status as "Notice".</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>RTI No.</b>     | <b>RTIF2-140402-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | <p>The following special characters cannot be included in the e-mail address input field for the [Settings] - [Service] - [SMTP], [Settings] - [Email Reporting], [Settings] - [Audit], and [Settings] - [CoD Activation] menus on XSCF Web.</p> <p>!"#\$%&amp;'()*+,-./:;=?@^_`{ }~</p>                                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | <p>Use the setemailreport(8) command in the XSCF shell.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-140402-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | <p>When using XSCF Web in Internet Explorer, if you attempt to delete the second and subsequent User/Group settings of [Groups] and [View Access] of VACM in the [XSCF] - [Settings] - [Service] - [SNMP Security] menu, an error message is output and the deletion cannot be executed.</p>                                                                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | <p>Execute the deletion by using XSCF Web in Firefox or the setsnmpvacm(8) command in the XSCF shell.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>RTI No.</b>     | <b>RTIF2-140212-008</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | <p>When powering on, or rebooting a physical partition (PPAR), even if the process is stopped due to the failure of a component, the POWER LED of the operation panel keeps blinking. This problem does not occur while operating the system.</p>                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>Blinking of the POWER LED on the operation panel is canceled if the physical partition (PPAR) is powered on when the process of powering on/restarting the physical partition (PPAR) is interrupted.</p>                                                                                                                                                                                                                                                                                                                               |

Table 3-21 Problems Resolved in XCP 2280 (continued)

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|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140212-012</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | In a system configuration with at least 3 BBs or a crossbar box, if the XSCF master/ standby switchover occurs 60 times or more, a "process down" may occur on the slave XSCF and the slave XSCF may be reset.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Workaround</b>  | There is no effective workaround.<br>The slave XSCF will be restored by the reset and the system can be used continuously after that.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>RTI No.</b>     | <b>RTIF2-131213-012</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | After replacing SPARC M10 with the replacefru(8) command, diagnosis processing may not terminate properly.<br>[Example] The replacement of BB#2<br>Diagnostic tests for BB#2 have started.<br>Initial diagnosis is about to start, Continue?[y n] :y<br>PSB#02-0 power on sequence started.<br>0.....30.....end<br>Initial diagnosis started. [7200sec]<br>0..... 30..... 60..... 90.....120.....150.....180.....210.....240.....<br>270.....300.....330.....360.....390.....420.....450.....480.....510.....<br>540.....570.....600.....630.....660.....690.....720.....750.....780.....<br>810.....840.....870.....900..end<br>Initial diagnosis has completed.<br>PSB power off sequence started. [1200sec]<br>0..... 30..... 60..... 90.....120.....150.....180.....210.....240.....<br>270.....300.....330.....360.....390.....420.....450.....480.....510.....<br>540.....570.....600.....630.....660.....690.....720.....750.....780.....<br>810.....840.....870.....900.....930.....960.....990.....1020.....1050.....<br>1080.....1110.....1140.....1170.....1200end<br>Failed to power off. Please check the FRU.<br>An internal error has occurred. Please contact your system administrator.<br>done<br>[Warning:030]<br>testsb failed.[c:cancel] : |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Re-execute the replacefru(8) command.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-130802-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | When you replace the XSCF unit of the crossbar box by using the replacefru(8) command, the command is completed normally, but the following error log may be registered. The suspected location indicated by this error log is incorrect.<br>SCF:Board control error (link failed)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Workaround</b>  | Replace the XSCF unit of the same crossbar box by using the replacefru(8) command again. If you have turned off the input power (AC OFF) during the replacement work, disconnect and then connect all the XSCF BB control cables.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |

Table 3-21 Problems Resolved in XCP 2280 (*continued*)

|                    |                                                                                                                                                                                                                                                                            |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130711-003</b>                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                        |
| <b>Description</b> | Before active replacement of the XSCF unit of the crossbar box is completed, the <code>replacefru(8)</code> command is completed normally.                                                                                                                                 |
| <b>Workaround</b>  | When you use the <code>replacefru(8)</code> command to replace the XSCF unit, the following message may appear after you replace the XSCF unit. If it does, wait 10 minutes, and then enter "f".<br>The replacement of XBBBOX#xx/XSCFU has completed normally.[f:finish] : |

|                    |                                                                                                                                                                                                                            |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130305-023</b>                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                        |
| <b>Description</b> | If an error occurs in the DC-DC converter of a crossbar unit, "XB-XB interface link-up error." is erroneously registered instead of the correct error log of "LSI detected errors with power subsystem failure."           |
| <b>Workaround</b>  | There is no effective workaround.<br>Check for power errors if the error log of "XB-XB interface link-up error" was registered.<br>Ignore the error log for "XB-XB interface link-up error" if a power error has occurred. |

## Problems Resolved in XCP 2271

The following table lists the problems resolved in XCP 2271.

Table 3-22 Problems Resolved in XCP 2271

|                    |                                                                                                                                                                                                                                                                                                                                                     |
|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150929-001</b>                                                                                                                                                                                                                                                                                                                             |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | When you turn on the input power to the SPARC M10-4S or crossbar box that has a failure in the connection between chassis in a system having a building block configuration, the XSCF of these chassis may not start. After that, the failed XSCF or the XSCF BB control cable connected to this XSCF may not be displayed as a suspected location. |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Replace the CPU memory unit lower (CMUL) of the XSCF that has not been started or the XSCF unit. Also replace the XSCF BB control cable connected to it.                                                                                                                                   |

|                    |                                                                                                                                                   |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150929-002</b>                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                               |
| <b>Description</b> | When you turn on the input power to all the chassis in a system having a building block configuration, the master XSCF may be reset and switched. |
| <b>Workaround</b>  | There is no effective workaround.<br>Execute the <code>switchscf(8)</code> command to switch the master XSCF.                                     |

Table 3-22 Problems Resolved in XCP 2271 (*continued*)

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|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150729-001</b>                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | In a system where SPARC M10-4S units from a 2BB configuration to a 4BB configuration are connected via direct inter-chassis connection, the deleteboard(8) command is executed to delete a system board with dynamic reconfiguration of a physical partition (PPAR DR). However, subsequent PPAR DR operations fail. |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Execute the poweroff(8) and poweron(8) commands, turn off the power to the PPAR, and then turn it on again.                                                                                                                                                 |

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150710-002</b>                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                             |
| <b>Description</b> | Indicating an excessive fan rotation speed, "FAN speed too high" in the error log is mistakenly registered for a non-defective fan.                                                                                       |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Execute the replacefru(8) command, and select the fan registered in the error log. Use the fan as is. Perform fan replacement by following the maintenance menu. |

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Table 3-22 Problems Resolved in XCP 2271 (continued)

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|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150629-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | <p>Suppose that a total of 29 or more PCIe or link cards is installed in some or all of the SPARC M10-4S units in a system consisting of multiple SPARC M10-4S units. When executed in this system, the testsb -a command takes time for the communication processing between each card and the XSCF, and the command terminates abnormally. For example, in a system in an 8BB configuration, the command terminates abnormally approximately 30 minutes after the start of command execution.</p> <p>Also suppose that a total of 29 or more PCIe or link cards is installed in some or all of the SPARC M10-4S units in a physical partition (PPAR) consisting of multiple SPARC M10-4S units. When executed, the poweron(8) command causes repeated XSCF retry processing, and the PPAR cannot be powered on. "Event: SCF:Reset retry" is registered in the event log at this time.</p> <p>[Example of the testsb command]<br/> XSCF&gt; <b>testsb -v -p -s -a -y</b><br/> Initial diagnosis is about to start, Continue?[y n] :<b>y</b><br/> PSB power on sequence started.</p> <p>Hardware error occurred by initial diagnosis.<br/> PSB power off sequence started. [1200sec]<br/> 0...end<br/> PSB powered off.<br/> PSB Test Fault<br/> -----<br/> 00-0 Failed Faulted<br/> 01-0 Failed Faulted<br/> 02-0 Failed Faulted<br/> 03-0 Failed Faulted<br/> 04-0 Failed Faulted<br/> 05-0 Failed Faulted<br/> 06-0 Failed Faulted<br/> 07-0 Failed Faulted</p> <p>A hardware error occurred. Please check the error log for details.</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/> Update the XCP firmware to XCP 2271 or later.<br/> [How to restore]<br/> Turn off and on (AC OFF/ON) the input power to all the SPARC M10-4S chassis.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>RTI No.</b>     | <b>RTIF2-150522-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-1</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | <p>If you disconnect one of the power cords from a power supply unit (PSU), the following problems may occur.- Power failures are not registered in the event log.- The showhardconf(8) command unvaryingly shows "ON; AC:xxx V;" in "Power_Status" of the PSU.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/> [How to restore]<br/> Connect the power cord again.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |

## Problems Resolved in XCP 2270

The following table lists the problems resolved in XCP 2270.

Table 3-23 Problems Resolved in XCP 2270

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-160129-003</b>                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | Suppose that a total of 47 or more PCI cards or link cards is installed in one physical partition (PPAR) and that "Msg: SB deconfigured (not running)" is registered in the error log when the PPAR is powered on. Then, the PPAR may be powered on with a degraded PSB.                                                                                                                                |
| <b>Workaround</b>  | There is no effective workaround.<br>Perform the following procedure. <ol style="list-style-type: none"><li>1. Set the mode switch on the operation panel (OPNL) of the master chassis to Service.</li><li>2. Turn off the input power to the system and then turn it on again (AC OFF/ON).</li><li>3. Reset the mode switch on the OPNL of the master chassis.</li><li>4. Power on the PPAR.</li></ol> |

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## Problems Resolved in XCP 2260

The following table lists the problems resolved in XCP 2260.

Table 3-24 Problems Resolved in XCP 2260

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| <b>RTI No.</b>     | <b>RTIF2-150710-001</b>                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                             |
| <b>Description</b> | Security fixes.<br>(CVE-2014-3570),<br>(CVE-2014-3571),<br>(CVE-2014-3572),<br>(CVE-2014-8275),<br>(CVE-2015-0204),<br>(CVE-2015-0235)<br>For details, see the Oracle website relating to the following:<br><i>Critical Patch Updates</i> |
| <b>Workaround</b>  | Update the XCP firmware to XCP 2260 or later.                                                                                                                                                                                             |

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Table 3-24 Problems Resolved in XCP 2260 (continued)

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|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150622-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>The following phenomena occur while a physical partition (PPAR) is powered on.</p> <ol style="list-style-type: none"> <li>1. When an excessively low fan speed is detected, it is registered in the error log. If this error is detected multiple times, domain power-on will fail.</li> <li>2. The fan speed level reaches level 5 (full speed), resulting in a noisy fan. One or no fan error is registered in the error log at this time.</li> </ol>                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>Use either of the following procedures.</p> <ul style="list-style-type: none"> <li>- How to restore 1           <ul style="list-style-type: none"> <li>Execute the <code>replacefru(8)</code> command for pseudo active replacement of all the fans (temporarily remove and then reinstall the fans).</li> </ul> </li> <li>- How to restore 2           <ul style="list-style-type: none"> <li>Turn off the power to the PPAR, and then disconnect and reconnect the input power supply of the system (AC OFF/ON). When turning off/on the input power, power off the system, wait 30 seconds, and then power on the system.</li> </ul> </li> </ul> <p>Even after you have tried both of the above procedures, the fan may again rotate at the maximum speed with no reduction in noise.</p> |
| <b>RTI No.</b>     | <b>RTIF2-150611-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | The fan speed may increase although the physical partition is powered off.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>RTI No.</b>     | <b>RTIF2-150514-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>If you connect a PCI expansion unit to PCI slot #0 of a SPARC M10 system chassis and execute the <code>prtfru(8)</code> or <code>snapshot(8)</code> command, a "process down" may occur and the XSCF may be reset. Also, the physical partition is operating with two or more link cards connected to PCI slot #0 of a SPARC M10 system chassis, a "process down" may occur and the XSCF may be reset.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | <ul style="list-style-type: none"> <li>- To execute the <code>prtfru(8)</code> command, connect the PCI expansion unit to a PCI slot other than #0.</li> <li>- To execute the <code>snapshot(8)</code> command, perform either of the following operations:           <ul style="list-style-type: none"> <li>- Connect the PCI expansion unit to a PCI slot other than #0.</li> <li>- Execute the <code>snapshot(8)</code> command without specifying the Full log set (-L F) option.</li> </ul> </li> </ul> <p>[How to restore]</p> <p>Since the system is restored by resetting the XSCF, you can continue to operate the system.</p>                                                                                                                                                                                                                          |
| <b>RTI No.</b>     | <b>RTIF2-150514-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>If the XCP version at <code>dumpconfig(8)</code> command execution is different from the XCP version at <code>restoreconfig(8)</code> command execution, a problem may occur.</p> <p>For details, see "<a href="#">Problems Related to RTIF2-150514-002.</a>"</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Workaround</b>  | See " <a href="#">RTIF2-150514-002 Workarounds and How to Restore.</a> "                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |

Table 3-24 Problems Resolved in XCP 2260 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150514-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | If you power on a physical partition or execute the diagxbu(8) command while the testsb(8) command is being executed, the testsb(8) command may terminate abnormally.                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Workaround</b>  | There is no effective workaround.<br>If the testsb(8) command terminates abnormally, execute the testsb(8) command again.                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-150507-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | The power supply units (PSUs) of the SPARC M10-1 or a crossbar box are in a redundant configuration. Suppose that you connect the power cord of PSU#0 (AC ON), and start an XSCF without connecting the power cord of PSU#1 (AC OFF). Once the READY LED of the XSCF is lit, the input power connection of PSU#1 is not recognized even if you connect the disconnected power cord of PSU#1 afterward. In this case, the Power_Status of PSU#1 is displayed as "Input fail; AC: -;" by executing the showhardconf(8) command.                    |
| <b>Workaround</b>  | When connecting the power cords of the PSUs, be sure to connect the power cords of all the PSUs at the same time. For SPARC M10-1, connect the cables within 5 minutes when the startup mode of the XSCF startup mode function is set to normal, or within 2 and a half minutes in fast mode.<br>[How to restore]<br>Perform either of the following operations:<br>- Switch the mode switch on the operation panel to Locked or Service.<br>- Select PSU#1 by executing the replacefru(8) command, and perform a pseudo replacement of the PSU. |
| <b>RTI No.</b>     | <b>RTIF2-150331-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | If you update the firmware after importing a firmware file for a PCI expansion unit to the system by using XSCF Web, an "Operation was not successful." error is detected and the update may fail.                                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | There is no effective workaround.<br>Use the getflashimage(8) command to import a firmware file for a PCI expansion unit to the system. Then, execute the ioadm -c update command to update the firmware.                                                                                                                                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-150331-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | The serial number and part number of a link card connected to a chassis other than the master chassis are not displayed in the output results of the ioadm -v list command.                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | There is no effective workaround.<br>To confirm the serial number and part number of the connected link card, directly check the link card.                                                                                                                                                                                                                                                                                                                                                                                                      |

Table 3-24 Problems Resolved in XCP 2260 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                      |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150331-003</b>                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | The CPU utilization (system time: sys) of a CPU on the domain may reach 100% during while the XSCF is resetting.                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | There is no effective workaround.<br>No corrective action is required since CPU utilization returns to normal after the XSCF resetting is completed.                                                                                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-150313-001</b>                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | After updating the firmware with the flashupdate(8) command while the physical partition is powered on, if dynamic reconfiguration of PPARs is performed without first rebooting the PPAR in order to update the CMU firmware, the following error log may be registered:<br>Warning: /BB#x/CMUL:SCF:POST/OBP/HV data write error                                                                    |
| <b>Workaround</b>  | When updating the firmware with the flashupdate(8) command while the physical partition is powered on, reboot the PPAR before performing dynamic reconfiguration of PPARs. Moreover, the error message when the problem occurs, may be ignored as it has no effect on the proper functioning of the system.                                                                                          |
| <b>RTI No.</b>     | <b>RTIF2-150305-001</b>                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | If an Oracle Solaris kernel zone has been created or started on any of the following domains, the guest domain or kernel zone enters the panic state:<br>- Live-migrated guest domain<br>- Guest domain in a physical partition (PPAR) from which a system board was deleted by dynamic reconfiguration (DR) of the PPAR<br>The same phenomenon also occurs when you resume a suspended kernel zone. |
| <b>Workaround</b>  | - Restart the guest domain before creating or starting the kernel zone.<br>- If an Oracle Solaris kernel zone has been suspended, do not resume the kernel zone but instead start it with the -R option specified.<br>[Example]<br>guest# <b>zoneadm -z kz000 boot -R</b><br>[How to restore]<br>Restart the PPAR that has the guest domain and the PPAR where the kernel zone has been resumed.     |
| <b>RTI No.</b>     | <b>RTIF2-141031-002</b>                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | The self-authenticated certificate used by the HTTPS service will expire in 1 year, not 10 years.                                                                                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | You can access XSCF Web even though the certificate has expired. Use the same certificate, create a certificate, or import a certificate with the sethttps(8) command.                                                                                                                                                                                                                               |

Table 3-24 Problems Resolved in XCP 2260 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140616-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | When you execute the <code>switchscf(8)</code> command, XSCF master/standby switching may take time. As a result, the following error may occur:<br>Master switch synchronization timeout.                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | There is no effective workaround.<br>If the current master XSCF differs from what you intended, re-execute the <code>switchscf(8)</code> command.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>RTI No.</b>     | <b>RTIF2-140407-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | Suppose one version from XCP 2210 to XCP 2250 is applied to a SPARC M10-4S. On that SPARC M10-4S, the <code>dumpconfig(8)</code> and <code>restoreconfig(8)</code> commands cannot save/restore the PPAR DR feature setting values that were configured with the <code>setpparmode -p ppar_id -m ppar_dr</code> command.                                                                                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | There is no effective workaround.<br>Update the firmware to XCP 2260 or later. Save and restore the PPAR DR feature setting values in XCP 2260 or later.<br>[How to restore]<br>Perform the following procedure.<br>1. Execute the <code>showpparmode(8)</code> command to check whether the PPAR DR feature is enabled or disabled.<br>2. Execute the <code>setpparmode(8)</code> command to enable/disable the PPAR DR feature.<br>[Example] Enabling the PPAR DR feature for PPAR#0<br><code>XSCF&gt; setpparmode -p 0 -m ppar_dr=on</code><br>[Example] Disabling the PPAR DR feature for PPAR#0<br><code>XSCF&gt; setpparmode -p 0 -m ppar_dr=off</code> |
| <b>RTI No.</b>     | <b>RTIF2-131213-010</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | When the firmware of the PCI expansion unit is updated with the <code>ioxadm -c update</code> command, a "process down" may occur in the XSCF firmware.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Workaround</b>  | There is no effective workaround.<br>Re-execute the <code>ioxadm -c update</code> command to update the PCI expansion unit firmware again.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>RTI No.</b>     | <b>RTIF2-130702-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | If the following applies, "I/O devices error detected" is detected at the time of PPAR power-on and control domain reboot: In SPARC M10-4/M10-4S, the CPU memory unit upper (CMUU) is added to a configuration that has been operated with only the CPU memory unit lower (CMUL) and false has been set as the IOreconfigure setting of the physical partition (PPAR).                                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | There is no workaround for errors that are detected at power-on after the addition.<br>Making the following setting prevents the error detection that occurs every time the PPAR is powered on or the control domain is rebooted.<br>1. After starting Oracle Solaris, execute the <code>ldm rm-io</code> command to delete the PCIe root complex of the added CMUU from the configuration of the control domain.<br>2. Execute the <code>ldm add-spconfig</code> command to save the constructed logical domain configuration to the XSCF.                                                                                                                   |

## Problems Related to RTIF2-150514-002

If the XCP version at `dumpconfig(8)` command execution is different from the XCP version at `restoreconfig(8)` command execution, a problem listed in [Table 3-25](#) may occur.

**Table 3-25** Combinations of XCP Versions and Related Problems in `dumpconfig(8)` and `restoreconfig(8)` Command Execution

|     | XCP Version Executing <code>dumpconfig(8)</code> Command | XCP Version Executing <code>restoreconfig(8)</code> Command | Problem Occurring |
|-----|----------------------------------------------------------|-------------------------------------------------------------|-------------------|
| (1) | 2092 or earlier                                          | 2210 to 2221                                                | (a)               |
| (2) | 2092 or earlier                                          | 2230 to 2250                                                | (a) or (b)        |
| (3) | 2210 to 2221                                             | 2230 to 2250                                                | (b)               |
| (4) | 2210 to 2221                                             | 2092 or earlier                                             | (c)               |
| (5) | 2230 to 2240                                             | 2092 or earlier                                             | (c)               |
| (6) | 2250                                                     | 2240 or earlier                                             | (c)               |
| (7) | 2260                                                     | 2250 or earlier                                             | (c)               |

- **Problem (a)**

Physical partition fails to start and then the XSCF stops.

- **Problem (b)**

Suppose XSCF setting information saved in XCP 2221 or earlier is used to restore the XSCF when recovery mode, which is supported in Oracle VM Server for SPARC 3.1 or later, is enabled. Since the physical partition starts with logical domain configuration information created with XCP 2221 or earlier, the command may fail to restore the logical domain configuration information, and the domain configuration may remain switched to the factory-default state.

This is because the recovery mode of the logical domain configuration information created with XCP 2221 or earlier is not enabled. Subsequently, even with the logical domain configuration information created again with XCP 2230 or later, the command may fail to restore the logical domain configuration information, and the domain configuration may remain switched to the factory-default state.

- **Problem (c)**

The following error occurs.

[Example]

```
XSCF> restoreconfig -u user https://...
--- Omitted ---
restoreconfig: could not verifying backup file.(...)
```

## RTIF2-150514-002 Workarounds and How to Restore

The RTIF2-150514-002 workarounds are as follows.

- **For (1) to (3) in Table 3-25**  
Perform the following procedure.
  1. **Execute the version(8) command to check the XCP version.**
  2. **Execute the flashupdate(8) command to update the firmware to the XCP version used when the XSCF setting information was previously saved by the dumpconfig(8) command.**
  3. **Execute the restoreconfig(8) command to restore the XSCF setting information.**
  4. **Execute the flashupdate(8) command to update the firmware again to the original XCP version checked in step 1.**
- **For (4) to (7) in Table 3-25**  
There is no effective workaround.

The restore methods for RTIF2-150514-002 are as follows.

- **For (1) to (3) in Table 3-25**  
Perform the following procedure.
  1. **Execute the version(8) command to check the XCP version.**
  2. **Execute the flashupdate(8) command to update the firmware to the XCP version used when the XSCF setting information was previously saved by the dumpconfig(8) command.**
  3. **For a system configuration with multiple SPARC M10-4S units, execute the initbb(8) command to disconnect all slave chassis from the system.**
  4. **Execute the restoredefaults -c factory command to initialize the system to the factory default state.**
  5. **Turn off the input power to all SPARC M10 system chassis or crossbar boxes. Then, turn on the chassis or crossbar boxes again and confirm that the READY LED of the XSCF or XSCF unit is lit.**
  6. **Execute the restoreconfig(8) command to restore the XSCF setting information.**
  7. **Execute the flashupdate(8) command to update the firmware again to the original XCP version checked in step 1.**
- **For (4) to (7) in Table 3-25**  
Perform the following procedure.
  1. **Execute the version(8) command to check the XCP version.**
  2. **Execute the flashupdate(8) command to update the firmware to the XCP version used when the XSCF setting information was previously saved by the dumpconfig(8) command.**
  3. **Execute the restoreconfig(8) command to restore the XSCF setting information.**
  4. **Execute the flashupdate(8) command to update the firmware again to the original XCP version checked in step 1.**

# Problems Resolved in XCP 2250

The following table lists the problems resolved in XCP 2250.

Table 3-26 Problems Resolved in XCP 2250

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150514-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | If you execute the <code>rebootxscf(8)</code> command to reset an XSCF, "SCF panic detected" may be detected and the XSCF may stop.                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Turn off the input power to the system and then turn it on again (AC OFF/ON).                                                                                                                                                                                                                                                                                                                                      |
| <b>RTI No.</b>     | <b>RTIF2-150507-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | If the temperature of a part in a SPARC M10 system chassis or crossbar box rises, a high temperature part warning may in rare cases be registered even when the warning level has not been reached.                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | There is no effective workaround.<br>If a high temperature warning is registered, the system will automatically recover. This problem does not affect the system operation.                                                                                                                                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-150507-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | The "LINKCARD I2C error" error log may inadvertently be registered during firmware update for a PCI expansion unit.                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | There is no effective workaround.<br>Ignore this error log entry. The firmware update for the PCI expansion unit will end normally.                                                                                                                                                                                                                                                                                                                                         |
| <b>RTI No.</b>     | <b>RTIF2-150227-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | If you connect a power cord of one of the power supply units (PSUs) in a redundant configuration right after disconnecting it, the event logs for power failure and power recovery may not be registered. In addition, if you repeat the operation of connecting the power cord of a PSU right after disconnecting it, "Input fail; AC: - ;" may appear in "Power Status" of PSU shown with the <code>showhardconf(8)</code> command even when the power cord is connected. |
| <b>Workaround</b>  | Wait at least 10 seconds after you disconnect a power cord before connecting it again.<br>[How to restore]<br>If "Input fail; AC: - ;" appears in "Power Status" of the PSU shown with the <code>showhardconf(8)</code> command even when the power cord is connected, wait at least 10 seconds after you disconnect the power cord, and then connect it again.                                                                                                             |

Table 3-26 Problems Resolved in XCP 2250 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150226-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | Suppose you set the remote power management group with the <code>setremotepwrmgmt(8)</code> command when the remote power management function (RCIL) is used on a system configured with two or more SPARC M10-4S. If you execute the <code>switchscf</code> command on the master XSCF to switch between master and standby, remote power management may fail in the subsequent power supply operation.                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Execute the <code>rebootxscf(8)</code> command to reset the standby XSCF.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>RTI No.</b>     | <b>RTIF2-150220-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | If the XSCF is reset due to any of the following while the physical partition (PPAR) is active, a break signal may be sent to the control domain of the PPAR.<br><ul style="list-style-type: none"> <li>- <code>rebootxscf(8)</code> command execution</li> <li>- watchdog timeout occurrence due to a firmware factor</li> <li>- Panic occurrence due to a firmware factor</li> <li>- Pressing of RESET switch of XSCF</li> </ul> This problem may occur even when you execute the <code>setppartmode(8)</code> command to set the break signal suppression.                                                                                                |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>RTI No.</b>     | <b>RTIF2-150218-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | If an XSCF-LAN network is configured while an XSCF-LAN cable is not connected to an XSCF-LAN port, the XSCF-LAN may seem to be in the active state of "RUNNING".<br>[Example] If no cable is connected to XSCF-LAN#1<br>XSCF> <b>shownetwork -a</b><br>bb#00-lan#0<br>Link encap:Ethernet HWaddr 00:00:00:12:34:56 inet addr: 192.168.11.10 Bcast: 192.168.11.255<br>Mask:255.255.255.0<br>UP BROADCAST RUNNING MULTICAST MTU:1500 Metric:1<br>(Omitted)<br>bb#00-lan#1<br>Link encap:Ethernet HWaddr 00:00:00:12:34:57 inet addr:192.168.10.10 Bcast: 192.168.10.255<br>Mask:255.255.255.0<br>UP BROADCAST RUNNING MULTICAST MTU:1500 Metric:1<br>(Omitted) |
| <b>Workaround</b>  | This state does not affect the system operation. Use it as is.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |

Table 3-26 Problems Resolved in XCP 2250 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150129-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-1</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | <p>[1] If the input voltage drops, usually a power failure notification is issued and the power of the physical partition is turned off. However, if the input voltage to the SPARC M10-1 drops instantaneously, no power failure notification is issued and the power of the physical partition is turned off.</p> <p>[2] During the physical partition power-off due to the trouble in [1], if you execute the console(8) command to switch to the control domain console where you have entered a character or input a key, the following error log may be detected and the mother board unit (MBU) may need to be replaced.</p> <p>MBC-CPU interface fatal error</p> <p>Check all the following five conditions are satisfied to see if the trouble in [1] occurs.</p> <ul style="list-style-type: none"> <li>- Any of the following error logs is registered. <ul style="list-style-type: none"> <li>- MBU:SCF:failed to read/write interrupt mask register</li> <li>- MBU:SCF:failed to read/write active status register</li> <li>- Voltage out of range error</li> </ul> </li> <li>- Oracle Solaris is not accessible via the network.</li> <li>- No power failure notification is issued. (*1)</li> <li>- The physical partition status display that you can check with the showpparstatus(8) command is not updated and indicates the physical partition is active (the status is other than "Powered Off").</li> <li>- You can access the XSCF, but the execution result of the showlogspower command does not include "SCF Reset" indicating that the XSCF is reset.</li> </ul> <p>*1 If a power failure notification is issued, usually the following log is registered with the showlogsevent command.</p> <p>PSU input power failure(/PSU#0)<br/> PSU input power failure(/PSU#1)</p> <p>In addition, the following error log is detected in many cases when the trouble in [1] occurs:<br/> failed to read/write extended active status register</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>If "MBC-CPU interface fatal error" is detected, replace the MBU.</p> <p>If "MBC-CPU interface fatal error" is not detected, turn off the input power supply and then turn it on again.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |



Table 3-26 Problems Resolved in XCP 2250 (continued)

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|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-141226-001</b>                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | <p>If time synchronization of the master XSCF and the NTP server fails during master/standby XSCF switching, the XSCF time may deviate and the ntpd may not start. If ntpd is not active, a message "NTP is unavailable." is output by executing the showntp -l command.</p> <p>Note that this problem does not occur if the NTP client setting of the XSCF and the NTP server setting are disabled.</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>If ntpd is not active when the NTP client setting of the XSCF or the NTP server setting is enabled, execute the rebootxscf(8) command to reset the XSCF.</p>                                                                                                                                                                         |

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|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-141208-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>Suppose that the audit system is configured so that writing to audit records stops temporarily when the audit trail reaches its full capacity. An XSCF shell or XSCF Web operation at this time may lead to an XSCF reset due to the mistaken detection of one of the following errors: "Hardware access error." or "SCF panic detected."</p> <p>This problem occurs even when the XSCF shell is automatically run regularly to monitor the logical domain status by using PRIMECLUSTER software.</p> <p>You can check the contents of the currently set audit system by using the showaudit(8) command. In the following example, "Policy on full trail" is "suspend," indicating that writing to the audit record has been set to stop temporarily. Also, "Audit space free" is "0," indicating that the audit trail has reached its full capacity.</p> <p>[Example]</p> <pre>XSCF&gt; showaudit all Auditing: enabled Audit space used: 4194304 (bytes) Audit space free: 0 (bytes) Records dropped: 0 Policy on full trail: suspend --- Omitted ---</pre> |
| <b>Workaround</b>  | <p>Execute the setaudit -p count command to configure the audit system to delete new audit records when the audit trail has reached its full capacity. (Default)</p> <p>[How to restore]</p> <p>Perform the following procedure.</p> <ol style="list-style-type: none"> <li>1. Perform login authentication using the default user account.</li> <li>2. Execute the restoredefaults xscf command.</li> </ol> <p>The audit log is erased. You can execute the setaudit command.</p> <ol style="list-style-type: none"> <li>3. Execute the setaudit -p count command to configure the audit system to delete new audit records when the audit trail has reached its full capacity. (Default)</li> </ol>                                                                                                                                                                                                                                                                                                                                                            |

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Table 3-26 Problems Resolved in XCP 2250 (continued)

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|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-141111-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>On the SPARC M10-1/M10-4 or SPARC M10-4S in a single-chassis configuration, the contents of the scheduled operation setting for automatic power-on/power-off is lost and automatic power-on/ power-off becomes disabled. This occurs even when the PSU backplane (PSUBP) or PSU backplane unit (PSUBPU) and the microSD card are not replaced at the same time, as shown in the following procedure.</p> <ol style="list-style-type: none"> <li>1. Turn off the input power (AC OFF) and replace PSUBP or PSUBPU.</li> <li>2. Turn on the input power (AC ON) and start XSCF.</li> <li>3. Turn off the input power (AC OFF) and replace the microSD card on the mother board unit (MBU) or CPU memory unit lower (CMUL).</li> </ol> <p>Note that this problem occurs even if the microSD card is replaced first.</p>                                                                                                                                                                   |
| <b>Workaround</b>  | <p>After the first FRU is replaced, disable the scheduled operation and then enable it again to transfer the contents of the scheduled operation to the replacing FRU. Execute the following procedure.</p> <ol style="list-style-type: none"> <li>1. Replace the first FRU. (step 1 in the Description)</li> <li>2. Disable the scheduled operation.<br/>XSCF&gt; <b>setpowerschedule -a -c control=disable</b></li> <li>3. Enable the scheduled operation again.<br/>XSCF&gt; <b>setpowerschedule -a -c control=enable</b></li> <li>4/ Execute step 2 and the subsequent steps in the Description.</li> </ol> <p>[How to restore]</p> <ul style="list-style-type: none"> <li>- If the dumpconfig(8) command is used to save the XSCF setting information, execute the restoreconfig(8) command and restore the XSCF setting information.</li> <li>- If the XSCF setting information is not saved, execute the setpowerschedule(8) command and reset the scheduled operation.</li> </ul> |

Table 3-26 Problems Resolved in XCP 2250 (continued)

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-141111-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>If any of the following events occur and the XSCF is reset, an incorrect error log may be registered. In addition, the XSCF time after the error occurrence time may be initialized to January 1, 1970.</p> <ul style="list-style-type: none"> <li>- XSCF panic</li> <li>- watchdog timeout</li> <li>- The RESET switch on the rear panel pressed</li> </ul> <p>The following two examples show incorrect entries registered in the error log and also error occurrence times that were initialized.</p> <p>[Example 1]<br/> Date: Jan 01 09:05:40 JST 1970<br/> Code: 80002008-000e010000ff0000ff-018b00010000000000000000<br/> Status: Alarm Occurred: Jan 01 09:04:56.276 JST 1970<br/> FRU: /MBU<br/> Msg: Hardware access error.<br/> Diagnostic Code:<br/> 00000000 00000000 0000<br/> 00000000 00000000 0000<br/> 00000000 00000000 0000<br/> 00000000 00000000 00000000 00000000<br/> 00000000 00000000 0000</p> <p>[Example 2]<br/> Date: Jan 01 09:05:51 JST 1970<br/> Code: 10000000-000e010000ff0000ff-010240020000000000000000<br/> Status: Information Occurred: Jan 01 09:05:35.008 JST 1970<br/> FRU: /MBU<br/> Msg: TWI access error<br/> Diagnostic Code:<br/> 00000000 00000000 0000<br/> 00000000 00000000 0000<br/> 00000000 00000000 0000<br/> 00000000 00000000 00000000 00000000<br/> 00000000 00000000 0000</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/> Do not press the RESET switch unless it is urgent and required.<br/> [How to restore]<br/> After powering off the physical partition (PPAR), turn off and on (AC OFF/ON) the input power to the system. By turning on the power again, the XSCF time is restored automatically.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |

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|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140929-001</b>                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                         |
| <b>Description</b> | "NOTICE: Unable to get TX queue state!" is displayed mistakenly if Oracle Solaris is started in a system configured with Oracle Solaris kernel zones. |
| <b>Workaround</b>  | There is no effective workaround.<br>Ignore this message.                                                                                             |

Table 3-26 Problems Resolved in XCP 2250 (continued)

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|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140929-002</b>                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                 |
| <b>Description</b> | In a system with the SPARC M10-1, SPARC M10-4, or SPARC M10-4S in a single-chassis configuration, the settings of the remote power management function (RCIL) are lost when the fixed microSD card in the motherboard unit (MBU) or CPU memory unit lower (CMUL) is replaced. |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>See the <i>Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 RCIL User Guide</i> , and reconfigure RCIL.                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-140808-002</b>                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                           |
| <b>Description</b> | When the showhardconf(8) command is executed after the input power is turned off and then on, information on the configuration of components installed on the system may not be recognized.                                                                                   |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Execute the rebootxscf -a command to reset all XSCFs.                                                                                                                                                                |

Table 3-26 Problems Resolved in XCP 2250 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140407-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>If [Ctrl] + [t] is pressed in response to the ok prompt that appears after starting the OpenBoot PROM, the following trap related error message is output on the domain console of the logical domain.</p> <pre>{0} ok ERROR: Last Trap: Fast Instruction Access MMU Miss TL: 1 %TL:1 %TT:64 %TPC:1056e6f20 %TnPC:1056e6f24 %TSTATE:4420001600 %CWP:0 %PSTATE:16 AG:0 IE:1 PRIV:1 AM:0 PEF:1 RED:0 MM:0 TLE:0 CLE:0 MG:0 IG:0 %ASI:20 %CCR:44 XCC:nZvc ICC:nZvc %TL:2 %TT:183 %TPC:f0248e68 %TnPC:f0200c80 %TSTATE:14420001400 %CWP:0 %PSTATE:14 AG:0 IE:0 PRIV:1 AM:0 PEF:1 RED:0 MM:0 TLE:0 CLE:0 MG:0 IG:0 %ASI:20 %CCR:44 XCC:nZvc ICC:nZvc --- Omitted --- ?? Called from (f0227228) at f0227240 0 (emit Called from (lf at f020c3c8 (lf Called from lf at f020c404 lf Called from crlf at f020c424 pop-base Called from (f0225fe0) at f0226024 pop-base Called from (f0225fe0) at f0226024 (f0225fe0) Called from (f0248350) at f024838c 7fffffffffff98 8000000fecdaaff8 (f02081dc) Called from (f0248350) at f024837c {0} ok</pre> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>         [How to restore]<br/>         From the ok prompt, execute the reset-all command to restart the OpenBoot PROM.<br/>         {0} ok <b>reset-all</b></p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140403-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>If you connect to the control domain console by executing the console(8) command on the XSCF serial terminal and then execute any of the following while the control domain console outputs a message, the "SCF process down detected" error may occur and the XSCF may be reset.</p> <ul style="list-style-type: none"> <li>- Entering "#" to disconnect the control domain console</li> <li>- Executing the console(8) command by specifying the -f option from another terminal to forcibly connect to the control domain console of the physical partition (PPAR)</li> <li>- Executing the switchscf(8) command to switch the master XSCF</li> <li>- Executing the rebootxscf(8) command to reset the XSCF</li> </ul> |
| <b>Workaround</b>  | <p>Execute the console(8) command after connecting to the XSCF via telnet or ssh, or terminate the console(8) command after powering off the PPAR.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |

Table 3-26 Problems Resolved in XCP 2250 (*continued*)

|                    |                                                                                                                                                                                                                                                               |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140227-004</b>                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                 |
| <b>Description</b> | If you execute the <code>cfgadm</code> command from a logical domain to use PCI hot plug (PHP) to disconnect a link card from the system, power supply to the link card will stop and "LINKCARD I2C error" will be inadvertently registered in the error log. |
| <b>Workaround</b>  | There is no effective workaround.<br>Ignore this error log entry. The system can operate continuously since the supply of power to link card has definitely stopped.                                                                                          |
| <b>RTI No.</b>     | <b>RTIF2-140212-013</b>                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                 |
| <b>Description</b> | When powering on the physical partition (PPAR), the failure of a DIMM might register "CPU internal fatal error" for the following components: motherboard unit (MBU), CPU memory unit lower (CMUL), or CPU memory unit upper (CMUU).                          |
| <b>Workaround</b>  | There is no effective workaround.<br>If after replacing the MBU, CMUL or CMUU in question, the same error is registered again, replace all the DIMMs that are mounted on the MBU, CMUL and the CMUU.                                                          |

## Problems Resolved in XCP 2240

The following table lists the problems resolved in XCP 2240.

Table 3-27 Problems Resolved in XCP 2240

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-141208-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | After a "CPU fatal error" occurs, the time required for a physical partition (PPAR) restart may be 6 to 30 minutes longer than usual. The required restart time varies depending on the number of mounted PCIe cards.<br>If many PCIe cards are mounted, the following error may be mistakenly detected.<br>- In the case of a SPARC M10-4S system consisting of 2BB or more<br>"SRAM data error"<br>- In the case of a SPARC M10-1/M10-4 system<br>"Timeout of Reset Watch. PPARID 0" |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>- If "SRAM data error" has been detected<br>Replace the component at the suspected location indicated by the error log. After that, update the XCP firmware to XCP 2240 or later.<br>- If "Timeout of Reset Watch. PPARID 0" has been detected<br>Update the XCP firmware to XCP 2240 or later.                                                                                                                               |

Table 3-27 Problems Resolved in XCP 2240 (*continued*)

|                    |                                                                                                                                                                                                                                                                |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-141203-001</b>                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                  |
| <b>Description</b> | In some rare cases, "CPU Internal Serious error" might occur.                                                                                                                                                                                                  |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                              |
| <b>RTI No.</b>     | <b>RTIF2-141113-001</b>                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                  |
| <b>Description</b> | Security fixes. (CVE-2014-3566)<br>For details, see the Oracle website relating to the following:<br><i>Critical Patch Updates</i>                                                                                                                             |
| <b>Workaround</b>  | Update the XCP firmware to XCP 2240 or later.                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-141110-001</b>                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                            |
| <b>Description</b> | The ldm command of Oracle VM Server for SPARC may not display correct CPU utilization and the dynamic resource management (DRM) may not work correctly on large domain systems where 150 domains or more exist.                                                |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                              |
| <b>RTI No.</b>     | <b>RTIF2-141106-001</b>                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1</b>                                                                                                                                                                                                                                             |
| <b>Description</b> | Any of the following errors related to abnormal temperature may be erroneously registered.<br>- Temperature out of range error<br>- High temperature at SW<br>In these error logs, the first two bytes of the second field in [Code:] has the value of "0013". |
| <b>Workaround</b>  | There is no effective workaround.<br>Ignore this message because there is no impact on system operation.                                                                                                                                                       |
| <b>RTI No.</b>     | <b>RTIF2-141031-003</b>                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                            |
| <b>Description</b> | After the system board is deleted by dynamic reconfiguration of physical partitions (PPAR DR), I/O devices on I/O domain may not work, or root domain or I/O domain may cause a panic.                                                                         |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>If I/O devices does not cause a panic after physical partitions (PPAR DR), reboot the I/O domain.                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-141031-004</b>                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                  |
| <b>Description</b> | Hypervisor Abort may occur if all of cores are degraded on the primary domain.                                                                                                                                                                                 |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                              |

Table 3-27 Problems Resolved in XCP 2240 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-141020-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>When using the ASR function and an abnormality occurs with the fan unit in the PCI expansion unit or the SPARC M10 system chassis, the following incorrect fault telemetry is sent to ASR Manager.</p> <ul style="list-style-type: none"> <li>- An error in the fan unit of a PCI expansion unit is sent as an error in the fan unit of a SPARC M10 system chassis.</li> <li>- An error in the fan unit of a SPARC M10 system chassis is sent as an error in the fan unit of a PCI expansion unit.</li> </ul> <p>[Example] Error in a fan unit of the SPARC M10 system chassis</p> <ul style="list-style-type: none"> <li>- Incorrect<br/>message ID: device.fan.toofast_pcibox</li> <li>- Correct<br/>message ID: device.fan.tooslow</li> </ul> <p>This problem occurs when the four bytes from the beginning of the third field of [Code:] at execution of the showlogs error command are any of the following.<br/>01911026, 01911027, 01911028, 01911029, 0191102a, 0191102b</p> <p>[Example] Error in fan unit of PCI expansion unit</p> <ul style="list-style-type: none"> <li>- Incorrect<br/>message ID: device.fan.tooslow</li> <li>- Correct<br/>message ID: device.fan.toofast_pcibox</li> </ul> <p>This problem occurs when the four bytes from the beginning of the third field of [Code:] at execution of the showlogs error command are any of the following.<br/>1100002E, 1100002F</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>When the four bytes from the beginning of the third field of [Code:] are in [Example] in [Description] at execution of the showlogs error command, take action by interpreting the message to be correct.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-141016-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>AC input power may be incorrectly determined to have a mix of 100 V and 200 V if any of the power supply units (PSU) in redundant configuration triggers an AC input error, and then power loss/recovery repeats in a short time. Consequently, though system operation may continue when power is recovered, two PSUs are degraded with the "PSU shortage (power off started)" message displayed and the system stops.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>Replace the PSU where the power loss/recovery was detected.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |

Table 3-27 Problems Resolved in XCP 2240 (continued)

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|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140715-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | In the SPARC M10-1, if the power supply unit (PSU) has a redundant configuration and the power cable connected to one of the power supply units is pulled out, a PSU error may be erroneously detected, and the SPARC M10-1 chassis CHECK LED (amber) may go on. At this time, executing the showlogs error command outputs the "Msg: PSU failed" log entry.                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Use either of the following procedures. <ul style="list-style-type: none"> <li>- How to restore 1<br/>Turn off the power to the SPARC M10 chassis connected to the PCI expansion unit. Then disconnect both PCI expansion unit power cables (AC OFF). Then, wait 30 seconds before reconnecting the cables (AC ON).</li> <li>- How to restore 2<br/>Perform pseudo active replacement of the PSU in which the error has been detected (temporarily remove the PSU and then reinstall it). When performing pseudo replacement of the PSU, use the replacefru(8) command. Note that the fieldeng privilege is required to execute the replacefru(8) command.</li> </ul> |

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|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140711-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | After Oracle Solaris has been kept active for a long time, powering off and then powering on a physical partition (PPAR) may cause the Oracle Solaris time to deviate.<br>This phenomenon occurs in one of the following cases: <ul style="list-style-type: none"> <li>- The setting specifying the XSCF as an NTP client is enabled and Oracle Solaris does not use the NTP server.</li> <li>- The setting specifying the XSCF as an NTP client is enabled and Oracle Solaris uses the NTP server. In this situation, the ntpdate command is not executed when Oracle Solaris boots, or the ntpdate command fails to correct the time.</li> </ul> |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |

**Note** - To update the firmware from XCP 2232 or earlier to XCP 2040 or later, in which this problem has been resolved, see "[RTIF2-140711-001 Additional Information](#)."

[How to restore]  
Execute the following procedure to boot Oracle Solaris in single-user mode and set the time.

1. Execute the poweroff(8) command to power off the PPAR.
2. Execute the showpparstatus(8) command to confirm that the status is "Powered off" meaning that the PPAR is disconnected.
3. Set the OpenBoot PROM environment variable auto-boot? to "false."  
XSCF> **setpparparam -p PPAR-ID -s bootscript "setenv auto-boot? false"**
4. Execute the poweron(8) command to power on the PPAR.
5. Execute the console(8) command to connect to the control domain console and display the ok prompt.
6. Boot Oracle Solaris in single-user mode.  
{0} ok **boot -s**
7. Execute the date command to set the Oracle Solaris time.

[Example] Setting of 18:30:00 on June 27, 2014 # date 0627183014.00  
# **date 0627183014.00**

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Table 3-27 Problems Resolved in XCP 2240 (*continued*)

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|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140710-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | After you replace a motherboard or CPU memory unit lower (CMUL), the XCP firmware version should differ from the pre-replacement version. Nevertheless, the message "XCP version of XSCF and Back-Panel mismatched!" that should appear at the time of login to the XSCF is not displayed.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>When replacing a component, execute the following procedure:</p> <ol style="list-style-type: none"><li>1. Before replacement, execute the <code>version(8)</code> command to check the XCP version.</li></ol> <p>[Example]</p> <pre>XSCF&gt; <b>version -c xcp</b><br/>BB#00-XSCF#0 (Master)<br/>XCP0 (Current): 2051<br/>XCP1 (Reserve): 2051</pre> <ol style="list-style-type: none"><li>2. After replacement, execute the <code>version(8)</code> command to check the XCP version again.</li><li>3. If the XCP versions before and after replacement do not match, execute the <code>flashupdate(8)</code> command to update the firmware.</li></ol> <p>[Example] Updating to XCP 2052</p> <pre>XSCF&gt; <b>flashupdate -c update -m xcp -s 2052</b></pre> |

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Table 3-27 Problems Resolved in XCP 2240 (continued)

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| <b>RTI No.</b>     | <b>RTIF2-140616-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | <p>If a problem occurs with the microSD card on the XSCF or the XSCF unit, you may not be able to access data in the restored XSCF. This may cause the output of the error message "An internal error has occurred. Please contact your system administrator." when any of the following commands is executed. After that, the command terminates abnormally.</p> <pre>initbb(8) resetdateoffset(8) setpciboxdio(8) setpowercapping(8) setpparmode(8) setpparparam(8) setservicetag(8)</pre> <p>In addition, the following command execution terminates normally but the setting information is not reflected in the system.</p> <pre>addboard(8) deleteboard(8) setpcl(8) setupfru(8)</pre> <p>This problem also occurs when you perform the setting on XSCF Web.</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>Replace the following part.</p> <ul style="list-style-type: none"> <li>- SPARC M10-1             <ul style="list-style-type: none"> <li>PSU backplane (PSUBP)</li> </ul> </li> <li>- SPARC M10-4/M10-4S             <ul style="list-style-type: none"> <li>PSU backplane unit (PSUBP)</li> </ul> </li> <li>- SPARC M10-4S (with crossbar box)             <ul style="list-style-type: none"> <li>XSCF interface unit (XSCFIFU)</li> </ul> </li> </ul>                                                                                                                                                                                                                                              |
| <b>RTI No.</b>     | <b>RTIF2-140523-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>In the event of an instantaneous power failure lasting 11 ms or more, the CHECK LED on the operation panel will remain on after recovery, and the XSCF may not start. Therefore, even if the system is set to automatically start after recovery, it may not actually start automatically.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>After turning off the input power, wait at least 10 seconds before turning it on again.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |

Table 3-27 Problems Resolved in XCP 2240 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140515-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | <p>In order to configure the XSCF startup mode function, after changing the startup mode with the <code>xscfstartupmode(8)</code> command or setting the operation panel mode switch to "Locked" or "Service," if the <code>rebootxscf(8)</code> command is executed to reset the XSCF, the new startup mode gets enabled. However, under normal circumstances, the configuration of the startup mode is not enabled when XSCF is reset using the <code>rebootxscf(8)</code> command. It is enabled only when the input power of the system is turned off/on (AC OFF/ON). This state can be confirmed with the <code>xscfstartupmode -d</code> command as follows.</p> <p>[Example] When startup mode is set to "fast" after setting the operation panel mode switch to "Locked"</p> <pre>XSCF&gt; xscfstartupmode -d Setting Mode: normal Current Mode: normal XSCF&gt; xscfstartupmode -m fast XSCF&gt; rebootxscf -a XSCF&gt; xscfstartupmode -d Setting Mode: fast[need AC OFF/ON] Current Mode: fast</pre> <p>However, the correct result will be that the "Current Mode" be "normal" like the following:</p> <pre>XSCF&gt; xscfstartupmode -d Setting Mode: fast [need AC OFF/ON] Current Mode: normal</pre> |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-140507-013</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | Even if two default gateways are set up using the <code>setroute(8)</code> command, connection from other networks is possible only on either XSCF-LAN#0 or XSCF-LAN#1.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Workaround</b>  | Out of the XSCF-LAN#0 and XSCF-LAN#1 gateways, make one of their IP addresses available for connection.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>RTI No.</b>     | <b>RTIF2-140314-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | <p>Suppose XCP 2210 is applied to a SPARC M10-1. In that SPARC M10-1, if an internal component failure occurs in a Type B motherboard unit (MBU), it may be not detected, the following may be registered many times in the error log.</p> <ul style="list-style-type: none"> <li>- Voltage problem within MBU <ul style="list-style-type: none"> <li>FRU: /MBU</li> <li>Msg: Critical low voltage error</li> </ul> </li> <li>Or</li> <li>- Fan speed problem <ul style="list-style-type: none"> <li>FRU: /FAN#x,/MBU</li> <li>Msg: FAN speed too low</li> </ul> </li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Replace the MBU.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |

Table 3-27 Problems Resolved in XCP 2240 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                           |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140227-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | When the crossbar boxes are powered on (AC ON) for the first time, "SCF panic detected" or "XSCF hang up is detected" may be registered in the error log when the XSCF starts.                                                                                                                                                                                                                                                            |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Perform the following procedure.<br>1. Confirm that all crossbar boxes and SPARC M10-4S are displayed by the showhardconf(8) command.<br>2. Perform AC OFF/ON on all crossbar boxes and SPARC M10-4S.<br>3. Check the master XSCF with the showbbstatus(8) command. If XBBOX#81 had been switched to master XSCF, use the switchscf(8) command to make XBBOX#80 the master XSCF. |
| <b>RTI No.</b>     | <b>RTIF2-130305-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | If you reconnect the input power cable soon after it is unplugged, the error log of PSUs detect 100 V and 200 V at AC inputs indicating that an error in the configuration of the input power supply may be registered.<br>- XCP 2041 or later<br>Wrong PSU is installed<br>- XCP 2032 or earlier<br>PSUs detect 100 V and 200 V at AC inputs                                                                                             |
| <b>Workaround</b>  | There is no effective workaround.<br>Ignore this error log entry.                                                                                                                                                                                                                                                                                                                                                                         |

## RTIF2-140711-001 Additional Information

This problem (RTIF2-140711-001) has been resolved for XCP 2240 and later versions. However, the Oracle Solaris time may deviate if a physical partition is started up immediately after updating the firmware from XCP 2232 or earlier to XCP 2240 or later.

To synchronize the time, see "[Notes on the Logical Domain Time.](#)"

# Problems Resolved in XCP 2232

The following table lists the problems resolved in XCP 2232.

Table 3-28 Problems Resolved in XCP 2232

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|                    |                                                                                                                                    |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-141031-001</b>                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                      |
| <b>Description</b> | Security fixes. (CVE-2013-4784)<br>For details, see the Oracle website relating to the following:<br><i>Critical Patch Updates</i> |
| <b>Workaround</b>  | Update the XCP firmware to XCP 2232 or later.                                                                                      |

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# Problems Resolved in XCP 2231

The following table lists the problems resolved in XCP 2231.

Table 3-29 Problems Resolved in XCP 2231

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|                    |                                                                                                                |
|--------------------|----------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-161011-001</b>                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                  |
| <b>Description</b> | In logical domains (including the control domain), dynamically adding or deleting a CPU may cause I/O to hang. |
| <b>Workaround</b>  | There is no effective workaround.                                                                              |

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|                    |                                                                                                                                                                                            |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-141003-001</b>                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                              |
| <b>Description</b> | After performing a live migration on a system with XCP 2230, adding a CPU to a logical domain or creating a new logical domain on the source machine may cause the logical domain to hang. |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>After a live migration, power off and on the physical partition (PPAR) that is the source.                                        |

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|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140930-001</b>                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                  |
| <b>Description</b> | Security fixes. (See MOS Doc Id 1934739.1.) (CVE-2014-7169)<br>For details, see the Oracle website relating to the following:<br><i>Critical Patch Updates</i> |
| <b>Workaround</b>  | Update the XCP firmware to XCP 2231 or later.                                                                                                                  |

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# Problems Resolved in XCP 2230

The following table lists the problems resolved in XCP 2230.

Table 3-30 Problems Resolved in XCP 2230

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140910-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | If Oracle Solaris 11.2 and later is running and the system board is deleted by dynamic reconfiguration of physical partitions, Hypervisor may abort.                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-140819-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | When the XSCF is rebooted or the input power is turned on again, the following problem may, in rare cases, occur: ntpd is left inactive and, as a result, synchronization of the XSCF time with the NTP server time fails. To check whether ntpd is inactive, execute the showntp -l command. If the message "NTP is unavailable." appears, ntpd is inactive. This problem does not occur if the setting specifying the XSCF as an NTP client or NTP server is disabled. |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>If the setting specifying the XSCF as an NTP client or NTP server is enabled and ntpd is inactive, execute the rebootxscf(8) command to reboot the XSCF.                                                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-140815-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | If the system board is deleted by the dynamic reconfiguration of physical partitions (DR), the monitoring of Host Watchdog for logical domains may stop.                                                                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | After the system board is deleted, to restart the monitoring of Host Watchdog, reboot one of the logical domains in the physical partition (PPAR).                                                                                                                                                                                                                                                                                                                       |

Table 3-30 Problems Resolved in XCP 2230 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140813-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>When an attempt is made to install Oracle Solaris via vnet, installation menu activation may take one hour or more in the following case: the network speed between the SPARC M10 system chassis and the installation server exceeds 1 Gbps or the system consists of multiple SPARC M10-4S chassis.</p> <p>[Example] Network installation on a SPARC M10-4S via vnet</p> <pre>{0} ok <b>boot net:dhcp - install</b> Boot device: /virtual-devices@100/channel-devices@200/network@0:dhcp File and args: - install &lt;time unavailable&gt; wanboot info: WAN boot messages-&gt;console &lt;time unavailable&gt; wanboot info: configuring /virtual-devices@100/channel-devices@200/ network@0:dhcp &lt;time unavailable&gt; wanboot info: Starting DHCP configuration &lt;time unavailable&gt; wanboot info: DHCP configuration succeeded &lt;time unavailable&gt; wanboot progress: wanbootfs: Read 368 of 368 kB (100%) &lt;time unavailable&gt; wanboot info: wanbootfs: Download complete Wed Aug 13 06:24:51 wanboot progress: miniroot: Read 4864 of 243471 kB (1%) (*1) *1 This step takes time.</pre>                                                                                                                                                                           |
| <b>Workaround</b>  | Install Oracle Solaris from a vdisk or DVD-ROM created using an ISO image.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-140808-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>On a SPARC M10 system connected to an uninterruptible power supply (UPS), the following problem may occur: after recovery from a power failure occurring while Oracle Solaris is inactive or the physical partition (PPAR) is powered off, the PPAR may erroneously reboot even though the PPAR should remain powered off.</p> <p><b>Note</b> - When a power failure occurs while Oracle Solaris is active and a UPS is connected, Oracle Solaris will automatically reboot upon power recovery.</p> <p>This phenomenon may occur when the operation below is followed by a power failure recurrence and then a recovery therefrom.</p> <ul style="list-style-type: none"> <li>- A power failure occurs while Oracle Solaris is active. After Oracle Solaris boots automatically upon power recovery, the shutdown command is executed on Oracle Solaris to stop Oracle Solaris (display the ok prompt).</li> </ul> <p>[Example]</p> <pre># <b>shutdown -y -g0 -i0</b></pre> <ul style="list-style-type: none"> <li>- A power failure occurs while Oracle Solaris is active. After Oracle Solaris boots automatically upon power recovery, the shutdown command is executed on Oracle Solaris to power off the PPAR.</li> </ul> <p>[Example]</p> <pre># <b>shutdown -y -g0 -i5</b></pre> |
| <b>Workaround</b>  | <p>If you want to stop Oracle Solaris, there is no effective workaround. To power off the PPAR, execute the poweroff(8) command on the XSCF.</p> <p>[How to restore]</p> <p>Execute the poweroff(8) command on the XSCF to power off the PPAR.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |

Table 3-30 Problems Resolved in XCP 2230 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140808-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | A PCI Express correctable error may occur if you connect a PCI Express expansion unit (connected by using PCI Express Gen1), which is an IHV product, to a SPARC M10-1.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-140805-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | <ol style="list-style-type: none"> <li>1. If the building block (PSB) is deleted by a dynamic reconfiguration of physical partitions (PPAR DR), a process on the logical domains may produce a core dump due to SIGILL (Illegal Instruction). (SPARC M10-4S only)</li> <li>2. After a live migration of a guest domain, some commands using the libcrypt library (e.g., ssh, pkg, zonename) on the guest domain may produce a core dump due to SIGILL (Illegal Instruction) if the guest domain is Oracle Solaris 11.1 or Oracle Solaris 11.2 with PRIMECLUSTER GD or PRIMECLUSTER GDS applied to the guest domain.</li> </ol>                                                        |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-140731-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | An Event Log ("Host stopped") is not displayed by a showlogs(8) event and showmonitorlog(8) when the primary domain is shutdown.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-140623-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | If you specify "pop" as the authentication method by executing the setsmtpt(8) command, e-mail sending may fail even after you execute the setemailreport(8) command to enable e-mail notification.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | When executing the setsmtpt(8) command to specify an authentication method, specify either "smtp-auth" or "none" instead of "pop".                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>RTI No.</b>     | <b>RTIF2-140623-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | <p>Suppose that the XSCF STANDBY LED on the operation panel blinks immediately after turning on the input power (AC ON). If the power switch on the operation panel is pressed, powering on/off the physical partition (PPAR) becomes disabled. The power to the PPAR is turned on/off when:</p> <ul style="list-style-type: none"> <li>- The power switch on the operation panel is pressed.</li> <li>- The poweron(8) or poweroff(8) command is executed.</li> <li>- PPAR power on/off operation is performed on XSCF Web.</li> <li>- Remote power management using RCIL takes place.</li> <li>- The auto power control system (APCS) issues a power on/off instruction.</li> </ul> |
| <b>Workaround</b>  | To operate the power switch on the operation panel immediately after turning on the input power, wait at least 30 seconds after the STANDBY LED switches from blinking to being constantly on.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |

Table 3-30 Problems Resolved in XCP 2230 (*continued*)

|                    |                                                                                                                                                                                                                                                                          |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140606-002</b>                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                            |
| <b>Description</b> | Even though the "AEV_AUTHENTICATE" audit event is enabled, XSCF Web login failure events are not registered in the audit log.                                                                                                                                            |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-140606-003</b>                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                            |
| <b>Description</b> | When an attempt is made to look at an audit log concerning XSCF Web operation using the <code>viewaudit(8)</code> command, it displays incorrect values. The displayed values of the port number and IP address at the time of XSCF Web access are "0" and "0000".       |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-140606-006</b>                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                            |
| <b>Description</b> | When specifying a system board (PSB) for a logical system board (LSB) on the [PPAR Operation] - [PPAR Configuration] screen of XSCF Web, you need to note the following: A PSB cannot be specified if it has already been assigned to another physical partition (PPAR). |
| <b>Workaround</b>  | For the setting, use the <code>setpcl(8)</code> command in the XSCF shell.                                                                                                                                                                                               |

Table 3-30 Problems Resolved in XCP 2230 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140606-007</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | <p>Suppose that the system is made up of multiple physical partitions (PPARs) consisting of multiple SPARC M10-4S chassis. If you turn on the power to multiple PPARs at the same time, for the first time after turning on the input power or rebooting the XSCF, the following event may occur: A PPAR ID error is registered, the self-diagnosis test (POST) on some PPARs is not completed, and these PPARs fail to start.</p> <p>You can confirm this event by executing the showpparstatus command.</p> <p>[Example] Only PPAR #4 is indicated with "Initialization Phase" and fails to complete POST processing.</p> <pre>XSCF&gt; <b>poweron -a</b> XSCF&gt; <b>showpparstatus -a</b> PPAR-ID PPAR Status 00 Initialization Complete 01 - 02 - 03 - 04 Initialization Phase 05 - 06 Initialization Complete 07 - 08 - 09 - 10 - 11 Initialization Complete 12 - 13 - 14 - 15 - XSCF&gt;</pre> |
| <b>Workaround</b>  | <p>Do not use the poweron -a command to turn on the power to multiple PPARs at the same time. Use the poweron -p command to turn on the power to PPARs one at a time.</p> <p>[How to restore]</p> <p>Turn off the power to PPARs by using the following procedure and then turn it on again:</p> <ol style="list-style-type: none"> <li>Execute the poweroff -f command to forcibly turn off the power to the PPARs that have failed to start. <pre>XSCF&gt; <b>poweroff -y -f -p ppar_id</b></pre> </li> <li>Confirm that the status of each PPAR specified in step 1 is "Powered OFF". <pre>XSCF&gt; <b>showpparstatus -p ppar_id</b> --- Omitted --- 04 Powered Off --- Omitted ---</pre> </li> <li>Turn on the power to the PPAR again. <pre>XSCF&gt; <b>poweron -y -p ppar_id</b></pre> </li> </ol>                                                                                              |

Table 3-30 Problems Resolved in XCP 2230 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140527-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>On a system that is configured with several SPARC M10-4S, if an XSCF reboot is not performed with the <code>rebootxscf(8)</code> command or if the input power is not turned off for about 10 days in respect to the XSCF in the following status, the "XSCF self-diagnosis warning detection" is registered in the error log:</p> <ul style="list-style-type: none"> <li>- A standby XSCF which did not perform master/standby switchover for about 10 days.</li> <li>- Slave XSCF.</li> </ul> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>Ignore this error log entry.</p>                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>RTI No.</b>     | <b>RTIF2-140212-009</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | <p>When the XSCF is under high load due to a high volume of packets sent to the XSCF network by the L2 loop etc., the false error "RTC read data error" or "Hardware access error" regarding the power unit (PSU) is detected. Moreover, the PSU is marked as failed due to this error.</p>                                                                                                                                                                                                        |
| <b>Workaround</b>  | <p>If the reason for the high load is the L2 loop, eliminate the root cause by reviewing the network connection etc.<br/>[How to restore]<br/>To remove the "Failure" mark from the PSU, set the mode switch on the operation panel to Service mode, turn off the input power, and then turn it on again. After that, restore the mode switch to its original state.</p>                                                                                                                           |

## Problems Resolved in XCP 2221

The following table lists the problems resolved in XCP 2221.

Table 3-31 Problems Resolved in XCP 2221

|                    |                                                                                                                                                                                                                                                                                                                                             |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140617-002</b>                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | <p>On the SPARC M10-4/M10-4S, remote power management using RCIL does not work. Moreover, if the following RCIL-related commands are executed, the "Controller response timeout" error message is output and the command ends abnormally.</p> <pre>setremotepwrmgmt(8) showremotepwrmgmt(8) getremotepwrmgmt(8) clearremotepwrmgmt(8)</pre> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>Handle the supply of power manually for the target SPARC M10 systems and I/O devices.</p>                                                                                                                                                                                                          |

Table 3-31 Problems Resolved in XCP 2221 (*continued*)

|                    |                                                                                                                                    |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140611-001</b>                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                      |
| <b>Description</b> | Security fixes. (CVE-2014-0224)<br>For details, see the Oracle website relating to the following:<br><i>Critical Patch Updates</i> |
| <b>Workaround</b>  | Update the XCP firmware to XCP 2221 or later.                                                                                      |

## Problems Resolved in XCP 2220

The following table lists the problems resolved in XCP 2220.

Table 3-32 Problems Resolved in XCP 2220

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-141106-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | For XCP 2210 or earlier, the error log "PCI Express link up failed" may be erroneously registered, and an on-board I/O device or a PCI slot may be degraded if a physical partition (PPAR) is powered on/off after any of the following error logs related to abnormal temperature is registered. <ul style="list-style-type: none"> <li>- Temperature out of range error</li> <li>- High temperature at SW</li> <li>- Hardware access error.(thermal sensor)</li> </ul> In these error logs, the first two bytes of the second field in [Code:] has the value of "0013". |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Turn off the system input power, then restore the power (AC-OFF/ON).                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>RTI No.</b>     | <b>RTIF2-140804-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | When the fan speed is switched due to a change in the intake air temperature, an error log indicating an abnormal fan speed may be erroneously registered for a fan that is not faulty.                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Execute the replacefru(8) command, and select the fan registered in the error log. Follow the replacement instructions on the maintenance menu, without replacing the fan.                                                                                                                                                                                                                                                                                                                                       |

Table 3-32 Problems Resolved in XCP 2220 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140606-005</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | <p>Suppose that the testsb(8) command running for a system board (PSB) diagnosis detects one of the errors listed below during the period between power-off and power-on of the SPARC M10 Systems chassis or crossbar box chassis. In this case, the command terminates, leaving the power to the chassis on.</p> <ul style="list-style-type: none"> <li>- The DC-DC converter (DDC) in the chassis is faulty.<br/>Error log: Msg: Power-on failure</li> <li>- The circuit that monitors cooling components within the chassis is abnormal.<br/>Error log: Msg: LLC protection circuit test error</li> <li>- A crossbar cable is connected to the crossbar unit (XBU) within the chassis in an invalid combination.<br/>Error log: Msg: XB CABLE configuration error</li> </ul> <p>You can determine the occurrence of this phenomenon from the above error log and the output of the showboards(8) command. If this phenomenon has occurred, the command outputs "y" for [Pwr] and "Failed" for [Test].<br/>[Example] If the power to the SPARC M10-4S chassis is on and the system board diagnosis indicates an error</p> <pre>XSCF&gt; showboards -a PSB  PPAR-ID(LSB) Assignment Pwr Conn Conf Test  Fault ----- 00-0 00(00)      Assigned  y  n  n  Failed Faulted</pre> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>Check the error log and perform maintenance on the component.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>RTI No.</b>     | <b>RTIF2-140605-007</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | <p>For XCP 2041 or later, when an error log for a configuration error in a power supply unit (PSU) is registered, inappropriate message text is registered for this log. Although the message text should indicate the mixing of different AC input voltages (100 V and 200 V), it indicates the existence of an incorrect combination of PSU types.<br/>[Example] Message text (to the right of "Msg:") inappropriately registered when different AC input voltages of 100 V and 200 V are mixed.<br/>Code: 80000000-0001020000ff0000ff-018af0220000000000000000<br/>Status: Alarm Occurred: May 27 11:22:00.0000 JST 2014<br/>FRU: /MBU<br/>Msg: Wrong PSU is installed<br/>The correct message indicating the mixing of different AC input voltages is as follows:<br/>Msg: PSUs detected 100V and 200V at AC inputs</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>Execute the showlogs error command and check the value of the third and fourth bytes in the third field in [Code:]. Depending on this value, take appropriate action by following the correct message indicated below instead of the output message.</p> <ul style="list-style-type: none"> <li>- If the value of the bytes is "f022"<br/>Different AC input voltages of 100 V and 200 V are mixed on the PSU. Check the PSU for AC input connection status.</li> <li>- If the value of the bytes is not "f022"<br/>The combination of PSU types is incorrect. Contact a field engineer.</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |

Table 3-32 Problems Resolved in XCP 2220 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140507-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | In a SPARC M10 system with XCP 2043 or later, a disk is removed from a hardware RAID (RAID1 or RAID1E), and the resulting disk failure is detected. If the subsequent power-off/on, reboot of Oracle Solaris, or execution of the shutdown -i0 command causes a state transition of the Open Boot PROM, the disk is not placed in the original RAID volume but in another RAID volume, even after disk restoration. Moreover, the original RAID volume stays in a degraded state.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Workaround</b>  | <p>Execute any of the following procedures according to the circumstances.</p> <ul style="list-style-type: none"> <li>- If one of the disks composing a RAID volume fails while the system is running, replace the disk while the system is running, without restarting Oracle Solaris.</li> <li>- If one of the disks composing a RAID volume is removed while the system is running, remount the removed disk while the system is running, without restarting Oracle Solaris.</li> <li>- If one of the disks composing a RAID volume is removed while the PPAR is powered off, remount the removed disk while the PPAR is in the powered off state.</li> </ul> <p>[How to restore]<br/>           Perform the following procedure if this symptom occurs:</p> <ol style="list-style-type: none"> <li>1. Remove the newly created RAID volume.</li> <li>2. Set up the target disk as "hot spare".</li> </ol> <p>When the "hot spare" feature is enabled, the disk will be automatically placed in the original RAID volume.</p> <p>Meanwhile, refer to "F.5 Creating a Hot Spare of a Hardware RAID Volume" and "F.6 Deleting a Hot Spare of a Hardware RAID Volume" in the <i>Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide</i> for the procedures of removing a volume or setting up a hot spare.</p> |
| <b>RTI No.</b>     | <b>RTIF2-140507-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | If the system board is deleted by dynamic reconfiguration of physical partitions, Oracle Solaris on the domain may panic, or Hypervisor may abort.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>RTI No.</b>     | <b>RTIF2-140507-015</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | <p>If the showhardconf(8) command is executed in a system configuration with at least 3 BBs or a crossbar box, the wrong information may be output at the FRU-Part-Number of the crossbar cable.</p> <p>[Example]<br/>           For crossbar cable number R0:<br/>           Incorrect:<br/>           CBL#R0 Status:Normal;<br/>             + FRU-Part-Number:@@D00Q@; Ver:0020h;<br/>             + Type:Optic; Length: 2;<br/>           Correct:<br/>           CBL#R0 Status:Normal;<br/>             + FRU-Part-Number: ALLT03FQPE; Ver:0020h;<br/>             + Type:Optic; Length: 2;</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>           There is no effect of this wrong display on the system.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |

Table 3-32 Problems Resolved in XCP 2220 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140507-017</b>                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | If the XSCF is reset using the <code>rebootxscf(8)</code> command, the XSCF may stop due to an XSCF failure.                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Turn off the input power to the system and then turn it on again (AC OFF/ON).                                                                                                                                                                                                                                                                                         |
| <b>RTI No.</b>     | <b>RTIF2-140507-018</b>                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | When the system is in the initialized state, the XSCF may not start even after turning on the input power.                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Replace the CPU memory unit lower (CMUL), the XSCF unit (XSCFU), or the motherboard unit (MBU).                                                                                                                                                                                                                                                                       |
| <b>RTI No.</b>     | <b>RTIF2-140418-001</b>                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | The Dynamic Resource Management(DRM) of Oracle VM Server for SPARC does not work.                                                                                                                                                                                                                                                                                                                                              |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>RTI No.</b>     | <b>RTIF2-140407-002</b>                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | In a system configuration with a connected PCI expansion unit, you may update the XCP firmware or reset the XSCF with the physical partition (PPAR) powered on. In this case, the PCI expansion unit information is not output in the results from an executed <code>ioxadm(8)</code> or <code>showhardconf(8)</code> command. Or, the PCI expansion unit can no longer be controlled with the <code>ioxadm(8)</code> command. |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Perform one of the following operations:<br>- Power off the PPAR and then power it on again.<br>- Use PCI hot plug (PHP) to remove the link card from the system and reinstall it.                                                                                                                                                                                    |

Table 3-32 Problems Resolved in XCP 2220 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140407-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | <p>Suppose you execute the setaudit -a command to set the user audit policy, and then reset the XSCF. In this case, the audit policy of the registered user may be replaced by the audit policy of the user account "default," and the audit policy of the registered user may be erased. This occurs when the user account "default" has no set audit policy.</p> <p>[Example] Setting an audit policy for yyyy user<br/> XSCF&gt; <b>setaudit -a yyyy=disable</b><br/> XSCF&gt; <b>showaudit all</b><br/> --- Omitted ---<br/> User policy:<br/>   yyyy disabled<br/> Events:<br/>   AEV_AUDIT_START enabled<br/> XSCF&gt; <b>rebootxscf -a</b><br/> XSCF&gt; <b>showaudit all</b><br/> --- Omitted ---<br/> User policy:<br/>   default disabled<br/> Events:<br/>   AEV_AUDIT_START enabled</p> |
| <b>Workaround</b>  | <p>If user account "default" is not displayed for [User Policy:] in the output results from the showaudit all command, execute the setaudit -a command to set the audit policy of the user account "default".</p> <p>[Example] Set the audit policy for the user account "default".<br/> XSCF&gt; <b>setaudit -a yyyy=disable</b></p> <p>[How to restore]<br/> Perform the following procedure.</p> <ol style="list-style-type: none"> <li>1. Execute the setaudit -a command to reset the user audit policy that had been used as the audit policy of the user account "default".</li> <li>2. Execute the rebootxscf(8) command to reset the XSCF.</li> <li>3. Execute the showaudit all command to confirm that the set user audit policy is displayed for [User Policy:].</li> </ol>             |
| <b>RTI No.</b>     | <b>RTIF2-140402-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | <p>In the search function for each log in the [XSCF] - [Logs] menu on XSCF Web, if you click the [Search] button with both the [Start] and [End] times set to perform a search, a message indicating an error in the specified period may be output and it may not be possible to perform the search.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | <p>Use the showlogs(8) command in the XSCF shell.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |

Table 3-32 Problems Resolved in XCP 2220 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140314-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | <p>In the case of XCP 2210 applied to a SPARC M10-4S attached to a crossbar box (XBBOX), if the crossbar cable is degraded due to some problem, the following symptom may occur while the system is running.</p> <ul style="list-style-type: none"> <li>- If a failure occurs in the crossbar unit (XBU) of the crossbar box (XBBOX), it may not be detected and the physical partition may hang.</li> </ul> <p>Failure of crossbar cable can be confirmed by any of the following error logs:<br/> FRU: /BB#n/XBU#x/CBL#y,/BB#n/XBU#x,/XBBOX#m/XBU#x<br/> Msg: XB-XB interface fatal bus protocol error<br/> Or<br/> FRU: /BB#n/XBU#x/CBL#y,/BB#n/XBU#x,/XBBOX#m/XBU#x<br/> Msg: XB-XB interface timeout error<br/> Or<br/> FRU: /BB#n/XBU#x/CBL#y,/BB#n/XBU#x,/XBBOX#m/XBU#x<br/> Msg: XB-XB interface link-up error</p> |
| <b>Workaround</b>  | <p>If a problem with any crossbar cable has occurred, stop all PPARs immediately and replace the crossbar cable.<br/> [How to restore]</p> <ol style="list-style-type: none"> <li>1. Stop all PPARs. If any of them cannot be stopped normally, stop it forcibly.</li> <li>2. Replace the degraded crossbar cable.</li> <li>3. Diagnose the hardware with the testsb -a command and check whether there is any XBU failure in the XBBOX.</li> <li>4. If there is any failure in the XBU, replace it.</li> </ol>                                                                                                                                                                                                                                                                                                            |
| <b>RTI No.</b>     | <b>RTIF2-140312-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | Configuration of SNMP fails when it is done from the [SNMP] or the [SNMP Security] menu of XSCF Web.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Workaround</b>  | <p>Do not configure SNMP from XSCF Web. Use the following XSCF shell commands to configure SNMP.</p> <ul style="list-style-type: none"> <li>- setsnmp(8)</li> <li>- setsnmpusm(8)</li> <li>- setsnmpvacm(8)</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-140310-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | When executed to import a server certificate, the setldap -c command will end with an error and importing will fail.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Workaround</b>  | <p>There is no effective workaround when the setldap(8) command is used.<br/> Use the encryption feature of LDAP (LDAP over SSL) with the setldaps(8) command.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |

Table 3-32 Problems Resolved in XCP 2220 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140227-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | After the detection of degradation of a SPARC M10 chassis, if reboot or AC OFF/ON is executed on the physical partition, "PSU shortage" may be mistakenly registered in the error log. Moreover, the PSU is marked as failed due to this error.                                                                                                                                                                                                                                                       |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Execute the following procedures.<br>- If the "Status" of the power supply unit (PSU) has not become "Faulted":<br>1. Eliminate the cause of the degradation of the SPARC M10 chassis.<br>2. If a "Failure" mark is attached to the PSU, perform AC OFF/ON to remove it.<br>- If the "Status" of the power supply unit (PSU) has become "Faulted":<br>1. Replace the PSU.<br>2. Perform AC OFF/ON to remove the "Failure" mark from the PSU. |
| <b>RTI No.</b>     | <b>RTIF2-140227-006</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | If [Ctrl]+[C] is pressed when any of the following commands is running, in some very rare cases, the command may not terminate. Moreover, other running commands executed by other logged-in users also may not terminate.<br>ping(8), setservicetag(8), shownetwork(8), showntp(8), showpacketfilters(8), showservicetag(8), traceroute(8)                                                                                                                                                           |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Perform AC OFF/ON.                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>RTI No.</b>     | <b>RTIF2-140227-007</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | When the SSCP IP address of an unmounted SPARC M10-4S chassis is set up using the setsscp(8) command, the showsscp(8) command displays the "Not installed" message, and the set IP address cannot be confirmed. Therefore, before adding a SPARC M10-4S chassis or crossbar box with the addfru(8) command, you cannot confirm the SSCP IP address of the added chassis with the showsscp(8) command.                                                                                                 |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-140227-008</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | When the lockout feature of user accounts is enabled with the setloginlockout(8) command, if three logins to XSCF Web are performed with the same user account, further logins are disabled for the duration of the set lockout time.                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | Disable the lockout feature of user accounts.                                                                                                                                                                                                                                                                                                                                                                                                                                                         |

Table 3-32 Problems Resolved in XCP 2220 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                   |
|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140221-003</b>                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | If the addboard(8) or deleteboard(8) command is executed using the DR feature, addition or removal of system boards using the DR feature may fail, displaying the following message:<br>"Failed to evacuate board resources".                                                                                                                                                     |
| <b>Workaround</b>  | Re-execute the addboard(8) or deleteboard(8) command.<br>If it fails again, either restart only the control domain or execute the rebootxscf -a command to reset the XSCF. After that, re-execute the addboard(8) or deleteboard(8) command, using the DR feature.                                                                                                                |
| <b>RTI No.</b>     | <b>RTIF2-140212-002</b>                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | If the crossbar cable in the crossbar unit (XBU) of the SPARC M10-4S fails, the name of the failed component is wrongly displayed with the showstatus(8) command. The numbers and letters in the part number are displayed in reverse.<br>[Example]<br>For crossbar cable number 0L<br>Wrong display: CBL#L0 Status: Deconfigured;<br>Right display: CBL#0L Status: Deconfigured; |
| <b>Workaround</b>  | There is no effective workaround.<br>Read the part numbers accordingly.                                                                                                                                                                                                                                                                                                           |
| <b>RTI No.</b>     | <b>RTIF2-140212-004</b>                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | The OID information of scPPAROsMachine in the definition file of the XSCF extension MIB should have a value of "sun4v" but instead contains CMU firmware version information like "02090000".                                                                                                                                                                                     |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-140212-006</b>                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | In the case of SNMPv1 or SNMPv2, if the trap host name, which contains the XSCF host name, is registered by the setsnmp(8) command, the trap host is not displayed by the showsnmp(8) command.<br>[Example of non-displaying with the showsnmp(8) command]<br>XSCF host name: example.com<br>Trap host name: test.example.com                                                     |
| <b>Workaround</b>  | Either specify a trap host name which does not contain the XSCF host name or specify the IP address of the trap host.                                                                                                                                                                                                                                                             |
| <b>RTI No.</b>     | <b>RTIF2-140212-022</b>                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | Suppose you execute the setsnmp addtraphost command with -t inform specified to register a trap host. Then, the UDP address of a trap that is reported to that trap host will become the IP address (physical IP address) assigned to the XSCF-LAN, instead of a takeover IP address (virtual IP address). This symptom occurs when a takeover IP address is set up.              |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                 |

Table 3-32 Problems Resolved in XCP 2220 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130806-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | <p>Suppose that the XSCF is configured as an NTP client and is synchronized with the upper level NTP server time. If you replace one of FRUs listed below for each model and then power on the physical partition (PPAR), the Oracle Solaris time may become January 1, 2001.</p> <p>[SPARC M10-1]</p> <ul style="list-style-type: none"> <li>- Motherboard unit (MBU)</li> <li>- PSU backplane (PSUBP)</li> </ul> <p>[SPARC M10-4S]</p> <ul style="list-style-type: none"> <li>- crossbar backplane unit (XBBPU)</li> <li>- XSCF unit (XSCFU)</li> </ul> <p>You can determine the occurrence of this problem by executing the <code>showdateoffset(8)</code> command. If an extremely large value (400 million seconds or more) is displayed in [Domain Date Offset], this problem has occurred.</p> <p>[Example]</p> <pre>XSCF&gt; showdateoffset -a PPAR-ID Domain Date Offset 00      424915200 sec 01      424915200 sec 02      424915200 sec 03      424915200 sec --- Omitted --- 15      424915200 sec</pre>                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | <p>Use the following procedure to replace a FRU listed in "Description."</p> <ul style="list-style-type: none"> <li>- If the setting specifying the XSCF as an NTP client is disabled <ul style="list-style-type: none"> <li>This problem will not occur. Perform the ordinary replacement procedure.</li> </ul> </li> <li>- If the setting specifying the XSCF as an NTP client is enabled: <ol style="list-style-type: none"> <li>1. Disconnect the LAN cable from the XSCF-LAN port and then perform replacement.</li> <li>2. After the XSCF starts, connect the serial cable to the serial port. Do not connect the LAN cable at this time.</li> <li>3. Disable the setting specifying the XSCF as an NTP client. You need to reboot the XSCF to make your change effective.</li> <li>4. Execute the <code>setdate(8)</code> command to set the XSCF time. After this, the XSCF is rebooted.</li> <li>5. Connect the LAN cable to the XSCF-LAN port.</li> <li>6. Enable the setting specifying the XSCF as an NTP client. You need to reboot the XSCF to make your change effective.</li> </ol> </li> </ul> <p>[How to restore]</p> <p>After replacing the FRU listed in "Description," perform the following:<br/>When you power on the physical partition, the Oracle Solaris time becomes January 1, 2001. Start Oracle Solaris in single-user mode and then set the time.</p> |

## Problems Resolved in XCP 2210

The following table lists the problems resolved in XCP 2210.

Table 3-33 Problems Resolved in XCP 2210

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140605-005</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | <p>If either of the following events occurs, the ttydm process may fail, causing the XSCF to be reset:</p> <ul style="list-style-type: none"> <li>- The console(8) command is executed to connect to the control domain console after one of the following events occurs: A large amount of data is output to the screen of the domain console and the console(8) command terminates when the XSCFs for multiple SPARC M10-4S chassis are reset.</li> <li>- The console(8) command is executed to forcibly connect to the control domain console.</li> </ul> <p>If the ttydm process fails, the following error log is registered:<br/>         [Example]<br/>         XSCF&gt; <b>showlogs error -v</b><br/>         Date: Dec 01 22:08:13 JST 2013<br/>         Code: 20000000-00fcff00b0000000ff-010400010000000000000000<br/>         Status: Notice Occurred: Dec 01 22:08:04.886 JST 2013<br/>         FRU: /FIRMWARE,/XBBOX#80/XSCFU<br/>         Msg: SCF process down detected<br/>         Diagnostic Code:<br/>         00000000 00000000 0000<br/>         50000000 00000000 0000<br/>         00000000 00000000 0000<br/>         74747964 6d2e3239 37302e62 7a320000<br/>         00000000 00000000 0000</p> <p>If the first five bytes on the fourth line in [Diagnostic Code:] are "747479646d", the ttydm process has failed.</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>         [How to restore]</p> <ul style="list-style-type: none"> <li>- For a system that consists of multiple SPARC M10-4S chassis<br/>             By executing the console(8) command after XSCF resetting, which causes master/standby XSCF switching, you can connect to the control domain console.</li> <li>- For the SPARC M10-1, SPARC M10-4, or SPARC M10-4S in a single-chassis configuration<br/>             By executing the console(8) command after XSCF resetting, you can connect to the control domain console.</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>RTI No.</b>     | <b>RTIF2-140507-019</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | <p>If Firefox 26 is used in an environment with XSCF firmware older than XCP 2210, the frame of the pop-up window appears with a thick black line.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Workaround</b>  | <p>Ignore the thick black frame.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>RTI No.</b>     | <b>RTIF2-140507-020</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>If the master XSCF is switched to the standby state just after executing the <code>poweroff -f -p -n</code> command (n: partition number), the following message is registered in the error log, displayed by the showlogs error command.<br/>         Msg: SCF process down detected</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>         This symptom has no effect on the system operation.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |

Table 3-33 Problems Resolved in XCP 2210 (*continued*)

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| <b>RTI No.</b>     | <b>RTIF2-140410-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | An XSCF NTP server function may be used as the platform for a DDoS attack (CVE-2013-5211).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Workaround</b>  | Update the XCP firmware to XCP 2210 or later.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>RTI No.</b>     | <b>RTIF2-140410-007</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | When setting the takeover IP address (virtual IP address) for the XSCF network, the XSCF-LAN IP address (physical IP address) needs to be set for both the master and standby XSCFs. Otherwise, the takeover IP address setting may not be reflected after the setting is applied and the XSCF is reset.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Workaround</b>  | Set the XSCF-LAN IP address (physical IP address) of both master and standby XSCFs and then set the takeover IP address.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>RTI No.</b>     | <b>RTIF2-140407-007</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | After a degraded system board is replaced using the <code>replacefru(8)</code> command, the replacement system board is diagnosed using the <code>replacefru(8)</code> command menu or the <code>testsb(8)</code> command. The diagnosis fails if all of the following conditions are met. <ul style="list-style-type: none"> <li>- Physical partition (PPAR) consists of multiple system boards and the PPAR power is on.</li> <li>- Some system boards in the PPAR are stopped through degradation.</li> </ul>                                                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | Use either of the following procedures to replace a system board and execute diagnosis. <ol style="list-style-type: none"> <li>1. Before replacing the system board, execute the <code>deleteboard(8)</code> command to change the system board state to system board pool (SP).</li> <li>2. After completing system board replacement and diagnosis, execute the <code>addboard(8)</code> command to assign the system board to the PPAR again.</li> </ol> <p>Or</p> <ol style="list-style-type: none"> <li>1. After powering off the PPAR, execute system board replacement and diagnosis.<br/>[How to restore]</li> </ol> <p>After executing the <code>deleteboard(8)</code> command to change the system board state to system board pool (SP) or after powering off the PPAR, redo the system board diagnosis.</p> |
| <b>RTI No.</b>     | <b>RTIF2-140303-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | In some very rare cases, the XSCF does not start after performing AC OFF/ON.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Workaround</b>  | There is no effective workaround.<br>Contact a field engineer if this symptom occurs.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-140221-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | The "LDC Protocol info from PPAR (PPAR ID X : Domain Service Data Send Failed)" message is registered while logical domains are running and the "WARNING: ds@x: ds_handle_recv: invalid message length, received xxx bytes, expected xxx" message is displayed on the control domain console.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | There is no effective workaround.<br>Reset the XSCF with the <code>rebootxscf -a</code> command. After that, execute the <code>svcs</code> command on the control domain to restart the <code>picl</code> , <code>fmd</code> , and <code>ldmd</code> services.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |

Table 3-33 Problems Resolved in XCP 2210 (continued)

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| <b>RTI No.</b>     | <b>RTIF2-140212-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | <p>In a configuration with several SPARC M10-4S chassis (system boards), a system board in a physical partition has been degraded due to failure. Afterward, in an attempt with the <code>replacefru(8)</code> command to replace the system board, the system board is recognized as running, and the replacement attempt fails with the following error message:</p> <pre>[Warning:028] BB#XX cannot be replaced because the PSB in the BB that you indicated is running. Similarly, an attempt to initialize the degraded system board with the <code>initbb(8)</code> command fails with the following error message: Hardware error occurred by initial diagnosis. This symptom occurs if the system board is in the following state, when the <code>showhardconf(8)</code> or the <code>showboards(8)</code> command is executed: [Example] In case PSB#01-0 (BB#01) has been degraded. XSCF&gt; <b>showhardconf</b> ... * BB#01 Status:Deconfigured; ... XSCF&gt; <b>showboards -a</b> PSB  PPAR-ID(LSB) Assignment Pwr  Conn Conf Test   Fault ----- 01-0 00(01)      Assigned   n   n   n   Passed Faulted</pre> |
| <b>Workaround</b>  | <p>After removing the degraded system board from the physical partition with the <code>deleteboard(8)</code> command, execute either the <code>replacefru(8)</code> or the <code>initbb(8)</code> command to replace or remove the system board.</p> <pre>[Example] XSCF&gt; <b>deleteboard -c unassign 01-0</b></pre> <p>After replacing the system board, assign the system board to the physical partition using the <code>addboard(8)</code> command and follow the maintenance procedures to add it in the physical partition.</p> <pre>[Example] XSCF&gt; <b>addboard -c assign -p 0 01-0</b></pre>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-140212-010</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>If the [Ctrl] + [C] keys are pressed partway through the execution of the <code>setsnmp(8)</code>, <code>setsnmpusm(8)</code>, or <code>setsnmpvacm(8)</code> command, SNMP cannot be set up using the above commands after that. In such a case, the error message "configuration being changed by another user" may be displayed.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |

Table 3-33 Problems Resolved in XCP 2210 (*continued*)

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| <b>RTI No.</b>     | <b>RTIF2-140212-015</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | <p>If the version(8) command is executed after replacement of the CPU memory unit lower (CMUL), XSCF unit (XSCFU) or motherboard unit (MBU), the Current bank and Reserve bank are incorrectly displayed.</p> <p>[Example]</p> <ul style="list-style-type: none"> <li>- Before replacing CMUL<br/>XSCF&gt; <b>version -c xcp</b><br/>BB#00-XSCF#0 (Master)<br/>XCP0 (Reserve): 2210<br/>XCP1 (Current): 2210</li> <li>- (Wrong) After replacing CMUL<br/>XSCF&gt; <b>version -c xcp</b><br/>BB#00-XSCF#0 (Master)<br/>XCP0 (Reserve): 2210<br/>XCP1 (Current): 2210</li> <li>- (Right) After replacing CMUL<br/>XSCF&gt; <b>version -c xcp</b><br/>BB#00-XSCF#0 (Master)<br/>XCP0 (Current): 2210<br/>XCP1 (Reserve): 2210</li> </ul> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>It will not affect the system as it is only a problem with the command output.</p> <p>[How to restore]</p> <p>Re-execute the flashupdate(8) command.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-140212-017</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | <p>After the SPARC M10-4S or a crossbar box (XBBOX) is removed with the initbb(8) or replacefru(8) command, the XSCF network (host and domain names, SSCP, IP address, routing, name server) is set up and the applynetwork(8) command is executed. Then, if the "An internal error has occurred. Please contact your system administrator" error message is output, the XSCF network parameters do not reflect the above settings.</p>                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>After re-connecting or replacing the removed SPARC M10-4S or XBBOX, set up the XSCF network again.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>RTI No.</b>     | <b>RTIF2-140212-018</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | <p>In the REMCS customer information input screen, if any of the following two-byte characters are input in the two-byte input field, input error message may be output.</p> <ul style="list-style-type: none"> <li>- The leading character corresponds to A1xx of the EUC code.</li> <li>- The trailing character corresponds to xxA1 of the EUC code.</li> </ul>                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | <p>Do not input the two-byte characters that cause this error at the beginning or end of a character string.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |

Table 3-33 Problems Resolved in XCP 2210 (*continued*)

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| <b>RTI No.</b>     | <b>RTIF2-140212-019</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | When turning on the input power, the voltage reading of the power supply unit (PSU) fails and the PSU error "Hardware access error" may be mistakenly detected and the PSU is degraded.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Ignore this error log entry. To release the PSU from the degraded state, turn off the input power and turn it on again.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>RTI No.</b>     | <b>RTIF2-140212-020</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | When hot-swapping a fan, if the fan is installed right after removing it, hot-swapping may fail.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Workaround</b>  | When replacing a fan, after removing it, wait at least 10 seconds before installing one.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>RTI No.</b>     | <b>RTIF2-140212-023</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | If CPU failures like the following occur on a SPARC M10-4/M10-4S where a CPU memory unit lower (CMUL) and a CPU memory unit upper (CMUU) is mounted, when starting a physical partition (PPAR) the "Hypervisor Abort" error occurs and the PPAR cannot be used anymore.<br>- In the SPARC M10-4,<br>if all the CPUs mounted on the CMUL are degraded.<br>- In the SPARC M10-4S,<br>if all the CPUs mounted on the CMUL on all the chassis that configures the PPAR, are degraded.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | There is no effective workaround.<br>Replace the CMUL on which the failed CPUs are mounted.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-140212-024</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | After executing the <code>ldm add-spconfig</code> or <code>ldm set-spconfig</code> command in Oracle Solaris, if the OpenBoot PROM environment variable <code>auto-boot?</code> is changed, the set value does not show up when the <code>XSCF showpparparam(8)</code> command is executed. The value that existed before the <code>ldm add-spconfig</code> or <code>ldm set-spconfig</code> command was executed is displayed.<br>This symptom occurs when the following procedures are done in the following sequence.<br>1. Start the physical partition (PPAR).<br>2. Execute the <code>ldm add-spconfig</code> command from the control domain of the running PPAR to save logical domain configuration information. Or, execute the <code>ldm set-spconfig</code> command from the control domain of the PPAR to specify the name ( <code>config-name</code> ) of the logical domain configuration information that will be used at the next PPAR start time.<br>3. From Oracle Solaris on the control domain, execute <code>"eeprom auto-boot?=xxxx"</code> to change the environment variable of the OpenBoot PROM or, change the OpenBoot PROM environment variable from the OpenBoot PROM itself. |
| <b>Workaround</b>  | There is no effective workaround.<br>However, the value itself has been enabled. Check the value of the OpenBoot PROM environment variable <code>"auto-boot?"</code> , which will be used at the next PPAR start time, not from the XSCF but from Oracle Solaris on the control domain or from the OpenBoot PROM.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |

Table 3-33 Problems Resolved in XCP 2210 (*continued*)

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| <b>RTI No.</b>     | <b>RTIF2-140212-025</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | <p>With the SPARC M10-4/M10-4S, suppose that all the I/Os on the CPU memory unit lower (CMUL) in one of chassis composing a physical partition (PPAR) cease working. Or with the SPARC M10-1, suppose that all the I/Os on the motherboard unit (MBU) cease working. Then, if you power on the PPAR without replacing the failed components, hypervisor startup will be aborted or Oracle VM Server for SPARC will enter the "suspended" state after Oracle Solaris startup. Any of the following has occurred when all I/Os cease working.</p> <ul style="list-style-type: none"> <li>- I/O power supply has failed.</li> <li>- All PCI Express root complex have failed.</li> <li>- All PCI switches have failed.</li> </ul> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>                     [How to restore]<br/>                     In the case of the SPARC M10-4S/M10-4, replace the CMUL on which the I/O has failed.<br/>                     In the case of the SPARC M10-1, replace the MBU on which the I/O has failed.</p>                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>RTI No.</b>     | <b>RTIF2-140122-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | <p>When a PCI expansion unit is connected to a logical system board whose LSB number is equal to or greater than 01 (LSB#01), if the direct I/O feature is enabled for the PCI expansion unit, the PCI expansion unit may not be recognized by the logical domain.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | <p>If the PCI expansion unit is not recognized, reboot the logical domain.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-131218-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | <p>While the Solaris OS is running, if the physical partition is reset due to the output of the "Hypervisor Abort" message on the OS console, the state of Oracle VM Server for SPARC may change to maintenance mode at the next reboot of the physical partition.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>                     [How to restore]<br/>                     Reboot the control domain only.<br/>                     If the state of Oracle VM Server for SPARC does not become "online", execute the <code>rebootxscf -a</code> command to reset all the XSCFs and then check the state of Oracle VM Server for SPARC once again. If the state has not yet become "online", once more reboot the control domain only.</p>                                                                                                                                                                                                                                                         |

Table 3-33 Problems Resolved in XCP 2210 (*continued*)

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| <b>RTI No.</b>     | <b>RTIF2-131213-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>In a system configuration with at least 3 BBs or a crossbar box, when the XSCF master/standby switchover is executed due to some abnormality on the master XSCF, the master XSCF may fail to switch over to the standby XSCF and start up as another master XSCF. Due to this, the system seems to contain two master XSCFs. Normal behavior of a system is not guaranteed when there are two master XSCFs in the system.</p> <p>This state can be confirmed by a lighted MASTER LED at the rear panel of two chassis.</p>           |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>The system can be operated normally when the number of master XSCFs automatically returns to one after a few minutes.</p> <p>If the number of master XSCFs does not return to one even after 15 minutes, press the RESET switch at the rear panel of all existing SPARC M10 systems and crossbar boxes. The system can be operated normally after that.</p>                                                                                                         |
| <b>RTI No.</b>     | <b>RTIF2-131213-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | <p>Suppose the PPAR DR function is enabled/disabled on XSCF Web when PPAR-DR(Current) or PPAR-DR(Next) is selected from the [PPAR Operation] - [PPAR Mode Configuration] menu and [Configure] is clicked. Then, the wrong dialog box appears, and the PPAR DR function cannot be changed to disabled/enabled.</p>                                                                                                                                                                                                                       |
| <b>Workaround</b>  | Execute the XSCF <code>setpparmode(8)</code> command to enable/disable the PPAR DR function.                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>RTI No.</b>     | <b>RTIF2-131213-008</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>In a system configuration with at least 3 BBs or a crossbar box, after XSCF switchover occurs due to some problems on the master XSCF while the <code>addboard -c</code> configure command is running, the <code>deleteboard(8)</code> command executed on the new master XSCF may not respond. This symptom occurs if two master XSCFs exist on a system after a master/standby switchover of XSCFs. (Reference: RTIF2-131213-001)</p> <p>This state can be confirmed by a lighted MASTER LED at the rear panel of two chassis.</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>Execute the <code>deleteboard(8)</code> command when the number of master XSCFs automatically returns to one after a few minutes.</p> <p>If the number of master XSCFs does not return to one even after 15 minutes, press the RESET switch at the rear panel of all existing SPARC M10 systems and crossbar boxes. Execute the <code>deleteboard(8)</code> command after that.</p>                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-131213-009</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>If the <code>addboard(8)</code> or <code>deleteboard(8)</code> command is executed using the DR feature, addition or removal of system boards using the DR feature may fail, displaying the following message: "Failed to evacuate board resources".</p>                                                                                                                                                                                                                                                                             |
| <b>Workaround</b>  | <p>Re-execute the <code>addboard(8)</code> or <code>deleteboard(8)</code> command.</p> <p>If it fails again, either reboot the control domain or reboot the XSCF with the <code>rebootxscf -a</code> command. After that, re-execute the <code>addboard(8)</code> or <code>deleteboard(8)</code> command of the DR feature.</p>                                                                                                                                                                                                         |

Table 3-33 Problems Resolved in XCP 2210 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                       |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131213-021</b>                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | The timeout error log "Timeout detected during unconfiguration of PSB#xx-x." may be registered and an abnormal termination may occur when a system board (PSB) is removed by the deleteboard(8) command of the DR feature. In such a case, the showboards(8) command shows that the status of all the Pwr/Conn/Conf of the system board (PSB) is "y". |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Reboot all the XSCFs by executing the rebootxscf -a command.                                                                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-131212-001</b>                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | If there is a physical partition (PPAR) whose LSB number is 01 or larger, the showlogs error command either does not show the "I/O device error detected" log even if I/O errors occur, or the FRU in the "I/O device error detected" message is mistakenly displayed.                                                                                |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-131126-005</b>                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | The [Physical] or [Logical] tree displayed in the menu frame is not updated even if you click the [Refresh] button of the masthead frame on XSCF Web.                                                                                                                                                                                                 |
| <b>Workaround</b>  | Click the [Physical] or [Logical] tab of the menu frame to redisplay the tree.                                                                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-130801-002</b>                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4</b>                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | If a DIMM failure occurs, a configuration error is erroneously detected and the following event log is registered.<br>SCF:DIMM configuration error on PSB#xx-0                                                                                                                                                                                        |
| <b>Workaround</b>  | There is no effective workaround.<br>Ignore this event log.<br>Using the maintenance procedure for DIMM failures, turn off the input power to the SPARC M10 system chassis in which the failed DIMM is mounted, replace the DIMM, and then turn on the power. These steps will remove the configuration error and failure.                            |

# Problems Resolved in XCP 2092

The following table lists the problems resolved in XCP 2092.

Table 3-34 Problems Resolved in XCP 2092

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140115-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | If a failure occurs in the PCI expansion unit, all the domains that are situated in the physical partition (PPAR) that is connected to the PCI expansion unit are shut down.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-140110-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | In a SPARC M10-4S (with crossbar box) configuration, the following event log, which notifies power off, is not registered.<br>BB#xx was stopped by power failure. Power failure date is yyyy/mm/dd hh:mm:ss                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-140110-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | When the flashupdate(8) command is executed, only occasionally, the XSCF may not start up.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Workaround</b>  | There is no effective workaround.<br>Contact a field engineer if this problem occurs.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>RTI No.</b>     | <b>RTIF2-131213-015</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | In case, all of the three conditions below are met on a physical partition, the crossbar unit (XBU) error "Msg: XB-XB interface fatal error" is detected.<br>[Condition 1] The physical partition consists of several SPARC M10-4S chassis.<br>[Condition 2] The XSCF BB control cables that are connected to the SPARC M10-4S chassis are either defective or have been disconnected. In such a case, the "Msg: BB control cable detected unexpected" error log is registered.<br>[Condition 3] The physical partition was rebooted due to a hardware failure, the poweron(8) or reset(8) command was executed.<br>Moreover, the XSCF had either rebooted the physical partition due to a hardware failure or had performed retrieval of the poweron(8) or reset(8) command when this symptom previously occurred. |
| <b>Workaround</b>  | There is no effective workaround.<br>Do not execute the poweron(8) or the reset(8) command while the XSCF BB control cable is disconnected.<br>[How to restore]<br>If a hardware failure occurred, power off and on the physical partition.<br>If the XSCF is performing retrieval operations, execute the poweroff -f -p ppar-id command to forcibly power off the physical partition. After that power it on.                                                                                                                                                                                                                                                                                                                                                                                                     |

## Problems Resolved in XCP 2091

The following table lists the problems resolved in XCP 2091.

Table 3-35 Problems Resolved in XCP 2091

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|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140117-001</b>                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | When the setsnmp(8), setsnmpusm(8), setsnmpvacm(8) commands or [XSCF] - [Settings] - [Service] - [SNMP], [XSCF] - [Settings] - [Service] - [SNMP Security] on XSCF Web is executed, the "exclusive control file open error" error message is output and all setups regarding SNMP fail. This problem occurs only on XCP 2090. |
| <b>Workaround</b>  | Update firmware either to XCP 2080 or older, or to 2091 or later.                                                                                                                                                                                                                                                             |

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## Problems Resolved in XCP 2090

The following table lists the problems resolved in XCP 2090.

Table 3-36 Problems Resolved in XCP 2090

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|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140617-001</b>                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                 |
| <b>Description</b> | When the showhardconf -u command is executed on a system connected to a PCI expansion unit, the number of PCI cards installed on the PCI expansion unit is erroneously displayed as "11" (the maximum number of cards that can be installed). |
| <b>Workaround</b>  | There is no effective workaround.<br>To check information about the PCI cards installed in the PCI expansion unit, execute the showhardconf(8) command with no options.                                                                       |

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|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140605-003</b>                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | Suppose that an XSCF failure occurs on one SPARC M10-4S chassis in a physical partition (PPAR) and the CHECK LED is on and the READY LED is off on the XSCF. If you attempt to power on this PPAR, the system erroneously judges that an error has occurred on every SPARC M10-4S chassis in this PPAR and you cannot power on the PPAR. |
| <b>Workaround</b>  | There is no effective workaround.<br>Follow the maintenance procedure for XSCF failures to replace the CPU memory unit lower (CMUL) on the faulty SPARC M10-4S chassis.                                                                                                                                                                  |

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Table 3-36 Problems Resolved in XCP 2090 (continued)

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140605-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>When you add or replace one SPARC M10-4S chassis (BB#00 or BB#01) or crossbar box chassis (XBBOX#80 or XBBOX#81), power control schedule settings for automatic power-on/off are not synchronized between the existing and additional/replacement chassis.</p> <p>If master/standby XSCF switching is performed in this state, automatic power-on/off is not performed because the power control schedule settings remain unavailable.</p>                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>Execute the following procedure to synchronize power control schedule settings for automatic power-on/off between the master and standby XSCFs:</p> <ol style="list-style-type: none"> <li>Execute the <code>showpowerschedule -m list</code> command to confirm that the power control schedule has been registered.</li> </ol> <p>If the power control schedule has not been registered, re-register it by using the <code>addpowerschedule(8)</code> and <code>setpowerschedule(8)</code> commands.</p> <ol style="list-style-type: none"> <li>Execute the <code>setpowerschedule -c control</code> command to enable or disable the power control schedule.</li> </ol>                                                                                                          |
| <b>RTI No.</b>     | <b>RTIF2-140410-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | <p>If a CPU error occurs while powering on a physical partition (PPAR), an erroneous error log may be registered. This symptom can be confirmed by using the <code>showlogs error -v</code> command. If the result of the <code>showlogs error -v</code> command is "Msg: SP InternalError Occurred", and the first 4 bytes of the 5th line of the [Diagnostic Code] is "01920212", it corresponds to this symptom.</p> <p>[Example]</p> <pre>XSCF&gt; showlogs error -v Date: Nov 11 18:29:04 JST 2013 Code: 40000000-00fcff0000ff0000ff-0186ffff0000000000000000 Status: Warning Occurred: Nov 11 18:29:04.871 JST 2013 FRU: /FIRMWARE Msg: SP Internal Error Occurred Diagnostic Code: 00000000 00000000 0000 00000000 00000000 0000 00000000 00000000 0000 0007000b 02040002 00000000 00000000 01920212 00620000 0000</pre> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>Execute the <code>showstatus(8)</code> command to check whether a part has failed. A CPU whose [Status] is "Deconfigured" has failed. Follow the maintenance procedure to replace the FRU mounted on the CPU.</p> <p>[Example] When a "Deconfigured" CPU exists</p> <pre>XSCF&gt; showstatus BB#11 Status:Normal; CMUU Status:Normal; * CPU#0 Status:Deconfigured;</pre>                                                                                                                                                                                                                                                                                                                                                                                    |

Table 3-36 Problems Resolved in XCP 2090 (continued)

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140410-006</b>                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                 |
| <b>Description</b> | If you execute the reset xir command immediately after starting Oracle Solaris or the ldmd service, "LDC Protocol info from PPAR (PPAR ID 0 : Domain Service Data Send Failed)" may be registered in the event log and a "process down" and reset may occur on the XSCF.                                      |
| <b>Workaround</b>  | There is no effective workaround.<br>After XSCF is reset, execute the showlogs event command to confirm the event logs. Or, execute the showpparstatus(8) command to confirm that the physical partition (PPAR) was reset. If the physical partition has not been reset, execute the reset xir command again. |
| <b>RTI No.</b>     | <b>RTIF2-140303-001</b>                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                 |
| <b>Description</b> | Response may become slow when using XSCF Web.                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                             |
| <b>RTI No.</b>     | <b>RTIF2-131216-001</b>                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                 |
| <b>Description</b> | The "XCP update is started." message is mistakenly displayed when the flashupdate -c check command is executed.                                                                                                                                                                                               |
| <b>Workaround</b>  | There is no effective workaround.<br>Ignore this message.                                                                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-131213-006</b>                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                 |
| <b>Description</b> | The "LDC Protocol info from PPAR (PPAR ID X : Domain Service Data Send Failed)" event log is registered on a running logical domain and shutdown of that logical domain may not be properly executed.                                                                                                         |
| <b>Workaround</b>  | There is no effective workaround.<br>Either reboot the control domain or execute the rebootxscf -a command to reboot the XSCF.                                                                                                                                                                                |

Table 3-36 Problems Resolved in XCP 2090 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131213-017</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>Any of the following may occur while the XSCF is running.</p> <ul style="list-style-type: none"> <li>- Symptom 1<br/>A "process down" occurs and the XSCF resets.</li> <li>- Symptom 2<br/>The response of commands dealing with acquiring MIB information (get command, etc.) becomes slow.</li> <li>- Symptom 3<br/>Execution of commands like showhardconf(8) takes time. Moreover, when the showhardconf(8) command is executed, the following message may be output and the command is terminated abnormally. "Cannot communicate with the other XSCF. Check the other XSCF' stat"</li> <li>- Symptom 4<br/>The "SCF panic detected" error is produced and the XSCF is reset.</li> <li>- Symptom 5<br/>Login to XSCF Web fails. However, login to the XSCF shell is possible.</li> </ul> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>[How to restore]</p> <ul style="list-style-type: none"> <li>- For symptoms 1, 2, and 4:<br/>The system can be recovered by resetting the XSCF. The system can be used after that.</li> <li>- For symptoms 3 and 5:<br/>Reset the XSCF with the rebootxscf(8) command.</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-131213-018</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>If the XSCF has disabled the NTP client setup, the time difference between the XSCF and the hypervisor of each physical partition (PPAR) is mistakenly updated, and the time of the logical domain may be misaligned after the PPAR is rebooted.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Workaround</b>  | <p>Enable the NTP client setup of the XSCF by executing the setntp -s client -c enable command, and synchronize the XSCF with the upper-level NTP server.<br/>Otherwise, perform the following procedure.</p> <ol style="list-style-type: none"> <li>1. Execute the showdateoffset(8) command before powering on or rebooting the PPAR and confirm the time difference between the XSCF and the hypervisor.</li> <li>2. If the time difference is too large, execute the resetdateoffset(8) command with the PPAR in the stopped state in order to reset the time difference.</li> <li>3. After powering on the PPAR, set the Oracle Solaris time again.</li> </ol>                                                                                                                              |
| <b>RTI No.</b>     | <b>RTIF2-131213-020</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>Due to the retry functionality of the XSCF, if any abnormality occurs in the recoverable backup memory inside the PSU backplane (PSUBP) more than three times, the XSCF regards it incorrectly as a fixed error. That may prevent the XSCF from starting. In such a case, the "REBOOT: COUNTER ERROR" message is displayed.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>[How to restore]<br/>Re-execute power off and on.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |

Table 3-36 Problems Resolved in XCP 2090 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131129-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | <p>"Code" appears in memory information displayed by the showhardconf(8) command. If there is mounted memory whose last eight digits of "Code" begin with "00", execution of any of the following actions may result in incorrectly identified memory. The memory would have a value of 0 for both its "Code" and size.</p> <ul style="list-style-type: none"> <li>- Execute the restoredefaults factory command</li> <li>- Execute the initbb(8) command</li> <li>- Change the mounting point of memory</li> </ul> <p>[Example 1]<br/>XSCF&gt;<b>showhardconf</b><br/>:<br/>MEM#10B Status:Normal;<br/>+ Code:ce8001M393B2<br/>G70QH0-YK0 0000<br/>-00511571;<br/>+ Type:07; Size:16 GB;</p> <p>[Example 2]<br/>XSCF&gt;<b>showhardconf</b><br/>:<br/>* MEM#10B Status:Deconfigured;<br/>+ Code:000000 0000-00000000;<br/>+ Type: ; Size:0 GB;</p>                                      |
| <b>Workaround</b>  | Apply the latest version of the XCP, and then execute power off and on.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-131126-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | <p>Suppose that a system satisfying one of the conditions described in [Conditions] sets the remote power management function. If Oracle Solaris is running and one of the events mentioned in [Event] occurs, an external I/O device may be erroneously powered off.</p> <p>[Condition]</p> <ul style="list-style-type: none"> <li>- Multiple SPARC M10 systems are registered in the remote power management group, and the physical partitions other than those of the SPARC M10 system that resets the XSCF are powered off.</li> </ul> <p>[Event]</p> <ul style="list-style-type: none"> <li>- A firmware update was executed from the flashupdate(8) command and XSCF Web, and the XSCF was rebooted.</li> <li>- The XSCF was reactivated from the rebootxscf(8) command and XSCF Web, and the XSCF was rebooted</li> <li>- The XSCF was rebooted because of a failure.</li> </ul> |
| <b>Workaround</b>  | <p>Apply either of the following countermeasures:</p> <ul style="list-style-type: none"> <li>- Before firmware update or XSCF reboot, execute the setremotepwrmgmt -c disable command to disable the remote power management function. Subsequently, update the firmware or reboot the XSCF. After the XSCF has been activated, execute the setremotepwrmgmt -c enable command to enable the remote power management function.</li> <li>- Disable the remote power management function with the setremotepwrmgmt -c disable command and then manually power on the external I/O device.</li> </ul>                                                                                                                                                                                                                                                                                       |

Table 3-36 Problems Resolved in XCP 2090 (continued)

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|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131126-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | <p>If one of BB#0/BB#01/XBBOX#80/XBBOX#81 is added or replaced, the synchronous processing of the CPU Activation key in the master/standby XSCF will fail. Subsequently, if the master and standby are switched, the CPU Activation key may be lost. This generates the following phenomena.</p> <p>[If the PPAR power is on]<br/>         The PPAR power is powered off because of a CPU Activation violation.</p> <p>[If the PPAR power is off]<br/>         PPAR power-on fails because there is an insufficient number of CPU Activations.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | <p>After addition/replacement, the synchronous processing of the CPU Activation key is performed when the following is executed.</p> <ul style="list-style-type: none"> <li>- If the master/standby XSCF is not switched<br/>             Execute the <code>rebootxscf -a</code> or <code>addcodactivation(8)</code> command from the master XSCF that requested addition/replacement.</li> <li>- If the master/standby XSCF has been switched<br/>             Execute the <code>switchscf(8)</code> command to return the master XSCF to the pre-addition/replacement state. After that, execute the <code>rebootxscf -a</code> or <code>addcodactivation(8)</code> command.</li> </ul> <p><b>Note</b> - Do not perform the following operations before restoring the master XSCF to its original state by executing the <code>switchscf(8)</code> command. If these operations are performed, it may be necessary to register the CPU Activation key again.</p> <ul style="list-style-type: none"> <li>- <code>rebootxscf -a</code></li> <li>- <code>addcodactivation(8)</code></li> <li>- <code>deletecodactivation(8)</code></li> <li>- Input power off/on</li> </ul> |
| <b>RTI No.</b>     | <b>RTIF2-131126-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>When the firmware update of the PCI expansion unit and link card is complete, the firmware version of the PCI expansion unit is erroneously displayed. Specifically, "PCIBOX version" of the event log displays the pre-update version, while "last version" displays the post-update version.</p> <p>[Example] Version displayed after an update from 1120 to 1130</p> <ul style="list-style-type: none"> <li>- Incorrect<br/>             LINKCARD=1, bank=1, PCIBOX version=1120: last version=1130</li> <li>- Correct<br/>             LINKCARD=1, bank=1, PCIBOX version=1130: last version=1120</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | <p>There is no effective workaround. Replace the version number.<br/>         Moreover, update the XCP firmware to XCP 2090 or later.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-131112-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | <p>Suppose that the system uses a crossbar box and contains two or more physical partitions (PPARs) each consisting of 2BB or more. In this system, if a hardware failure that affects all the PPARs occurs, the failed part is degraded such that the PPAR reactivation processing may fail, causing all the PPAR power supplies to remain turned off.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>         The failed parts are degraded.<br/>         Power on the PPAR again.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |

Table 3-36 Problems Resolved in XCP 2090 (continued)

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| <b>RTI No.</b>     | <b>RTIF2-131112-003</b>                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | If the testsb(8), diagxbu(8), or flashupdate -c sync command is executed during the execution of the flashupdate(8) command, the error log "FMEM serious error" is registered, and the testsb(8), diagxbu(8), or flashupdate -c sync command may fail.                                                                                       |
| <b>Workaround</b>  | Do not execute the testsb(8), diagxbu(8), or flashupdate -c sync command while the flashupdate (8) command is being executed.<br>To confirm the completion of the flashupdate(8) command, execute the showlogs event command and then check for the following message.<br>XCP update has been completed (XCP version=xxxx:last version=yyyy) |

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| <b>RTI No.</b>     | <b>RTIF2-131112-008</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | Suppose that the error log "SCF Diagnosis error on System backup memory" is registered, which is related to the PSU backplane (PSUBP) or XSCF interface unit (XSCFIFU). The FRU of the error log registered immediately after that error log may not be correct.<br>In this case, any one of the following messages is displayed to indicate that the FRU PSUBP or XSCFIFU is not correct.<br>Msg: SCF Diagnosis initialize RTC<br>FRU: PSUBP or XSCFIFU<br>Or<br>Msg: SCF Diagnosis error on XXXX<br>FRU: PSUBP or XSCFIFU<br><br>XXXX indicates any of the following FRUs:<br>CPU, L2 cache, SDRAM, RTC, SPI FMEM, NAND |
| <b>Workaround</b>  | There is no effective workaround.<br>Replace the FRU as follows.<br>For SPARC M10-1: FRU: /MBU<br>For SPARC M10-4: FRU: /BB#00/CMUL<br>For SPARC M10-4S: FRU: /BB#xx/CMUL or FRU: /XBBOX#xx/XSCFU                                                                                                                                                                                                                                                                                                                                                                                                                         |

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| <b>RTI No.</b>     | <b>RTIF2-131112-011</b>                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | If the physical partition (PPAR) is powered on and then the ioxadm(8) command is executed before the power-on processing is complete, the error log "SCF process down detected" is registered, and the ioxadm(8) command may fail.                                                                                                                                                                                                                        |
| <b>Workaround</b>  | Do not execute the ioxadm(8) command until the PPAR power-on processing is complete.<br>You can check the progress of the PPAR power-on processing by executing the showpparstatus (8) command. If the "Running" message appears as a result of executing the showpparstatus(8) command, it indicates that the PPAR power-on processing has been completed.<br>Once the PPAR power-on processing has been completed, execute the ioxadm(8) command again. |

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Table 3-36 Problems Resolved in XCP 2090 (continued)

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|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131112-012</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>Suppose that the error log "FAN speed too high" is registered because of an excessive fan rotation count. Subsequently, if an event that changes the fan rotation speed occurs, the fan rotation speed may not be switched to the correct value and instead may be switched to level 5 (full speed).</p> <p>If an event that changes the fan rotation speed occurs, the following event logs are registered. However, the displayed fan rotation speed will not be correct. The actual fan rotation speed level is 5, which is full speed.</p> <ul style="list-style-type: none"> <li>- Low temperature was detected at air inlet. FANs are changed to speed (level-1) mode</li> <li>- Low temperature was detected at air inlet. FANs are changed to speed (level-2) mode</li> <li>- Middle temperature was detected at air inlet. FANs are changed to speed (level-3) mode</li> <li>- High temperature was detected at air inlet. FANs are changed to speed (level-4) mode</li> <li>- Air outlet temperature recovered from warning state</li> </ul> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>Replace the fan of the detected excessive rotation speed error.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-131112-014</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>If the following mail server is being used and REMCS registration or REMCS environment setting is performed, a communication timeout with the mail server is detected, causing processing to fail.</p> <ul style="list-style-type: none"> <li>- Windows ExchangeServer</li> <li>- E-PORT</li> <li>- qmail</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | <p>After changing the mail server to be used to sendmail or postfix, perform REMCS registration or REMCS environment setting. If it is difficult to change the mail server, update the firmware to XCP 2090 or later.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-131112-015</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | <p>If the <code>initbb(8)</code> or <code>replacefru(8)</code> command is executed, an event log stating "Change Master Failed" may be registered.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>Ignore the event log.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-131107-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | <p>If the XSCF BB control cable is faulty or loose or if an XSCF failure occurs, it may take several hours to disconnect or reset the power supply of the physical partition (PPAR) consisting of multiple SPARC M10-4S units.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | <p>There is no effective workaround.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |

Table 3-36 Problems Resolved in XCP 2090 (continued)

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|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131023-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | OID of scfDomainInfoGroup is duplicated and defined in the MIB definition file for XSCF expansion. Therefore, any attempt to install the MIB definition file for XSCF expansion in the server management software (e.g., JP1/Integrated Management manufactured by Hitachi) may fail.                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | <p>Delete the line of text of the duplicated OID from the MIB definition file for XSCF expansion. Then, install the MIB definition file for XSCF expansion again.</p> <p>The line of text to be deleted is the one defining scfDomainInfoGroup containing "::-={scfMIBObjectGroups 14 }".</p> <p>[Example]</p> <p>For the MIB definition file for XSCF expansion (XSCF-SP-MIB_2050.mib), delete the following lines 3558 to 3566.</p> <pre> ----- 3558: scfDomainInfoGroup OBJECT-GROUP 3559: OBJECTS { 3560: scfDomainNumber, scfPPARId, scfDomainName, 3561: scfDomainStatus 3562: } 3563: STATUS current 3564: DESCRIPTION 3565: "A collection of objects providing PPAR information." 3566: ::= { scfMIBObjectGroups 14 } ----- </pre> |
| <b>RTI No.</b>     | <b>RTIF2-130305-009</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>Suppose that 129 or more IP addresses of the trap hosts are registered by the setsnmp addtraphost or setsnmp addv3traphost command. Then, executing the shownsnmp(8) command displays each of the SNMPv1, SNMPv2, SNMPv2-inform, and SNMPv3 trap hosts, up to a maximum of 128 entries.</p> <p>Similarly, suppose that 129 or more entries are registered by the setsnmpusm create, setsnmpvacm creategroup, setsnmpvacm createview, or setsnmpvacm createaccess command. Then, executing the shownsnmpusm or shownsnmpvacm command displays no more than 128 entries for a user, view, group, and access.</p>                                                                                                                          |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>When 129 or more data entries are registered by the setsnmp(8), setsnmpusm(8), or setsnmpvacm(8) command that does not end abnormally, the data entries have been registered successfully.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |

Table 3-36 Problems Resolved in XCP 2090 (*continued*)

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|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130219-003</b>                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>The following error log is registered if you remove an XSCF BB control cable when removing a SPARC M10-4S with the <code>initbb(8)</code> command or replace a SPARC M10-4S or an XSCF unit with the <code>replacefru(8)</code> command.</p> <p>Msg: Board control error (MBC link error)<br/>         Msg: BB control cable detected unexpected<br/>         Msg: XSCF hang-up is detected</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>         Ignore the error log.</p>                                                                                                                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-130219-007</b>                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>When you turn on the input power to a crossbar box or SPARC M10-4S after disconnecting it in a system having a building block configuration, "Board control error (MBC link error)" may be registered in the error log. In addition, a non-existing crossbar box or SPARC M10-4S may be displayed as a faulty unit.</p>                                                                         |
| <b>Workaround</b>  | <p>This error log is registered when you disconnect the input power supply. Ignore this error log entry.</p>                                                                                                                                                                                                                                                                                       |

## Problems Resolved in XCP 2080

The following table lists the problems resolved in XCP 2080.

Table 3-37 Problems Resolved in XCP 2080

|                    |                                                                                                                                                                                                                                                                                                                               |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140507-008</b>                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | <p>The <code>testsb(8)</code> command detects the "STICK Stop Register error (does not stop)" or the "STICK does not stop (CPU)" error message and may fail.</p>                                                                                                                                                              |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>         [How to restore]<br/>         Collect diagnosis information by executing the <code>snapshot(8)</code> command. After the collection is complete, turn off/on the input power (AC OFF/ON) of all the chassis of the SPARC M10 system, and then contact a field engineer.</p> |

Table 3-37 Problems Resolved in XCP 2080 (continued)

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|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131126-006</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>Suppose that the input power is turned off and on, or that the XSCF is reset. If an operation related to CPU Activation is performed for the first time using XSCF Web rather than the XSCF shell, it may fail. The procedure that fails is any of the following.</p> <ol style="list-style-type: none"> <li>1. Perform any of the following on XSCF Web. <ul style="list-style-type: none"> <li>▪ From the [Settings] - [CoD Activation] menu, add the CPU Activation key.</li> <li>▪ From the [Maintenance] - [Configuration Management] menu, save the XSCF setting information.</li> <li>▪ From the [Maintenance] - [Configuration Management] menu, save/restore the CPU Activation key.</li> </ul> </li> <li>2. Execute the <code>deletecodactivation(8)</code> or <code>setcod(8)</code> command on the XSCF shell. Alternatively, delete the CPU Activation key on XSCF Web. Alternatively, change the assignment of the CPU Activation on XSCF Web.</li> </ol> <p>Or</p> <ol style="list-style-type: none"> <li>1. Perform any of the following on XSCF Web. <ul style="list-style-type: none"> <li>▪ From the [Settings] - [CoD Activation] menu, delete the CPU Activation key.</li> <li>▪ From the [Settings] - [CoD Reservation] menu, release the assignment of the CPU Activation.</li> </ul> </li> <li>2. Log in to the XSCF shell or XSCF Web with another user account.</li> <li>3. Execute the <code>deletecodactivation(8)</code> or <code>setcod(8)</code> command on the XSCF shell. Alternatively, delete the CPU Activation key on XSCF Web. Alternatively, change the assignment of the CPU Activation on XSCF Web.</li> </ol> |
| <b>Workaround</b>  | <p>To perform an operation related to the CPU Activation for the first time after the input power is turned off and on or after the XSCF is reset, execute it on the XSCF shell.</p> <p>[How to restore]<br/>Execute the <code>rebootxscf(8)</code> command to reset the XSCF. Then, execute the <code>deletecodactivation(8)</code> or <code>setcod(8)</code> command on the XSCF shell.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>RTI No.</b>     | <b>RTIF2-131112-009</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>If the physical partition (PPAR) is powered on, the event log of "LDC Protocol info from PPAR (PPAR ID 0:Domain Service Data Send Failed)" is output. Moreover, after Oracle Solaris activation, "ldoms/ldmd:default failed: transitioned to maintenance" is output to the OS console. This may cause Oracle VM Server for SPARC to enter maintenance mode.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]<br/>Execute the <code>rebootxscf -a</code> command to reset all the XSCFs, and then reactivate the control domain. If restoration fails, execute the <code>poweroff(8)</code> and <code>poweron(8)</code> commands on the XSCF to power off and then power on the physical partitions (PPARs).</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>RTI No.</b>     | <b>RTIF2-131108-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>The warning message "WARNING: invalid vector intr: number 0x10001, pil 0x11000" may be output to <code>/var/adm/messages</code>.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | <p>Ignore this message.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |

Table 3-37 Problems Resolved in XCP 2080 (*continued*)

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|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131023-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>If the XSCF is reset by the <code>rebootxscf(8)</code> or <code>switchscf(8)</code> command executed while the logical domain is either starting or stopped, the following problems may occur.</p> <ul style="list-style-type: none"> <li>- If the <code>showdomainstatus(8)</code> command is executed, the logical domain state is not displayed properly.</li> <li>- If the <code>showlogs</code> event command is executed, the log does not display notifications about the logical domain state.</li> <li>- An SNMP Trap for notification of the state of the logical domain is not sent.</li> </ul>                                                                                                    |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>To display the state of the logical domain properly, update it by reactivating the logical domain or other means.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-131023-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>Even when an I/O failure is detected in the logical domain, the XSCF may not be notified of an error. For this reason, the I/O failure information in Fault Report to be displayed from the execution of Oracle Solaris <code>fmdump</code> command in logical domains is not displayed, even if the <code>showlogs</code> error command is executed.</p> <p>Moreover, even if a CPU or memory error is detected in the XSCF, no notification may be sent to the logical domain. For this reason, Fault Report does not display the CPU or memory failure information to be displayed from the execution of the <code>showlogs</code> error command, even if the <code>fmdump</code> command is executed.</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>If an I/O failure is detected in the logical domain, locate the failed portion from the display in Fault Report by executing the <code>fmdump</code> command, and perform maintenance on the failed part. If a CPU or memory failure is detected in the XSCF, perform maintenance according to the FRU displayed by executing the <code>showlogs</code> error command.</p>                                                                                                                                                                                                                                                                                           |
| <b>RTI No.</b>     | <b>RTIF2-131001-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>If the SPARC M10-4S or the crossbar box is disconnected with the <code>initbb(8)</code> command, and then all chassis are turned off and then on again with the XSCF BB control cable left connected, the chassis disconnected with the <code>initbb(8)</code> command will become the master chassis, and the XSCF settings will be initialized to the factory defaults.</p>                                                                                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | <p>Be sure to follow the procedure below when disconnecting the SPARC M10-4S and the crossbar box.</p> <ol style="list-style-type: none"> <li>1. Execute the <code>initbb(8)</code> command to stop the target chassis.</li> <li>2. Turn off the stopped chassis first and then disconnect the XSCF BB control cable.</li> </ol>                                                                                                                                                                                                                                                                                                                                                                                 |

Table 3-37 Problems Resolved in XCP 2080 (continued)

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|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130307-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | If any SPARC M10-4S that is specified with the diagxbu(8) command is not implemented, the message "PSB#xx-0 is not installed." showing the PSB No. that represents the unimplemented SPARC M10-4S should appear. However, the PSB No. of the SPARC M10-4S which actually has been implemented may be erroneously output.                                                                                                                                                               |
| <b>Workaround</b>  | There is no effective workaround.<br>Execute the showhardconf(8) command to check the status of the SPARC M10-4S to which the corresponding PSB No. is assigned.<br>The SPARC M10-4S which has not been implemented is not displayed with the showhardconf (8) command.<br>When the corresponding SPARC M10-4S is displayed, read the PSB No. in the message "PSB#xx-0 is not installed." as that of the unimplemented SPARC M10-4S that was specified when this command was executed. |
| <b>RTI No.</b>     | <b>RTIF2-130305-018</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | An internal error may occur when performing "Diagnostic tests" when selecting "BB" with the replacfru(8) command or executing the testsb(8) command. If you check the error log at this time, you can see that "no PSB available in PPAR" was registered in "PPAR#30" when the internal error occurred.                                                                                                                                                                                |
| <b>Workaround</b>  | An error occurred in the applicable SPARC M10-4S and the system board (PSB) is in an unavailable state. Check the error log and replace the SPARC M10-4S.                                                                                                                                                                                                                                                                                                                              |
| <b>RTI No.</b>     | <b>RTIF2-130305-019</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | Diagnosis continues even when an error occurred on the testsb(8) command and timeout occurs after a two-hour wait.                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | Execute the showboards(8) command in another session while executing the testsb(8) command to check the status of PSB to be diagnosed. An operation will fail if "Test" is set to other than "Testing" and "Pwr" is set to "n". In this case, cancel the testsb(8) command by pressing [Ctrl] + [C].                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-121219-011</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | While the poweroff(8) command is in the process of powering off a physical partition (PPAR), if deleteboard -c unassign is executed for the system board that belongs to the PPAR, the PPAR may not be powered off.                                                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | After executing the poweroff(8) command, confirm that the status field of the relevant PPAR shows Powered Off by using the showpctl(8) command. Then, execute the deleteboard(8) command.                                                                                                                                                                                                                                                                                              |

# Problems Resolved in XCP 2070

The following table lists the problems resolved in XCP 2070.

Table 3-38 Problems Resolved in XCP 2070

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|                    |                                                                                                                |
|--------------------|----------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140623-001</b>                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                  |
| <b>Description</b> | Even when the snapshot(8) command is executed, it does not collect log data concerning NTP-related statistics. |
| <b>Workaround</b>  | There is no effective workaround.<br>This problem does not affect system operation.                            |

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|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131213-014</b>                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | If the XSCF time zone is changed by the settimezone(8) command, the time zone of Oracle Solaris on that physical partition booted after the change is shifted (misaligned) by the time difference before and after the XSCF time zone change.<br>[Example]<br>If the time zone before setup was UTC and after setup is JST, the time misalignment of Oracle Solaris will be 9 hours. |
| <b>Workaround</b>  | There is no effective workaround.<br>Boot the Oracle Solaris after executing the resetdateoffset(8) command and set to the right time on the Oracle Solaris.                                                                                                                                                                                                                         |

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|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131112-010</b>                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | If you execute the XSCF commands in the following order, the setting information for the setntp(8) or settelnet(8) command is not applied and may return to the original state.<br>1. Execute any of the sethostname(8), setnameserver(8), setnetwork(8), setroute(8), or setsscp(8) command.<br>2. Execute either the setntp(8) or settelnet(8) command.<br>3. Execute the applynetwork(8) command. |
| <b>Workaround</b>  | After executing any of the sethostname(8), setnameserver(8), setnetwork(8), setroute(8), or setsscp(8) command is executed, do not execute the setntp(8) or settelnet(8) command until the applynetwork(8) command is executed and the settings are applied.                                                                                                                                         |

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Table 3-38 Problems Resolved in XCP 2070 (continued)

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|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131112-016</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | <p>If you use the <code>deleteuser(8)</code> command to delete a user account for which an SSH user public key is registered, it is deleted but the user public key is not deleted. User public keys will continue to increase in number such that it may not be possible to register one for a new user account. Moreover, if a user account with the same name is registered again, the SSH user public key registered previously is set.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | <p>Before deleting a user account with the <code>deleteuser(8)</code> command, execute <code>setssh -c delpubkey -a -u</code> to delete the SSH user public key registered for the user account.</p> <p>[How to restore]</p> <p>Perform the following procedure.</p> <ol style="list-style-type: none"> <li>1. Execute the <code>adduser(8)</code> command to register the deleted user account again.</li> <li>2. Execute the <code>rebootxscf -a</code> command to reset the XSCF, or turn off and on the input power.</li> <li>3. Execute <code>setssh -c delpubkey -a -u</code> to delete the SSH user public key.</li> <li>4. Execute the <code>deleteuser(8)</code> command to delete the user account.</li> </ol>                                                                                                                                                                                                                                                                                                |
| <b>RTI No.</b>     | <b>RTIF2-131108-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | <p>If the "SCF Diagnosis initialize RTC" error occurs, or if the motherboard unit (MBU) is replaced with the SPARC M10-1 and the CPU memory unit lower (CMUL) is replaced with the SPARC M10-4/M10-4S, the following phenomena may occur.</p> <ul style="list-style-type: none"> <li>- Symptom 1<br/>The XSCF time may return to January 1, 2001.</li> <li>- Symptom 2<br/>The time difference between the XSCF and all physical partitions (PPARs) may become a value of 400 million seconds or more. You can check this phenomenon by executing the <code>showdateoffset(8)</code> command, since the time difference between the XSCF and all PPARs is displayed as a value of "400000000 sec" or more.</li> </ul> <pre>XSCF&gt; showdateoffset -a PPAR-ID Domain Date Offset 00 400000100 sec 01 400000100 sec : : 15 400000100 sec</pre> <ul style="list-style-type: none"> <li>- Symptom 3<br/>When you reset the PPAR or power cycle the PPAR, the Oracle Solaris time may return to January 1, 2001.</li> </ul> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>Update to firmware version XCP 2221 or later.</p> <p>[How to restore]</p> <p>See "<a href="#">Restoration After the "SCF Diagnosis initialize RTC" Error (RTIF2-131108-001).</a>"</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |

Table 3-38 Problems Resolved in XCP 2070 (*continued*)

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131004-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | If firmware update is executed when the physical partition (PPAR) is powered on, the "CPU-MBC interface fatal error" error which is related to the motherboard unit (MBU), is mistakenly detected and may be registered in the error log. This mistaken detection may lead to stopping of the logical domains.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | Execute firmware update when the physical partition (PPAR) is powered off.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-131004-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | If, in a system configured with 3 BB or greater, the chassis of the master XSCF and the standby XSCF are turned off and then on again, the system enters a state in which there is no master XSCF. If the master XSCF is stopped while the XSCF DUAL control cable is either faulty or not connected, master/standby XSCF switching is suppressed, so that the standby XSCF is not switched to the master XSCF.                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | There is no effective workaround.<br>Update to firmware version XCP 2070 or later.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>RTI No.</b>     | <b>RTIF2-131004-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | If master/standby XSCF switching occurs while the XSCF DUAL control cable is either faulty or not connected, switching may be performed even though communication between the master and standby is not guaranteed.<br><br>If an XSCF is configured and master/standby XSCF switching is performed while the XSCF DUAL control cable is either faulty or not connected, the information set in the XSCF will be erased.                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | There is no effective workaround.<br>Perform master/standby XSCF switching while the XSCF DUAL control cable is connected normally.<br>Whether the XSCF DUAL control cable is connected normally can be confirmed with the following procedure.<br>1. Execute the showsscp -a command.<br>2. Check that, in the output results obtained in Step 1., "Cannot communicate." is not displayed for the Address for which the SSCP link network ID (network_id) is 2 or 4.<br><br>[Example]If there is no crossbar box, confirm the Address with an SSCP link network ID (network_id) of 2.<br>XSCF> <b>showsscp -a -N 2</b><br>:<br>:<br>Location      Address<br>-----<br>bb#00-if#2    169.254.1.17<br>bb#01-if#2    169.254.1.18<br><br>Similarly, if there is a crossbar box, confirm the Address with an SSCP link network ID (network_id) of 4. |

Table 3-38 Problems Resolved in XCP 2070 (continued)

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130930-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>If, in an environment for which a time zone is set for the XSCF and daylight saving time is introduced, a physical partition (PPAR) is restarted or a PPAR is turned off and then on again, the start time of the logical domain may be advanced or delayed for 3600 seconds or longer. This can be confirmed by executing the <code>showdateoffset(8)</code> command.</p> <p>In the following execution example, the time difference between the PPAR and XSCF is +/-3600 seconds or greater, indicating that this defect has occurred.</p> <p>[Example]</p> <pre>XSCF&gt; showdateoffset -a PPAR-ID Domain Date Offset 00 -7205 sec 01 -7205 sec 02 -7205 sec 03 -7205 sec 04 -7205 sec 05 -7205 sec 06 -7205 sec 07 -7205 sec 08 -7205 sec 09 -7205 sec 10 -7205 sec 11 -7205 sec 12 -7205 sec 13 -7205 sec 14 -7205 sec 15 -7205 sec</pre> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>For every logical domain in the system, make the settings so that they can be synchronized with the NTP server time, and if the start time of a logical domain shifts, correct the time on the NTP.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>RTI No.</b>     | <b>RTIF2-130903-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>In a system consisting of multiple SPARC M10-4S units, it may take longer than usual from the time a physical partition (PPAR) is turned on until the Power-On Self-Test (POST) starts. For example, for a 2BB configuration, POST usually starts after about 10 minutes, but it may take 20 minutes or longer.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>If this defect occurs, execute the <code>rebootxscf -a</code> command to reset all the XSCFs and restore the system.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |

Table 3-38 Problems Resolved in XCP 2070 (*continued*)

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|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130903-006</b>                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | If multiple physical partitions (PPARs) exist in a system consisting of multiple SPARC M10-4S units, and some SPARC M10-4S units are turned off and then on again, an "SRAM Serious Error" may occur, requiring the replacement of the CPU memory unit lower (CMUL). When the state is displayed with the <code>showpparstatus(8)</code> command or the <code>showdomainstatus(8)</code> command, the PPAR state may not be displayed correctly. |
| <b>Workaround</b>  | There is no effective workaround.<br>While a PPAR is operating, do not turn off the SPARC M10-4S. Use the <code>poweroff(8)</code> command, for example, to stop a PPAR before turning it off.                                                                                                                                                                                                                                                   |

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|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130903-007</b>                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                      |
| <b>Description</b> | If the <code>setcod(8)</code> command is executed repeatedly on the physical partition (PPAR) in the PowerOn state, the resources available within the process may be exhausted, and <code>codd</code> may cause a "process down." |
| <b>Workaround</b>  | You can avoid this by executing <code>setcod(8)</code> when PPAR is in the PowerOff state.<br>[How to restore]<br>Restart <code>codd</code> .                                                                                      |

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Table 3-38 Problems Resolved in XCP 2070 (continued)

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|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130903-008</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | <p>If any device is specified with the select command of the OpenBoot PROM first but the unselect-dev command is not executed and subsequently the boot command is used to start Oracle Solaris from a network device, then the following defect occurs.</p> <p>On the console of the logical domain, the "seek failed" and "Can't mount root" messages are displayed, and the starting of Oracle Solaris fails. Then, the "I/O device error detected" message is registered in the error log, and the logical domain is reset. After the logical domain is reset, the device specified with the select command is degraded.</p> <p>After the reset, the logical domain enters either of the following states depending on the setting of the OpenBoot PROM environment variable "auto-boot?".</p> <ul style="list-style-type: none"> <li>- If auto-boot? is true           <ul style="list-style-type: none"> <li>Oracle Solaris is started from the device that is set as the boot-device. If, however, the device specified with the select command, above, is the same as the device that has been set as the boot-device, this device is degraded, so that Oracle Solaris will fail to start, and the ok prompt appears.</li> </ul> </li> <li>- If auto-boot? is false           <ul style="list-style-type: none"> <li>The ok prompt appears, in the same way as in normal operation.</li> </ul> </li> </ul> |
| <b>Workaround</b>  | <p>After specifying a device and executing the select command, be sure to execute the unselect-dev command before executing the boot command.</p> <p>[Example]</p> <pre>{0} ok <b>select /pci@8000/pci@4/pci@0/pci@1/network@0</b> {0} ok <b>unselect-dev</b> {0} ok <b>boot net</b></pre> <p>[How to restore]</p> <ul style="list-style-type: none"> <li>- If, after the defect occurs, the logical domain is in the ok prompt state           <ul style="list-style-type: none"> <li>Execute the following command to reset the logical domain.</li> <li>{0} ok <b>reset-all</b></li> </ul> </li> <li>- If, after the defect occurs, Oracle Solaris has been started in the logical domain           <ul style="list-style-type: none"> <li>Use the shutdown command to first enter the ok prompt state and then set environment variable auto-boot? to false. Then, use the reset-all command to restart the OpenBoot PROM.</li> </ul> </li> </ul> <p>[Example]</p> <pre># <b>shutdown -y -g0 -i0</b> {0} ok <b>setenv auto-boot? false</b> {0} ok <b>reset-all</b></pre> <p>After recovery, any device that was degraded as a result of this defect will be recognized normally. Ignore the message registered in the error log when the defect occurred.</p>                                                                                                                                                  |

Table 3-38 Problems Resolved in XCP 2070 (continued)

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|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130902-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | If the firmware is updated while a logical domain is operating in a system consisting of multiple SPARC M10-4S units, the master XSCF may not switch to a standby XSCF, causing the firmware update to fail.                                                                                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>Recover the system by following the procedure described below.</p> <ol style="list-style-type: none"> <li>1. Log in to either standby XSCF, and then execute the following command.<br/>XSCF&gt; <b>rebootxscf -s</b></li> <li>2. After 10 seconds, log in to the other standby XSCF, and then execute the following command.<br/>XSCF&gt; <b>rebootxscf -a</b></li> <li>3. Wait 20 minutes, log in to the master XSCF, and then execute the flashupdate(8) command again.</li> </ol>                      |
| <b>RTI No.</b>     | <b>RTIF2-130826-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | If you log in to XSCF Web from the master XSCF when the standby XSCF is in either the maintenance or input power off state, a dialog box starting with "Cannot communicate with BB#xxx: ..." is output, indicating a non-breaking communication error.                                                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>The message in the dialog indicates a display defect, so you can operate the system as is. Ignore the dialog related to this communication error.</p>                                                                                                                                                                                                                                                                                                                                                      |
| <b>RTI No.</b>     | <b>RTIF2-130802-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | <p>When you specify USB memory for the getflashimage(8) command, the following message may be output and the execution of the command may fail.</p> <p>Error: Unable to mount USB device.</p>                                                                                                                                                                                                                                                                                                                                                          |
| <b>Workaround</b>  | After disconnecting and then connecting the USB memory, execute the getflashimage(8) command again.                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>RTI No.</b>     | <b>RTIF2-130802-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | <p>When Oracle Solaris is operating, if you change the SNMP setting with the setsnmp(8) command, the following phenomena may occur.</p> <ol style="list-style-type: none"> <li>1. A part of the data such as the XCP version number is not output as a result of the prtptcl -v and prttdiag -v commands.</li> <li>2. For /var/adm/messages of Oracle Solaris, the following warning message is output.<br/>PICL snmpplugin: cannot fetch object value</li> </ol>                                                                                      |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <ul style="list-style-type: none"> <li>- If 1. occurs:<br/>Recover with the following procedure. <ol style="list-style-type: none"> <li>1) End the prttdiag command with [Ctrl] + [C].</li> <li>2) Wait about 30 minutes, and allow an SNMP timeout to occur in the XSCF.</li> <li>3) On the logical domain, execute the svcadm command to restart the picl service.</li> </ol> </li> <li>- If 2. occurs:<br/>The system can be operated continuously because this is a temporary warning message.</li> </ul> |

Table 3-38 Problems Resolved in XCP 2070 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130801-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | Even if you execute the switchscf(8) command, the XSCF may not be switched. At this time, the master XSCF and standby XSCF cannot communicate with each other, and the redundancy of the XSCF is not maintained.                                                                                                                                                                                                                                                                                                                  |
| <b>Workaround</b>  | There is no effective workaround.<br>If the XSCF is not switched even by executing the switchscf(8) command, execute the replacefru(8) command to perform active replacement of the XSCF unit that is in the standby chassis. Also, when you disconnect the XSCF unit, disconnect and then connect the XSCF BB control cable.                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-130716-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | When you update the PCI expansion unit firmware by executing the ioadm(8) command, a "LINKCARD I2C error" error may occur.                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | There is no effective workaround.<br>However, if both of the conditions below can be confirmed, the update of the PCI expansion unit firmware has been completed normally. In this case, ignore the "LINKCARD I2C error" error message, and continue the operation.<br>- The update of the PCI expansion unit firmware by using the ioadm(8) command has been completed normally.<br>- Executing the ioadm -v list command displays the version number of the PCI expansion unit firmware that has been specified for the update. |
| <b>RTI No.</b>     | <b>RTIF2-130711-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | When you perform maintenance of the SPARC M10-4S by executing the replacefru(8) or addfru(8) command, the "FMEM serious error" error log may be registered and the replacefru(8) or addfru(8) command may fail.<br>Also, when you turn on the power to the physical partition (PPAR) during the execution of the flashupdate(8) command, similarly, the "FMEM serious error" error log may be registered and the flashupdate(8) command may fail.                                                                                 |
| <b>Workaround</b>  | For details, see " <a href="#">Response to "FMEM serious error" of the SPARC M10-4S (RTIF2-130711-001).</a> "                                                                                                                                                                                                                                                                                                                                                                                                                     |

Table 3-38 Problems Resolved in XCP 2070 (continued)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130709-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | In the state where the physical partition (PPAR) is powered on, when switching of the master XSCF occurs, it may take time before the standby XSCF switches to the master XSCF. As a result, the following error may occur:<br>Master switch synchronization timeout                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>- If the error occurs during execution of the flashupdate(8) command when the power to the PPAR is on:<br>Turn off the power to the PPAR, and then execute the flashupdate(8) command again.<br>- If the error occurs during execution of the switchscf(8) command when the power to the PPAR is on, or if the error occurs due to an XSCF failure ("process down," etc.) when the power to the PPAR is on:<br>Perform recovery of the SPARC M10-4S chassis for which the "XSCF hang-up is detected" error log has been registered by using either of the following methods.<br>- Execute the replacefru(8) command to replace the CPU memory unit lower (CMUL) or XSCF unit (XSCFU).<br>- Power off and on the CPU memory unit lower (CMUL) or the XSCF unit (XSCFU). |
| <b>RTI No.</b>     | <b>RTIF2-130516-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | In a system configuration for which remote power management with the ETERNUS is set, the ETERNUS is not powered on even if the power is turned on from the power switch located on the operation panel of the SPARC M10 system.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | Turn on the power in one of the following ways:<br>- XSCF command, poweron(8) command<br>- Menu on the page of XSCF Web<br>- Automatic power turning on with schedule settings                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-130228-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | If a physical partition (PPAR) is powered on again after the PPAR is forcefully powered off with the poweroff -f command while starting up Oracle Solaris, "Unable to connect to Domain Service providers" is output to the OS console and Oracle Solaris may not be started.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | Power on the PPAR again with the poweron(8) command after disconnecting the power of the PPAR with the poweroff(8) command. If Oracle Solaris does not start up even after that, reset the XSCF after disconnecting the power of the PPAR and then power on the PPAR again.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |

## Response to "FMEM serious error" of the SPARC M10-4S (RTIF2-130711-001)

- Replacing the SPARC M10-4S  
When replacing the SPARC M10-4S by following the maintenance menu displayed by executing the replacefru(8) command, perform Step 3 and then turn on the input power to the target SPARC M10-4S (BB#x). Then, after waiting for 50

minutes, manually enter "f" in Step 4 to perform the work.

Please execute the following steps:

- 1) Remove (Delete) the BB#x from a system.
- 2) Turn off the breaker of the BB#x.
- 3) After the exchanged device is connected with the system, turn on the breaker of the BB#x.
- 4) Please select[f:finish] :

- Adding SPARC M10-4S

When adding the SPARC M10-4S by following the maintenance menu displayed by executing the addfru(8) command, perform Step 1 and then turn on the input power to the target SPARC M10-4S (BB#x). Then, after waiting for 50 minutes, manually enter "f" in Step 2 to perform the work.

Please execute the following steps:

- 1) After the added device is connected with the system, please turn on the breaker of the BB#x.
- 2) Please select[f:finish] :

- Executing the flashupdate(8) command

Do not power on the physical partition (PPAR) during the execution of the flashupdate(8) command. If you power on the PPAR during the execution of the flashupdate(8) command, power it on again after the completion of the command. Upon the completion of the flashupdate(8) command, execute the showlogs event command to confirm the following message.

```
XCP update has been completed (XCP version=xxxx:last version=yyyy)
```

## Restoration After the "SCF Diagnosis initialize RTC" Error (RTIF2-131108-001)

### [How to restore]

- If phenomenon 1 occurs:

- Case 1

If the Oracle Solaris time has returned to January 1, 2001, execute the setdate(8) command to set the XSCF time again. In this case, the XSCF is rebooted. After that, power cycle the PPAR. After this, the XSCF is reset. After that, power on the PPAR.

- Case 2

If the Oracle Solaris time is other than January 1, 2001, contact a field engineer. In this case, do not execute the resetdateoffset(8) of setdate(8) command on the XSCF.

- Case 3

If the PPAR power is off, power on the PPAR. After that, check the Oracle Solaris time, and perform the above case 1 or case 2.

- If phenomenon 2 occurs:

- Case 1

- If the Oracle Solaris time has returned to January 1, 2001, it is necessary to initialize the time difference between the XSCF time and Hypervisor on all of the PPARs. Stop all the PPARs, and execute the `resetdateoffset -a` command to clear the time difference.

- Case 2

- If the Oracle Solaris time is other than January 1, 2001, contact a field engineer. In this case, do not execute the `resetdateoffset(8)` or `setdate(8)` command on the XSCF.

- Case 3

- If the PPAR power is off, power on the PPAR. After that, check the Oracle Solaris time, and perform the above case 1 or case 2.

- If phenomenon 3 occurs:

- If phenomenon 1 or 2 also occurs, perform its [How to restore] action first.

- Set the Oracle Solaris time again.

## Problems Resolved in XCP 2052

The following table lists the problems resolved in XCP 2052.

Table 3-39 Problems Resolved in XCP 2052

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                   |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131004-004</b>                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>If the standby XSCF is rebooted while the XSCF BB control cable is either faulty or not connected, it is started as the master XSCF, so that there are two master XSCF units in the system.</p> <p>When there are two master XSCF units, system operation cannot be guaranteed. This state can be confirmed from the fact that there are two chassis for which the MASTER LED is lit on their rear panels.</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>Do not reset an XSCF when the XSCF BB control cable between the master and standby is either faulty or not connected.</p> <p>[How to restore]</p> <p>If there are two master XSCF units in the system, turn off all the chassis and then turn them on again.</p>                                                                                                      |

Table 3-39 Problems Resolved in XCP 2052 (*continued*)

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|                    |                                                                                                                                                                                                                                                                                                                                                      |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130903-004</b>                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | If XSCF master/standby switchover takes place when either the XSCF BB control cable or the XSCF DUAL control cable is not connected properly, the settings on the master XSCF side may not be properly carried on to the standby XSCF and thus, it may not operate properly.                                                                         |
| <b>Workaround</b>  | There is no effective workaround.<br>Please make sure that the XSCF BB control cable and the XSCF DUAL control cable is properly connected.<br>If this symptom occurs, check whether each setting by the master XSCF had been stored after confirming that the cables are properly connected. Set up XSCF again when the setting has not been saved. |

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|                    |                                                                                                                                                                                                 |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130827-001</b>                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                   |
| <b>Description</b> | A timeout or connection failure may be detected for the communication party because XSCF packet reception is delayed, delaying the response, due to a defect in the XSCF-LAN hardware settings. |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                               |

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## Problems Resolved in XCP 2051

The following table lists the problems resolved in XCP 2051.

Table 3-40 Problems Resolved in XCP 2051

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|                    |                                                                                                                                                                                                                                                                                                                                                    |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130717-001</b>                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | If an error occurs in the USB-SSD in the XSCF interface unit (XSCFIFU) of the PSU backplane (PSUBP) or crossbar box, the "System backup memory access error" error log may be registered. In this case, a problem such as the inability to execute the poweron(8) command or the inability to collect data with the snapshot(8) command may occur. |
| <b>Workaround</b>  | There is no effective workaround.<br>When this problem occurs, power off the input power supply and then power it on again (AC OFF/ON).                                                                                                                                                                                                            |

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## Problems Resolved in XCP 2050

The following table lists the problems resolved in XCP 2050.

Table 3-41 Problems Resolved in XCP 2050

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131107-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>If the input power to the system is turned on, the XSCF is reset, or the XCP firmware is updated, the "SCF Diagnosis initialize RTC" error is erroneously detected, causing the following phenomena to occur.</p> <ul style="list-style-type: none"> <li>- Symptom 1<br/>The XSCF time may return to January 1, 1970.</li> <li>- Symptom 2<br/>The time difference between the XSCF and all physical partitions (PPARs) may become a value of 1.3 billion seconds or more. You can check this phenomenon by executing the showdateoffset (8) command, since the time difference between the XSCF and all the PPARs is displayed as a value of "1300000000 sec" or more.<br/>XSCF&gt; <b>showdateoffset -a</b><br/>PPAR-ID Domain Date Offset<br/>00 1300000100 sec<br/>01 1300000100 sec<br/>:<br/>:<br/>15 1300000100 sec</li> <li>- Symptom 3<br/>When you reset the PPAR or power cycle the PPAR, the Oracle Solaris time may return to January 1, 1970.</li> </ul> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>Update to firmware version XCP 2050 or later.<br/>[How to restore]<br/>See "<a href="#">Restoration After Erroneous Detection of the "SCF Diagnosis initialize RTC" Error (RTIF2-131107-002).</a>"</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>RTI No.</b>     | <b>RTIF2-131023-005</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>If the [Physical] bar is selected on the XSCF Web menu, PCI (excluding the link card) may appear to be abnormal (⚠).</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>Even if PCI (excluding the link card) is indicated as being abnormal (⚠), ignore it.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>RTI No.</b>     | <b>RTIF2-130903-005</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>In the XSCF e-mail notification function, the following characters cannot be included in the local part or the domain part of the reply mail address which is sent by the setsmtp(8) command.<br/>"! " # " \$ " % " &amp; " ' " * " + " / " = " ? " ^ " _ " ` " { "   " } " ~ "</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | <p>Use reply mail addresses which do not include the characters shown by the description.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |

Table 3-41 Problems Resolved in XCP 2050 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130528-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | You cannot use the PCI hot plug (PHP) function to add a Quad Gigabit Ethernet card (SE1X7GQ2F) to a PCI Express slot of a PCI expansion unit.                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | This has been modified with XCP 2050 and SRU 11.1.6.4.0.<br><br>If XCP and SRU are not applied, you must stop the logical domain to which you want to add the PCI card before adding it.<br>[Precautions]<br>To resolve this problem when you have saved the configuration information of the logical domain and are operating the system with a configuration other than factory-default, you must reconstruct the logical domain configuration after update of the XCP firmware. For details, see " <a href="#">Reconstructing the Logical Domain (RTIF2-130528-001)</a> ." |
| <b>RTI No.</b>     | <b>RTIF2-130516-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | If the XSCF load is high, the following warning message may be output, indicating the power supply unit (PSU) as a suspected location.<br>Insufficient FAN rotation speed<br>PSU voltage out of range error<br>PSU output current too high<br>PSU over temperature warning                                                                                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | There is no workaround.<br>This is a warning message, so you can operate it as it is. Ignore the message.                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-130329-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | In a system with building block configuration, if the following operations are performed during power-on of the physical partition (PPAR), connection to the control domain console may not be possible.<br>1. XSCF for any of the SPARC M10-4S units constituting PPAR is reset.<br>2. Switching of the master XSCF and standby XSCF occurs.<br>3. XSCF of the SPARC M10-4S that was reset in 1. is reset again.                                                                                                                                                             |
| <b>Workaround</b>  | There is no effective workaround.<br>Reset the master XSCF using the switchscf(8) command.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>RTI No.</b>     | <b>RTIF2-130329-006</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | If the XSCF of the SPARC M10-4S belonging to the physical partition (PPAR) in operation is rebooted due to a panic or watchdog timeout while the master XSCF and the standby XSCF are restarting simultaneously, connection to the control domain console may not be possible.                                                                                                                                                                                                                                                                                                |
| <b>Workaround</b>  | There is no effective workaround.<br>Power off the PPAR by using the poweroff -f command, and then power it on again.                                                                                                                                                                                                                                                                                                                                                                                                                                                         |

Table 3-41 Problems Resolved in XCP 2050 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                             |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130319-002</b>                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | Before power-off of the physical partition (PPAR) completes in a SPARC M10-4S, suppose you cycle the input power to the SPARC M10-4S or a crossbar box and power on the PPAR again. Then, the PPAR in operation may be powered off if the master XSCF is switched with the switchscf(8) command, firmware update, or failure.                               |
| <b>Workaround</b>  | If you power off the PPAR before powering off the input power supply of the SPARC M10-4S or crossbar box, check that power-off of the target PPAR and SPARC M10-4S is complete, by using the showboards(8) and showlogs power commands. Then, power off the input power supply of the SPARC M10-4S or crossbar box.                                         |
| <b>RTI No.</b>     | <b>RTIF2-130319-003</b>                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | If the shutdown -i5 command and power-off command compete for power-off of the PPAR when the former is executed in the control domain or the latter is executed from the ok prompt while power-off of the physical partition (PPAR) is performed with the poweroff(8) command, "SCF:PPAR issued power-off request (PPARID X)" may be registered many times. |
| <b>Workaround</b>  | Ignore the registered event log, because the PPAR has been normally powered off.                                                                                                                                                                                                                                                                            |
| <b>RTI No.</b>     | <b>RTIF2-130305-016</b>                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | Timeout may occur if the XCP firmware version of a SPARC M10-4S or crossbar box is updated using the flashupdate -c sync command.                                                                                                                                                                                                                           |
| <b>Workaround</b>  | Execute the flashupdate(8) command by specifying the -f option to re-update the XCP firmware for all SPARC M10-4S or crossbar boxes.                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-130219-002</b>                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | When maintaining SPARC M10-4S with a slave XSCF, if you incorrectly connect the standby XSCF and the slave XSCF with an XSCF connection cable, the maintenance is considered to have finished normally without detecting the error.                                                                                                                         |
| <b>Workaround</b>  | After switching between the master XSCF and the standby XSCF by using the switchscf(8) command, execute the testsb(8) command to the target slave XSCF. The incorrect connection with the XSCF connection cable is detected and an error log is issued.                                                                                                     |

## Reconstructing the Logical Domain (RTIF2-130528-001)

To resolve this problem when you have saved the configuration information of the logical domain and are operating the system with a configuration other than factory-default, you must reconstruct the logical domain configuration after update of the XCP firmware through the following procedure:

1. **Check the current configuration information of the logical domain stored on the XSCF.**

This example assumes that the name of the saved logical domain configuration information is config1.

```

XSCF> showdomainconfig -p 0
20xx-yy-zz hh:mm:ss
PPAR-ID :0
Booting config
(Current) :config1
(Next) :config1

Index :1
config_name :factory-default
domains :1
date_created:-

Index :2
config_name :config1
domains :2
date_created:'20xx-yy-zz hh:mm:ss'

```

2. **Set a dummy variable and then clear it for all logical domains.**

Execute the following commands for all logical domains.

```

primary# ldm set-variable fix-php=true ldom
primary# ldm remove-variable fix-php ldom

```

3. **Save the changed configuration to the XSCF to replace the current configuration information.**

In this example, the name of the current configuration information is replaced with config1.

```

primary# ldm remove-sconfig config1
primary# ldm add-sconfig config1

```

4. **Restart all logical domains.**

## Restoration After Erroneous Detection of the "SCF Diagnosis initialize RTC" Error (RTIF2-131107-002)

[How to restore]

- If phenomenon 1 occurs:

- Case 1

If the Oracle Solaris time has returned to January 1, 1970, execute the setdate(8) command to set the XSCF time again. In this case, the XSCF is rebooted. After that, power cycle the PPAR. After this, the XSCF is reset. After that, power on the PPAR.

- Case 2

If the Oracle Solaris time is other than January 1, 1970, contact a field engineer.

In this case, do not execute the `resetdateoffset(8)` of `setdate(8)` command on the XSCF.

- Case 3

If the PPAR power is off, power on the PPAR. After that, check the Oracle Solaris time, and perform the above case 1 or case 2.

■ If phenomenon 2 occurs:

- Case 1

If the Oracle Solaris time has returned to January 1, 1970, it is necessary to initialize the time difference between the XSCF time and Hypervisor on all of the PPARs. Stop all the PPARs, and execute the `resetdateoffset -a` command to clear the time difference.

- Case 2

If the Oracle Solaris time is other than January 1, 1970, contact a field engineer. In this case, do not execute the `resetdateoffset(8)` of `setdate(8)` command on the XSCF.

- Case 3

If the PPAR power is off, power on the PPAR. After that, check the Oracle Solaris time, and perform the above case 1 or case 2.

■ If phenomenon 3 occurs:

If phenomenon 1 or 2 also occurs, perform its [How to restore] action first.

Set the Oracle Solaris time again.

## Problems Resolved in Versions Prior to XCP 2050

The following table lists the problems resolved in versions prior to XCP 2050.

Table 3-42 Problems Resolved in Versions Prior to XCP 2050

|                    |                                                                                                                                                                                                                                                                                                                                           |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130612-001</b>                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | If the XCP firmware version is XCP 2041, XCP 2042, or XCP 2043, SPARC M10-4S cannot be replaced by using the <code>replacefru(8)</code> command in a system with more than one SPARC M10-4S.                                                                                                                                              |
| <b>Workaround</b>  | Perform cold replacement (replacement performed with the input power off) or update the XCP firmware version to XCP 2044 or later before performing replacement.                                                                                                                                                                          |
| <b>RTI No.</b>     | <b>RTIF2-130516-005</b>                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | If the <code>showcodactivation(8)</code> command is executed while data is being restored with the <code>restoreconfig(8)</code> or <code>restorecodactivation(8)</code> command, it may be impossible to display the execution results. If this occurs, the <code>showcodactivation(8)</code> command ends with a "codd internal error". |
| <b>Workaround</b>  | To display the <code>showcodactivation(8)</code> command execution result, execute it after the execution of the <code>restoreconfig(8)</code> or <code>restorecodactivation(8)</code> command has completed.                                                                                                                             |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130515-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>While the system is operating, the following events may occur:</p> <ol style="list-style-type: none"> <li>1. Oracle Solaris prtpticl command no longer responds.</li> <li>2. Display data (such as the XCP version) of prtpticl -v and prttdiag -v is not output as expected.</li> <li>3. For /var/adm/messages, the warning message, "PICL snmpplugin: cannot fetch object value", is output.</li> <li>4. XSCF CMDD process shutdown occurs and an XSCF is reset repeatedly, such that it becomes unusable. At this time, system operation can be continued.</li> </ol>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>         [How to restore]<br/>         If event 1 occurs:<br/>             Recover with the following procedure.</p> <ol style="list-style-type: none"> <li>1. End the prttdiag command with [Ctrl] + [C].</li> <li>2. Wait about 30 minutes, and allow an SNMP timeout to occur in the XSCF.</li> <li>3. In the control domain, execute the svcadm command to restart the picl service.</li> </ol> <p>If event 2 occurs:<br/>             Execute the command again. If results are displayed, the system can be operated continuously.<br/>             If the state where results are not displayed as expected continues, check whether the XSCF is operating.</p> <ul style="list-style-type: none"> <li>- If the XSCF is operating, use the rebootxscf(8) command to reset the XSCF.</li> <li>- If the XSCF is not operating, turn the input power supply of the system off and then back on (AC OFF/ON) to recover.</li> </ul> <p>If event 3 occurs:<br/>             The system can be operated continuously because this is a temporary warning message.</p> <p>If event 4 occurs:<br/>             Turn off the input power supply of the system, and then back on (AC OFF/ON) to recover.</p> |
| <b>RTI No.</b>     | <b>RTIF2-130507-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | There is a case where a RAID volume cannot be recognized after a power failure occurred while using the hardware RAID function.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | <p>Please run the activate-volume command at ok prompt to activate a hardware RAID volume. For the detailed procedure, see "14.2.11 Re-enabling a Hardware RAID Volume" in the <i>Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide</i>.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-130417-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | If the XSCF mounted in the crossbar box panics, it may be impossible to perform communication between the master XSCF and non-master XSCF.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>         [How to restore]<br/>         If the event occurs, wait 15 minutes or longer, and then use rebootxscf -s to reset the master XSCF.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130416-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | If the PCI device in the I/O domain is deleted (ldm rm-io) or a PCI device is added (ldm add-io), hypervisor abort may occur when the I/O domain starts.                                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | To delete the PCI device in the I/O domain, delete all devices under the same route complex as that of the PCI device to be deleted (which have the same xxxx of /pci@xxxx) and then add the necessary devices again.<br>Alternatively, do not assign multiple devices under the same route complex to a single I/O domain.                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-130415-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | In SPARC M10-4/M10-4S, if the firmware version is XCP 2031 or XCP 2032, the initial value of the following OpenBoot PROM environment variable differs from the default.<br>Even if the setting was changed with the setpparparam(8) command in the XSCF shell or the setenv command from the OpenBoot PROM prompt, it will return to the original value.<br>auto-boot? false<br>diag-switch? true<br>fcode-debug? true<br>local-mac-address? false |
| <b>Workaround</b>  | There is no effective workaround.<br>Make a firmware update to XCP 2041 or a later version, and then set the value of the OpenBoot PROM environment variable again.                                                                                                                                                                                                                                                                                |
| <b>RTI No.</b>     | <b>RTIF2-130410-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | The power may not be turned on/off from the operation panel.                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Workaround</b>  | If you power on from the XSCF shell, execute the poweron(8) command. If you power off from the XSCF shell, execute the poweroff(8) command.<br>[How to restore]<br>If this event occurs, power off the physical partition (PPAR) by using the poweroff -f command.                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-130410-005</b>                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | Executing the poweron -a command leads to the failure of power-on if more than one PPAR is powered on or more than one PPAR has failed in the system that has multiple physical partitions (PPARs).                                                                                                                                                                                                                                                |
| <b>Workaround</b>  | Specify the -p option to execute the poweron(8) command and turn on the power of each PPAR.<br>[How to restore]<br>If this event occurs, execute the poweroff -f command to forcefully power off the PPAR whose power has stopped being supplied during the middle of the power supply process. Then execute the poweron -p command to power on the PPAR.                                                                                          |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                    |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130410-006</b>                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>If a crossbar box has failed in the system that satisfies all the following conditions, a crossbar box may be degraded and the operation may not be continued.</p> <ul style="list-style-type: none"> <li>- Two or more crossbar boxes are configured.</li> <li>- Multiple PPARs are configured.</li> <li>- Each physical partition (PPAR) is configured with multiple CPU memory units (CMUU/CMUL).</li> </ul> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>Execute the poweron -p command to power on the PPAR again whose power has been turned off due to an error in the crossbar box.</p>                                                                                                                                                                                                             |
| <b>RTI No.</b>     | <b>RTIF2-130410-007</b>                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>If you forcefully turn off the power of the physical partition using the poweroff -f command immediately after you powered on the physical partition, you may not be able to power on/off the physical partition after that.</p>                                                                                                                                                                                |
| <b>Workaround</b>  | <p>After powering on a PPAR, do not execute the poweroff -f command until the ok prompt is displayed.</p> <p>[How to restore]</p> <p>If you fail to forcefully turn off the power of a PPAR, power off the input power supply and then turn on the power again (AC OFF/ON).</p>                                                                                                                                    |
| <b>RTI No.</b>     | <b>RTIF2-130329-001</b>                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>During firmware updating in SPARC M10-4S, ALARM LED of the master chassis suddenly turns on and remains stopped, and the firmware updating may not complete.</p>                                                                                                                                                                                                                                                |
| <b>Workaround</b>  | <p>There is no effective workaround.</p>                                                                                                                                                                                                                                                                                                                                                                           |
| <b>RTI No.</b>     | <b>RTIF2-130329-002</b>                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>If the configuration has two or more crossbar boxes and the physical partition (PPAR) is configured not to use any crossbar box, you cannot disconnect the power supply from the operating panel.</p>                                                                                                                                                                                                           |
| <b>Workaround</b>  | <p>Disconnect the power by using the poweroff(8) command.</p>                                                                                                                                                                                                                                                                                                                                                      |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130329-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | If you power off the input power supply while the status of the physical partition (PPAR) is PowerOn (from the startup of Oracle Solaris to the completion of PowerOff), an SRAM serious error occurs in any of the SPARC M10-4S units in the PPAR when you next power on the input power supply, preventing you from powering on the input power supply.                                                                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | When you power off the input power supply, do so after disconnecting the PPAR power supply in advance and making sure that disconnection is complete.<br>[How to restore]<br>If any problem occurs, restore the configuration with the following procedure:<br>1. Save settings with the dumpconfig(8) command.<br>2. Initialize the system to the factory default with the restoredefaults -c factory command.<br>3. Confirm that the READY LED of the XSCF or XSCF unit is lit after performing AC OFF/ON.<br>4. Replace the CPU memory unit lower (CMUL) or the motherboard unit (MBU).<br>5. Execute the restoreconfig(8) command to restore the XSCF setting information. |
| <b>RTI No.</b>     | <b>RTIF2-130319-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | Immediately after the input power of the SPARC M10-4/M10-4S is turned on, "DMA timeout error Hard detected" may be detected. The CPU memory unit lower (CMUL) will be degraded if you start the logical domain with "DMA timeout error Hard detected" detected.                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Workaround</b>  | There is no effective workaround.<br>Before starting the logical domain, disconnect the input power supply and then power it on again.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>RTI No.</b>     | <b>RTIF2-130305-005</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | In a system consisting of multiple SPARC M10-4S chassis, suppose an XSCF panic occurs on a SPARC M10-4S during diagnosis processing with the POST after physical partition (PPAR) power-on. Then, without continuing the power-on process, the PPAR may be powered off.                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | There is no effective workaround.<br>Make sure that the XSCF of each SPARC M10-4S is rebooted. Power on the PPAR again if they are rebooted.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-130305-008</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | If each physical partition (PPAR) is configured with one system board (PSB) in the system that has more than one SPARC M10-4S chassis and crossbar box, the power of the crossbar unit of a crossbar box is not stopped and the crossbar box is also powered on.                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                       |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130305-010</b>                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | Hypervisor Abort or OS PANIC may occur if all the PPARs are powered on using the poweron -a command in a system that has four or more SPARC M10-4S chassis units with multiple physical partitions (PPARs).                                                                                                                                           |
| <b>Workaround</b>  | Do not power on all the PPARs at the same time using the poweron -a command. Power on each PPAR using the -p option.                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-130305-021</b>                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | Immediately after starting the XSCF, the system board (PSB) may be recognized as "Unmount" and the addboard(8) command or the poweron(8) command may fail.                                                                                                                                                                                            |
| <b>Workaround</b>  | Wait about 30 seconds after starting up the XSCF and check that the target PSB is mounted using the showboards(8) command. Then, execute the addboard(8) or poweron(8) command.                                                                                                                                                                       |
| <b>RTI No.</b>     | <b>RTIF2-130305-024</b>                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | If you cycle the input power to a crossbar box in the state where a physical partition (PPAR) is powered on in the system that has the crossbar box, the following message may be output on the control domain console, and the power-on process of the PPAR may not be completed.<br>WARNING: Unable to connect to Domain Service providers          |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>If the power-on process of the PPAR is canceled, disconnect the power of the PPAR forcefully using the poweroff -f command.<br>After that, reset all the XSCFs using the rebootxscf -a command or disconnect the input power supply of all the SPARC M10-4S chassis and then power on again. |
| <b>RTI No.</b>     | <b>RTIF2-130227-001</b>                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | If you collect a snapshot by specifying the -a option, "XSCF Kernel Panic" may occur due to the increased load on the master XSCF.                                                                                                                                                                                                                    |
| <b>Workaround</b>  | If you collect a snapshot of the entire system, collect snapshots one by one by specifying a BB-ID of the SPARC M10-4S in the -b option and not the -a option. Perform this operation on all SPARC M10-4S.                                                                                                                                            |
| <b>RTI No.</b>     | <b>RTIF2-130219-001</b>                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | The takeover IP address of an XSCF cannot be specified as a value for "IPAddress" or "SlaveAddress," which can be set in the management file for a remote power management group.                                                                                                                                                                     |
| <b>Workaround</b>  | Specify the IP addresses of XSCF-LAN#0 and XSCF-LAN#1 of the master XSCF and the standby XSCF for "IPAddress" and "SlaveAddress," respectively.                                                                                                                                                                                                       |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

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|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130219-005</b>                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | If a failure occurs in a component in a physical partition (PPAR), the status of the PPAR in the SNMP MIB is not updated.                                                                                                                                                                                                                                                                                                                          |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-130219-008</b>                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | If you turn on the input power supply to a SPARC M10-4S chassis while a physical partition (PPAR) is being powered on in a system that has multiple PPARs, the SPARC M10-4S chassis for which the input power supply is turned on may not be recognized by the master XSCF.                                                                                                                                                                        |
| <b>Workaround</b>  | Power on the input power supply to all the crossbar boxes and SPARC M10-4S chassis that compose the system before powering on a PPAR.                                                                                                                                                                                                                                                                                                              |
| <b>RTI No.</b>     | <b>RTIF2-130215-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | After the hardware initial diagnosis detects a system board (PSB) error as described in the following error log, the PSB may not be degraded but the physical partition (PPAR) may be reset repeatedly.<br>Code: 40002000-003cff0000ff0000ff-02000e000000000000000000<br>FRU: /BB#x<br>Msg: SB deconfigured (SB-SB access error)<br>Code: 40002000-003cff0000ff0000ff-02000e010000000000000000<br>FRU: /BB#x<br>Msg: SB deconfigured (not running) |
| <b>Workaround</b>  | Disconnect the PSB where an error is detected from the PPAR configuration by using the deleteboard(8) command.                                                                                                                                                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-130215-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | Even when policy is set to psb by the setpcl(8) command, a unit of resource to be degraded when an error occurs may not be a system board but Field Replaceable Unit (FRU).                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | Disconnect the PSB where an error is detected from the PPAR configuration by using the deleteboard(8) command.                                                                                                                                                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-130212-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | If the power save operation, which is set by the setpparmode(8) command, is "enabled," the following problems may occur.<br>- A hang (heartbeat function) in a logical domain may be unable to be detected.<br>- If the CPU core resources are reduced during system startup (while a physical partition (PPAR) is operating), the PPAR may be powered off.                                                                                        |
| <b>Workaround</b>  | Set the power save operation to "disabled" by using the setpparmode(8) command.                                                                                                                                                                                                                                                                                                                                                                    |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

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|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130212-002</b>                                                                                                                                                                                                                                                             |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                       |
| <b>Description</b> | When you check the domain status by using the <code>showdomainstatus(8)</code> command from the XSCF after executing the <code>ldm add-sponfig</code> command from Oracle Solaris, "Host stopped" is displayed for all domains, including the control domain.                       |
| <b>Workaround</b>  | There is no effective workaround.<br>Execute the <code>ldm list-domain</code> command from Oracle Solaris to check the domain status.                                                                                                                                               |
| <b>RTI No.</b>     | <b>RTIF2-130212-003</b>                                                                                                                                                                                                                                                             |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                       |
| <b>Description</b> | If you execute the <code>testsb(8)</code> command to a system board (PSB) that is being diagnosed by the <code>testsb(8)</code> or <code>diagxbu(8)</code> command in a different session, the PSB that is being diagnosed may enter an abnormal state and become unavailable.      |
| <b>Workaround</b>  | Confirm that the PWR of the PSB to be diagnosed is "n" and that Test is not "Testing" by using the <code>showboards(8)</code> command before executing the <code>testsb(8)</code> command.<br>If a PSB becomes unavailable, power off the entire system and then power it on again. |
| <b>RTI No.</b>     | <b>RTIF2-130130-001</b>                                                                                                                                                                                                                                                             |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                    |
| <b>Description</b> | If you turn on the AC when the system configuration has no CPU memory unit upper (CMUU), a "Hardware access error" error log is generated for CMUU. The same phenomenon occurs when you add or replace SPARC M10-4S.                                                                |
| <b>Workaround</b>  | The error log is registered because no CMUU is mounted. So, ignore it.                                                                                                                                                                                                              |
| <b>RTI No.</b>     | <b>RTIF2-130109-002</b>                                                                                                                                                                                                                                                             |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                 |
| <b>Description</b> | If "Console path is switched" is registered in the event log of a system in which the physical partition (PPAR) consists of multiple system boards (PSBs), the PPAR-ID may have an invalid value.                                                                                   |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-130109-005</b>                                                                                                                                                                                                                                                             |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                       |
| <b>Description</b> | If you use the <code>replacefru(8)</code> command to replace a power supply unit (PSU), "Indispensable parts are not installed (PSU)" may be registered in the event log.                                                                                                           |
| <b>Workaround</b>  | This is an error log that is registered because a PSU is removed. Ignore it.                                                                                                                                                                                                        |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

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|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130109-006</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | <p>If you change the setting information of the remote power management function while the standby XSCF is in failure or starting, the changed setting information may not be reflected in the standby XSCF even after the start of the standby XSCF is completed.</p> <p>If XSCF switching occurs in this state, the master XSCF after the switching may not be able to achieve remote power management based on the changed setting information.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | <p>Disable the remote power management function, and then configure it again using the following procedure:</p> <ol style="list-style-type: none"> <li>1. Execute the <code>setremotepwrmgmt -c disable</code> command to disable the remote power management function.</li> <li>2. Save the management file, and then use the <code>clearremotepwrmgmt(8)</code> command to initialize the setting information. <ul style="list-style-type: none"> <li>- If the standby XSCF is starting, execute the <code>clearremotepwrmgmt(8)</code> command after the start is completed.</li> <li>- If the standby XSCF is in failure, use the <code>replacefru(8)</code> command to replace the target FRU, and then execute the <code>clearremotepwrmgmt(8)</code> command.</li> </ul> </li> <li>3. After the start of the standby XSCF is completed, execute the <code>setremotepwrmgmt -c config</code> command to restore the setting information based on the saved management file.</li> <li>4. Execute the <code>setremotepwrmgmt -c enable</code> command to enable the remote power management function.</li> </ol> |
| <b>RTI No.</b>     | <b>RTIF2-130109-007</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | <p>If all physical partitions (PPARs) are stopped and you try to power on a PPAR, the PPAR may not be powered on without being registered in the error log.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>Turn off the input power supply and turn it back on to all SPARC M10 system chassis and then try to power on the PPAR again.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>RTI No.</b>     | <b>RTIF2-121219-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | <p>Some chassis may not be recognized if all power cables are not connected within four minutes when connecting the power cables of the system that is composed of multiple SPARC M10-4S chassis.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Workaround</b>  | <p>Make sure not to exceed four minutes to finish connecting all the power cables when the system is composed of multiple SPARC M10-4S chassis.</p> <p>Remove and connect the power cables of all the chassis again when there is a SPARC M10-4S chassis that is not recognized.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>RTI No.</b>     | <b>RTIF2-121219-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | <p>While powering on/off the remote power management group, the <code>showremotepwrmgmt(8)</code> command may be abnormally terminated with the following message.</p> <p>Controller response timeout.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | <p>Execute the <code>showremotepwrmgmt(8)</code> command again.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

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|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-121219-005</b>                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                    |
| <b>Description</b> | When a component with high temperature or low temperature at an air inlet is registered, the information on the second faulty component (the second suspect component) displayed in the FRU may not be correct.                                                                                  |
| <b>Workaround</b>  | Ignore the information on the displayed second faulty component (the second suspect component).                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-121219-006</b>                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                    |
| <b>Description</b> | Replacement of the power supply unit (PSU) using the <code>replacefru(8)</code> command may fail with "Warning:005".                                                                                                                                                                             |
| <b>Workaround</b>  | Execute the <code>replacefru(8)</code> command again and replace the PSU.                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-121219-009</b>                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                    |
| <b>Description</b> | If an error log indicating a fan failure, "Power-on failure" or "Power-off failure", is registered, other than the correct component may be displayed as the faulty FRU.                                                                                                                         |
| <b>Workaround</b>  | If the failure is "Power-on failure" or "Power-off failure", and the faulty component is a fan, replace the following component. <ul style="list-style-type: none"> <li>- For the SPARC M10-1<br/>Motherboard unit (MBU)</li> <li>- For the SPARC M10-4/M10-4S<br/>PSU backplane unit</li> </ul> |
| <b>RTI No.</b>     | <b>RTIF2-121219-010</b>                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                              |
| <b>Description</b> | Error log "XSCF hang-up is detected" may be registered when the <code>switchscf(8)</code> command is executed.                                                                                                                                                                                   |
| <b>Workaround</b>  | Ignore this error log entry.                                                                                                                                                                                                                                                                     |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

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|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-121219-012</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | When a SPARC M10-4S chassis is replaced using the <code>replacefru(8)</code> command, replacement of the SPARC M10-4S chassis may fail with "internal error" displayed.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | Use the following procedure to replace a SPARC M10-4S chassis by using the <code>replacefru(8)</code> command. <ol style="list-style-type: none"> <li>1. Enter "r" to replace the chassis in response to the message "Do you want to continue?[r: replace c:cancel]" which is displayed after the <code>replacefru(8)</code> command is executed.</li> <li>2. Replace the SPARC M10-4S chassis, and wait about 15 minutes after the input power of the chassis is turned on.</li> <li>3. Execute the <code>showlogs</code> event command to display an event log.</li> <li>4. Continue the replacement using either of the following methods. <ul style="list-style-type: none"> <li>- If the event log displays "XSCF update is started" <p>Wait until the "XCP update has been completed" log is registered. When "XCP update has been completed" is displayed, enter "f" in response to "Please select[f:finish]" and then continue the replacement by following the instructions displayed on the screen.</p> </li> <li>- If the event log does not display "XSCF update is started" <p>Enter "f" in response to "Please select[f:finish]" and then continue the replacement according to the instructions on the screen.</p> </li> </ul> </li> </ol> |
| <b>RTI No.</b>     | <b>RTIF2-121219-013</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | When a SPARC M10-4S chassis finds an error indicating that the XSCF cannot be started, not only an error log for the SPARC M10-4S chassis where the error occurred but also an error log regarding a cable connection error in a chassis that does not exist may be registered.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | Ignore the error log regarding a cable connection error registered for a SPARC M10-4S chassis that does not exist.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>RTI No.</b>     | <b>RTIF2-121219-014</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | When cold replacement is performed (replacement performed while the input power is off) due to an error with BB#00, the setting information for the XSCF is deleted.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | To replace a SPARC M10-4S chassis due to an error in BB#00, use the <code>replacefru(8)</code> command.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-121219-015</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | When a SPARC M10-4S chassis is removed with the <code>initbb(8)</code> command, the information on the removed chassis remains without being completely deleted.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Workaround</b>  | To remove a SPARC M10-4S chassis, perform the cold removal (the chassis is removed by turning off the input power).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>RTI No.</b>     | <b>RTIF2-121219-016</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | An "internal error" may occur if the <code>prtfru(8)</code> command is executed while powering on/off or resetting the physical partition (PPAR).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | Wait for the power on/off or reset of the PPAR to complete and then execute the <code>prtfru(8)</code> command again.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

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| <b>RTI No.</b>     | <b>RTIF2-121219-017</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | If a SPARC M10-4S chassis is powered off after an error log is registered because a memory error occurred in BB#00 or BB#01, the same error log may be registered again.                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>RTI No.</b>     | <b>RTIF2-121219-018</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | In a system in which two or more SPARC M10-4S chassis units constitute multiple physical partitions (PPARs), if you power on the PPAR after switching the master XSCF as a result of any of the following, error logs of "STICK does not start (CPU)", "STICK does not start (MBC)", or "STICK count up error" may be registered, causing degradation of the CPU memory units (CMUU/CMUL). <ul style="list-style-type: none"> <li>- Performing firmware update</li> <li>- Detecting an XSCF failure</li> <li>- Executing the switchscf(8) command</li> </ul> |
| <b>Workaround</b>  | There is no effective workaround.<br>After switching of the master XSCF is performed, power off the input power supply of all SPARC M10-4S chassis and power them on again, without powering on the PPAR.                                                                                                                                                                                                                                                                                                                                                    |
| <b>RTI No.</b>     | <b>RTIF2-121219-019</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | When a PCI card is inserted to or removed from a PCI expansion unit, a number that is different from the relevant PCI card number (PCI#) is displayed in the log. In addition, when error log "PCICARD failed" is registered due to an error with the PCIe slot on the PCI expansion unit or the PCI card, a number other than that of the relevant PCI card number (PCI#) is displayed in the error log.                                                                                                                                                    |
| <b>Workaround</b>  | Read it as the number obtained by subtracting one from the PCI number (PCI#) displayed in the log.                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>RTI No.</b>     | <b>RTIF2-121206-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | When starting the domain, the following error message may appear.<br>Msg: PCI Express link not active                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | Ignore this message if the showhardconf(8) command recognizes the PCI Express (PCIe) card.                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-121204-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | The dynamic resource management software (ServerView Resource Orchestrator) sometimes fails to register a server to be monitored.                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Workaround</b>  | There is no effective workaround.<br>Register the server to be monitored again by the dynamic resource management software.                                                                                                                                                                                                                                                                                                                                                                                                                                  |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-121204-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | If a node of PRIMECLUSTER stops, node switching does not occur automatically.                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | There is no effective workaround.<br>Switch nodes manually in PRIMECLUSTER.                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>RTI No.</b>     | <b>RTIF2-121204-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | When you set or change OpenBoot PROM environment variables by using setenv or nvramrc of the OpenBoot PROM, or by using the eeprom or ldm set-var command of Oracle Solaris, the setting or change may not be retained after power cycling of the system.                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | After you update an OpenBoot PROM environment variable, execute the ldm add-config command to save the domain configuration information to the XSCF.                                                                                                                                                                                                                                                                                                                                              |
| <b>RTI No.</b>     | <b>RTIF2-121204-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | When you enable the auto boot function of the guest domain by using the setpparmode(8) command of the XSCF and then start the control domain and the guest domain simultaneously, you may encounter the situation where the following error message is recorded and Oracle Solaris cannot be started in the guest domain.<br>Code: 20000000-00ffff0000ff0000ff-030000020000000000000000<br>Status: Notice Occurred: Nov 16 16:55:25.518 JST 2012<br>FRU: /UNSPECIFIED<br>Msg: Boot process failed |
| <b>Workaround</b>  | Use the XSCF setpparmode(8) to disable the autoboot function of the guest domain, and then start Oracle Solaris of the control domain. Then use the ldm start-domain command for Oracle VM Server for SPARC to start the guest domain.<br>[How to restore]<br>After stopping the guest domain with the ldm stop-domain command for Oracle VM Server for SPARC, execute the ldm start-domain command to start the guest domain.                                                                    |
| <b>RTI No.</b>     | <b>RTIF2-121130-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | When you enable and disable the remote power management using RCIL, and then enable it again, the remote power management of the ETERNUS DX80/DX90/DX410/DX440/DX8100/DX8700 S2 may not work.                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | When you enable and disable the remote power management using RCIL, reboot the XSCF.                                                                                                                                                                                                                                                                                                                                                                                                              |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-121129-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | During operation of the XSCF firmware, a "system backup memory access error", which is an error in the PSU backplane (PSUBP), may be erroneously detected and registered in the error log. If this erroneous detection occurs during the start process of the physical partition (PPAR), the start of the PPAR may fail. Also, if it occurs during the collection of log information, the log information collection fails. In addition, a failure mark may be displayed on the PSUBP when the error is detected. If the failure mark is displayed on the PSUBP, the resources necessary for starting the PPAR are insufficient. Therefore, when start of the PPAR is requested, a log indicating that the start of the PPAR failed is registered in the power log. You can display the power log with the showlogs power command.                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Workaround</b>  | <p>If you find a log described in "Description," execute the showstatus(8) or showhardconf(8) command to check whether the failure mark is displayed on the PSUBP.</p> <ul style="list-style-type: none"> <li>- When the failure mark is not displayed on the PSUBP:<br/>No problem has occurred in the hardware. So, ignore the error log and continue the operation.</li> <li>- When the failure mark is displayed on the PSUBP:<br/>Clear the failure mark by using the following procedure. <ol style="list-style-type: none"> <li>1. Switch the mode switch on the operation panel to the Service mode position.</li> <li>2. Turn the power to the target SPARC M10 system chassis off and then on again, and then reboot the XSCF. For the SPARC M10-4S, turn the power to every SPARC M10-4S chassis off and then on again.</li> <li>3. After the XSCF has been rebooted, return the mode switch on the operation panel to its original position.</li> </ol> </li> </ul> <p>If the same error recurs even after rebooting the XSCF, there is a possibility that the error has not been erroneously detected but that a PSUBP hardware failure has occurred. Ask your Fujitsu field engineer (service provider) to replace the PSUBP.</p> |
| <b>RTI No.</b>     | <b>RTIF2-121129-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | <p>When you replace the CPU memory unit lower (CMUL) or motherboard unit (MBU), the XSCF user account information may be deleted.</p> <p>To recover the XSCF user account information, you need to use the restoreconfig(8) command to restore the information saved using the dumpconfig(8) command.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Workaround</b>  | After you have conducted the replacement, use the restoreconfig(8) command to restore the information saved using the dumpconfig(8) command, or set the XSCF user account information again.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>RTI No.</b>     | <b>RTIF2-121129-004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | <p>The restoredefaults(8) cannot initialize the following information. The setting information remains as it is.</p> <ul style="list-style-type: none"> <li>- Time zone of the XSCF</li> <li>- Server certificate for HTTPS</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>Reset data with the settimezone(8), sethttps(8), and setssh(8) commands.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-121113-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | After setting an invalid value for the user name with the setsmtp(8) command, you execute the setemailreport(8) command to send a test mail. Then the reply address shows that the mail has been successfully sent.                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | There is no effective workaround.<br>Even if it is displayed that the test mail has been successfully sent, it is not sent.                                                                                                                                                                                                                                                                                                                                                                   |
| <b>RTI No.</b>     | <b>RTIF2-121113-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | If you execute the setaudit delete command and use the viewaudit(8) to display the audit log, some audit logs may not be deleted.                                                                                                                                                                                                                                                                                                                                                             |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>RTI No.</b>     | <b>RTIF2-121113-006</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | If you display the XSCF Web screen in another window or in another tab without using the XSCF Web menu, contents may not be normally displayed.                                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | For operation to display the XSCF Web screen, use the menu tree.                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>RTI No.</b>     | <b>RTIF2-121113-007</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | While the XSCF is starting, a "process down," panic, or watchdog timeout occurs, which may cause the XSCF to be reset.                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | Confirm that the XSCF is successfully started.<br>If it is not started, turn off the power of the physical partition (PPAR), and then disconnect the input power supply to the system and back on again (AC OFF/ON).<br>When turning off/on the input power, power off the system, wait at least 30 seconds, and then power on the system.<br>If the XSCF does not start even after the system input power is cycled, replace the CPU memory unit lower (CMUL) or the motherboard unit (MBU). |
| <b>RTI No.</b>     | <b>RTIF2-121113-009</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | While the XSCF is running, a "process down," panic, or watchdog timeout occurs, which may cause the XSCF not to be rebooted after an XSCF reset.                                                                                                                                                                                                                                                                                                                                              |
| <b>Workaround</b>  | Confirm that the XSCF is successfully started.<br>If it is not started, turn off the power of the physical partition (PPAR), and then disconnect the input power supply to the system and back on again (AC OFF/ON).<br>When turning off/on the input power, power off the system, wait at least 30 seconds, and then power on the system.<br>If the XSCF does not start even after the system input power is cycled, replace the CPU memory unit lower (CMUL) or the motherboard unit (MBU). |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

|                    |                                                                                                                                                                                                                                                       |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-121113-011</b>                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                         |
| <b>Description</b> | If you execute the <code>showsnmp(8)</code> command, the following message may appear, which means that the <code>snmp</code> daemon has been terminated.<br>Agent Status: Disabled                                                                   |
| <b>Workaround</b>  | Execute the <code>showsnmp(8)</code> command again to confirm that the <code>snmp</code> daemon is restarted. If "Agent Status: Disabled" remains displayed, execute the <code>setsnmp enable</code> command to restart the <code>snmp</code> daemon. |
| <b>RTI No.</b>     | <b>RTIF2-121113-014</b>                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                         |
| <b>Description</b> | The error message <code>"/etc/redhat-release not found"</code> appears while the XSCF is starting.                                                                                                                                                    |
| <b>Workaround</b>  | Ignore this message.                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-121113-018</b>                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                         |
| <b>Description</b> | When you replace a FRU with the <code>replacefru(8)</code> command, the message "configuration changed (...)" showing the configuration change may be registered more than once in the event log.                                                     |
| <b>Workaround</b>  | Ignore the message that appears second time and later.                                                                                                                                                                                                |
| <b>RTI No.</b>     | <b>RTIF2-121113-019</b>                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                         |
| <b>Description</b> | When the power supply of the physical partition (PPAR) is disconnected according to the power supply schedule, "-" may be displayed as the cause (Cause) in the power log.                                                                            |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                     |
| <b>RTI No.</b>     | <b>RTIF2-121113-021</b>                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                         |
| <b>Description</b> | If time cannot be read accurately due to a failed clock in the XSCF board, an error showing a clock failure may not be recorded in the error log.                                                                                                     |
| <b>Workaround</b>  | There is no effective workaround.<br>If the following message appears when you execute the <code>poweron(8)</code> command, the clock in the XSCF board has failed. Replace the XSCF board.<br>Poweron canceled due to invalid system date and time.  |
| <b>RTI No.</b>     | <b>RTIF2-121113-022</b>                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                         |
| <b>Description</b> | If the operation panel has failed or is not connected, the XSCF cannot be started.                                                                                                                                                                    |
| <b>Workaround</b>  | Connect the operation panel. If the operation panel has failed, replace it.                                                                                                                                                                           |

Table 3-42 Problems Resolved in Versions Prior to XCP 2050 (*continued*)

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-121113-023</b><br><b>RTIF2-121113-028</b>                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                               |
| <b>Description</b> | If a CPU failure occurs while Hypervisor is running, the physical partition (PPAR) is reset more than once, and restarting of the PPAR may take time.                                                                                                                                       |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                           |
| <b>RTI No.</b>     | <b>RTIF2-121113-025</b>                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                               |
| <b>Description</b> | When a CPU failure occurs, an error message showing degradation or offline on Oracle Solaris may not be output to Syslog.                                                                                                                                                                   |
| <b>Workaround</b>  | Use the showlogs(8) command to check a failure state on the XSCF.                                                                                                                                                                                                                           |
| <b>RTI No.</b>     | <b>RTIF2-121113-027</b>                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                               |
| <b>Description</b> | If you update the firmware and then upload XCP from XSCF Web without rebooting the XSCF, uploading of XCP will fail and the XSCF Web session will time out.                                                                                                                                 |
| <b>Workaround</b>  | If you update the firmware and then continue to upload XCP with XSCF Web, reboot the XSCF.                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-121113-031</b>                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                               |
| <b>Description</b> | After creating an I/O domain to which the PCI card is assigned with Oracle VM Server for SPARC, turn on the power of the I/O domain. If you then stop it at the ok prompt, the configuration information of the PCI card may not be displayed with the showhardconf(8) command of the XSCF. |
| <b>Workaround</b>  | Start Oracle Solaris of the logical domain to which the PCI card is assigned with Oracle VM Server for SPARC.                                                                                                                                                                               |

## Problems With Oracle Solaris and Workarounds

This section describes problems with Oracle Solaris and workarounds for each version.

### Problems That Might Occur in Any Versions of Oracle Solaris and Workarounds

The following table lists the problems that might occur in any versions of Oracle Solaris being supported and workarounds for them.

Table 3-43 Problems That Might Occur in Any Versions of Oracle Solaris and Workarounds

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|                    |                                                                                                                                                 |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>24718083</b>                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                           |
| <b>Description</b> | If the XSCF is rebooted while the control domain is running, the power capping function will not work.                                          |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Execute the svcadm command to restart the ldoms/ldmd service.<br># svcadm restart ldmd |

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Table 3-43 Problems That Might Occur in Any Versions of Oracle Solaris and Workarounds (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>23025823</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>The addboard command fails to revert the I/O devices which have been degraded with the deleteboard command.</p> <p>This phenomenon occurs when all of the following conditions are true:</p> <ul style="list-style-type: none"> <li>- There is/are root domain(s) with two or more root complexes.</li> <li>- The deleteboard command removes root complexes from the root domain(s).</li> <li>- At least one endpoint in one or more of the removed root complexes is either unassigned or assigned to another domain.</li> <li>- The addboard command restores the root complex(es).</li> </ul> <p>[Error message example]</p> <pre> ----- XSCF&gt; addboard -v -y -c configure -m bind=resource -p 0 00-0 PSB#00-0 will be configured into PPAR-ID 0. Continue?[y n] :y Start connecting PSB to PPAR. [3600sec] 0./ (Omitted) Processing of the incoming DR request by the LDoms Manager is pending Incoming DR request is being processed by the LDoms ManagerDR sequence started (sequence#=2, message#=6) ...../ Restoring primary failed, it failed in the allocation of PCIE0 60../ Restoring primary failed, it failed in the allocation of PCIE1 .- Restoring primary failed, it failed in the allocation of PCIE2 ..\ Restoring primary succeeded, PCIE3 was assigned 90  Restoring ioroot failed, it failed in the allocation of PCIE4 ..  Restoring ioroot failed, it failed in the allocation of PCIE5 ./ Restoring primary failed, it failed in the allocation of PCIE6 ..\ Restoring primary failed, it failed in the allocation of PCIE7 (Omitted) Restoring primary succeeded, 15 core(s) were assigned .  Restoring guest1 succeeded, 34896609280 byte memory were assigned DR sequence finished (sequence#=2, message#=6) end PSB#00-0 could not be configured into PPAR-ID 0 due to operating system or Logical Domains Manager error. XSCF&gt; ----- </pre> <p>Moreover, once this phenomenon occurs, retrying addboard fails and the ldmd daemon might dump core.</p> |
| <b>Workaround</b>  | Execute the addboard command with -m bind=none option and recover the I/O configuration manually.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |

Table 3-43 Problems That Might Occur in Any Versions of Oracle Solaris and Workarounds (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | -                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>When a logical domain with a 10 Gigabit Ethernet card (SP1X7HF1F) for which the value of the OpenBoot PROM environment variable <code>diag-switch?</code> is set to true, the console displays the following warning message and the error log records "Msg: Device error (FCode informed error)".</p> <p>WARNING: /pci@X,XXXXXX: FCODE mapin doesn't match decoded register type;</p> <p>Also, executing the <code>showstatus(8)</code> command may show "Degraded" for the FRU on which the relevant PCI card is mounted.</p> |
| <b>Workaround</b>  | <p>Ignore all these outputs.</p> <p>To prevent these outputs, execute the following command on the ok prompt to change the value of the OpenBoot PROM environment variable <code>diag-switch?</code> to false.</p> <pre>setenv diag-switch? false</pre>                                                                                                                                                                                                                                                                            |
| <b>Bug</b>         | -                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | <p>If dynamic reconfiguration is performed on a physical partition which has a dual channel 10G FCoE card (SP1X7FBR2F/SP1X7FBS2F/7101683 (7101684)+7101687 (7101688)) mounted on it, system panic may occur due to the processing of the <code>emlxs</code> driver.</p>                                                                                                                                                                                                                                                            |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>Use Oracle Solaris 11.2 SRU 11.2.2.8.0 or later and Oracle Solaris 10 1/13 or later.</p> <p>If this specific card is mounted on a system, conduct any reconfiguration in deactivated state instead of dynamic reconfiguration.</p>                                                                                                                                                                                                                                                     |
| <b>Bug</b>         | -                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | <p>If dynamic reconfiguration is performed on a physical partition which has a Dual Port Gigabit Ethernet card (MMF) (SP1X7GD1F/7100482 (7100481)), connection is terminated.</p>                                                                                                                                                                                                                                                                                                                                                  |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>If this specific card is mounted on a system, conduct any reconfiguration in deactivated state instead of dynamic reconfiguration.</p>                                                                                                                                                                                                                                                                                                                                                 |
| <b>Bug</b>         | -                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>When "Oracle VM Server for SPARC 3.1.1.1" is installed on the control domain, messages like the following will be registered to the log file (<code>/var/svc/log/ldoms-ldmd:default.log</code>) of the <code>ldoms/ldmd</code> service.</p> <p>[Example of message]</p> <pre>Get Device ID command failed: Unknown (0x7E) ERROR: Cannot connect to BMC</pre>                                                                                                                                                                    |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>Ignore this message because there is no impact on system operation.</p>                                                                                                                                                                                                                                                                                                                                                                                                                |

Table 3-43 Problems That Might Occur in Any Versions of Oracle Solaris and Workarounds (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | -                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | <p>When physical partition dynamic reconfiguration (PPAR DR) is performed to replace a building block in a physical partition containing logical domains with the whole-core constraint and logical domains without the whole-core constraint, the number of CPUs and memory size of the whole-core constrained domains may not be restored correctly.</p> <p>With the <code>ldm list-domain</code> command, you can check whether the whole-core constraint is enabled for a logical domain. In the following example, the whole-core constraint is applied on the logical domain <code>domain-A</code> and not on the logical domain <code>domain-B</code>.</p> <pre>primary# ldm list-domain -o resmgmt NAME domain-A  CONSTRAINT   cpu=whole-core   max-cores=unlimited  ----- NAME domain-B  CONSTRAINT</pre> |
| <b>Workaround</b>  | <p>Enable the whole-core constraint for all logical domains in the physical partition, or disable it for all the logical domains.</p> <p>For details on how to set the whole-core constraint, see the <i>Oracle VM Server for SPARC 3.4 Administration Guide</i>.</p> <p>[How to restore]</p> <p>Execute the <code>ldm</code> command to manually restore the resources of the domain.</p>                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Bug</b>         | -                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | When you start the SunVTS 7.0 ps19 tests on a SPARC M12 system, they may terminate with an error.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |

# Problems That Might Occur With Oracle Solaris 11 and Workarounds

The following table lists the problems that might occur with Oracle Solaris 11 and workarounds for them.

Table 3-44 Problems That Might Occur With Oracle Solaris 11 and Workarounds

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>16312511</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | In the environment described below, if the system board is deleted by the physical partition dynamic reconfiguration (PPAR DR) function, a system board deletion failure, stop of virtual function (VF) communication, a root domain panic, and other phenomena may occur. The environment has an SR-IOV configuration, and either a 10 Gigabit Ethernet card running the ixgbe driver for the root domain or the onboard LAN of the SPARC M12-2S is assigned there.                                                                                                                                  |
| <b>Workaround</b>  | PPAR DR cannot be used in the above SR-IOV configuration. Use it in a virtual network (vnet) configuration.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Bug</b>         | <b>25262938</b><br><b>25954620</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | Execution of the nvmeadm or fwupdate command may fail with the following message displayed.<br>[Display example]<br># nvmeadm<br>ERROR: Platform not supported<br># fwupdate<br>ERROR: Platform not supported                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | Perform the following procedure.<br>1. Delete the configuration information files in /etc/ssm/hmp.<br># rm /etc/ssm/hmp/*<br>2. Execute nvmeadm or fwupdate.<br># nvmeadm<br>Or<br># fwupdate<br><br>Configuration information files are automatically created in /etc/ssm/hmp.<br><br>3. Create the following configuration information files.<br>Create the following two files in a text editor:<br>host_profile.xml<br>platforms.json<br><br>- Creating host_profile.xml<br># cat /tmp/host_profile.xml<br><?xml version="1.0" encoding="utf-8"?><br><hmp_profile schemaVersion="1"><br><control> |

Table 3-44 Problems That Might Occur With Oracle Solaris 11 and Workarounds (*continued*)

```

<!-- Time in seconds from Epoch. -->
<timestamp>1438633604</timestamp>

<!-- Minimum version of HMP supported -->
<hmp_min_ver>2.3.2</hmp_min_ver>

<!-- ilom, host, patch, other -->
<origin>ILOM</origin>

<supported_platforms>SPARC M12-1</supported_platforms>
<supported_platforms>SPARC M12-2</supported_platforms>
<supported_platforms>SPARC M12-2S</supported_platforms>
</control>

<tool_support>
 <element>

 <platform_name>SPARC M12-1</platform_name>
 <platform_name>SPARC M12-2</platform_name>
 <platform_name>SPARC M12-2S</platform_name>
 <utility_property>
 <name>ALL</name>
 <supported>NO</supported>
 <add_runtime_mode>DEFAULT</add_runtime_mode>
 </utility_property>
 <utility_property>
 <name>FWUPDATE</name>
 <supported>YES</supported>
 <add_runtime_mode>DEFAULT</add_runtime_mode>
 </utility_property>
 <utility_property>
 <name>NVMEADM</name>
 <supported>YES</supported>
 <add_runtime_mode>DEFAULT</add_runtime_mode>
 </utility_property>
 </element>
</tool_support>
<platform_data>
{
 "version": "1.0.0.0",
 "platforms": {
 "aliases": {
 "SPARC M12-1": "undefined",
 "SPARC M12-2": "undefined",
 "SPARC M12-2S": "undefined"
 },
 "hosts": {
 "undefined": {
 "internal_hbas": {
 "1000:0050:1000:3060":
 {
 "backplanes": ["internal_4_square"],

```



Table 3-44 Problems That Might Occur With Oracle Solaris 11 and Workarounds (*continued*)

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|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>24819902</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>In the control domain with SRU 11.3.8.7.0 (Oracle VM Server for SPARC 3.4) or later applied, the deleteboard command may behave inappropriately where at least one logical domain binds cores shared with another logical domain. This is because Oracle VM Server for SPARC wrongly calculates the number of CPU cores to be removed.</p> <p>[Example]</p> <ul style="list-style-type: none"><li>- The deleteboard -m unbind=resource command succeeds but the more CPUs are removed than necessary.</li><li>- The deleteboard -m unbind=resource command fails even if the CPU allocation is the same as one where it succeeds with Oracle VM Server for SPARC 3.3.</li><li>- The deleteboard -m unbind=none command fails even though there are sufficient free CPUs in the remaining system boards.</li></ul> |
| <b>Workaround</b>  | <p>Apply either of the following (1) or (2).</p> <p>(1) Assign virtual CPUs with the whole-core constraint.</p> <p>(2) Change the PPAR DR policy to "targeted".</p> <pre># svcadm disable ldmd # svccfg -s ldmd setprop ldmd/fj_ppar_dr_policy=targeted # svcadm refresh ldmd # svcadm enable ldmd</pre>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |

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Table 3-44 Problems That Might Occur With Oracle Solaris 11 and Workarounds (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>20882700</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>After you execute dynamic reconfiguration of physical partitions, or after you execute the <code>ldm add-io/remove-io</code> command to add/delete a resource dynamically, a message indicating that the resource was added/deleted may be output to the console.</p> <p>[Example 1]<br/> SUNW-MSG-ID: FMD-8000-CV, TYPE:<br/> Alert, VER: 1, SEVERITY: Minor<br/> EVENT-TIME: Mon May 11 20:04:48 JST 2015<br/> PLATFORM: ORCL,SPARC64-X, CSN:<br/> 2081232009, HOSTNAME: 4S-408-D0<br/> SOURCE: software-diagnosis, REV: 0.1<br/> EVENT-ID: 76d8e4f6-d621-4ede-a86e-93abdc908a6<br/> DESC: FRU '/SYS//BB0/CMUU/CMP1/MEM17A' has been removed from the system.<br/> ...<br/> [Example 2]<br/> SUNW-MSG-ID: FMD-8000-A0, TYPE:<br/> Alert, VER: 1, SEVERITY: Minor<br/> EVENT-TIME: Thu May 14 15:50:31 JST 2015<br/> PLATFORM: unknown, CSN: unknown, HOSTNAME: iodom0<br/> SOURCE: software-diagnosis, REV: 0.1<br/> EVENT-ID: 1f8d1ae8-9097-4204-b5d6-d605aac28390<br/> DESC: FRU '/SYS/BB#1-PCI#6' has been added to the system.<br/> ...</p> |
| <b>Workaround</b>  | <p>Before executing dynamic reconfiguration of physical partitions, or before executing the <code>ldm add-io/remove-io</code> command to add/delete a resource dynamically, add the setting below to the <code>/usr/lib/fm/fmd/plugins/fru-monitor.conf</code> file of all logical domains where Oracle Solaris 11.3 is installed. Then, restart the logical domains.</p> <pre>setprop fmon_alert_enable "false"</pre>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |

Table 3-44 Problems That Might Occur With Oracle Solaris 11 and Workarounds (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>20950622</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | <p>Suppose the root domain is running Oracle Solaris 11.3 or later when an I/O domain created in one of the following procedures is started/restarted. Then, the OpenBoot PROM aborts, and the I/O domain cannot be started/restarted.</p> <p>[Creation pattern 1]</p> <ol style="list-style-type: none"> <li>1. Add a PCI expansion unit to the root domain by using the PCI hot plug (PHP) function.</li> <li>2. Add two PCI cards to the PCI expansion unit by using PHP.</li> <li>3. Dynamically create a virtual function (VF) from the physical function (PF) of each PCI card.</li> <li>4. If the created VF is dynamically assigned to an active I/O domain, the OpenBoot PROM will abort when the I/O domain restarts.</li> </ol> <p>Or</p> <ol style="list-style-type: none"> <li>4. If the created VF is statically assigned to an inactive I/O domain, the OpenBoot PROM will abort when the I/O domain starts.</li> </ol> <p>[Creation pattern 2]</p> <ol style="list-style-type: none"> <li>1. Add the PCIe bus to the root domain by using dynamic PCIe bus assignment.</li> <li>2. Add two PCI cards to the same PCIe bus on the root domain by using PHP.</li> <li>3. If each PCI card is dynamically assigned to an active I/O domain, the OpenBoot PROM will abort when the I/O domain restarts.</li> </ol> <p>Or</p> <ol style="list-style-type: none"> <li>3. If each PCI card is statically assigned to an inactive I/O domain, the OpenBoot PROM will abort when the I/O domain starts.</li> </ol> <p>After step 2, even if you dynamically create a virtual function (VF) from the physical function (PF) of each PCI card and assign the created VF to an I/O domain, the same phenomenon occurs.</p> <p>[Creation pattern 3]</p> <ol style="list-style-type: none"> <li>1. With two PCI cards mounted to the same PCIe bus, use dynamic PCIe bus assignment to add the PCIe bus to the root domain.</li> <li>2. If each PCI card is dynamically assigned to an active I/O domain, the OpenBoot PROM will abort when the I/O domain restarts. If each PCI card is statically assigned to an inactive I/O domain, the OpenBoot PROM will abort when the I/O domain starts.</li> </ol> <p>After step 1, if you dynamically create a virtual function (VF) from the physical function (PF) of each PCI card and assign the created VF to an I/O domain, the same phenomenon occurs.</p> |
| <b>Workaround</b>  | <p>To prevent the OpenBoot PROM from aborting, delete the VFs and the PCI cards assigned to the I/O domain by using the <code>ldm remove-io</code> command before starting/restarting the I/O domain. After starting the I/O domain, dynamically reassign the VFs and the PCI cards with the <code>ldm add-io</code> command.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Bug</b>         | <b>21849217</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | <p>For Oracle Solaris 11.2 SRU 11.2.14.5.0 or later and for Oracle Solaris 11.3 or later, the following error message may be output when the <code>fwupdate</code> command is executed, and the command may terminate abnormally.</p> <p>ERROR: Could not identify host type</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Workaround</b>  | <p>Delete all the files in the <code>/etc/ssm/hmp</code> directory, and then execute the <code>fwupdate</code> command.</p> <pre># cd /etc/ssm/hmp # rm -f *</pre> <p>Do not delete the <code>/etc/ssm/hmp</code> directory at this time.</p> <p>After you execute the <code>fwupdate</code> command, new files are created in the <code>/etc/ssm/hmp</code> directory. Do not delete these files.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |

Table 3-44 Problems That Might Occur With Oracle Solaris 11 and Workarounds (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | -                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | When the fwupdate command is executed, the following message may be output.<br>Get Device ID command failed: Unknown (0x7E)<br>No metadata provided, so version verification can not be completed                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | Ignore this error message because there is no impact on command operation.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Bug</b>         | <b>25473170</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | In Oracle Solaris 11.2 SRU 8.4 or later, suppose that the network interface of the dual 10Gbps FCoE card that is made by QLogic and runs the qlcnic driver is deactivated. When it is activated again (*1), panic may occur with the following message displayed during communication using this interface.<br>panic[cpux]/thread=xxxxxxxxxx: Fatal error has occurred in: PCIe fabric.(0x40)(0x41)<br><br>*1 This applies to the following cases, where the dual 10Gbps FCoE card is used.<br>- To change the configuration, the interface is deactivated by the ipadm delete-ip or ifconfig unplumb command, and activated by the ipadm create-ip or ifconfig plumb command.<br>- The FCoE interface is assigned to a logical domain. The physical partition dynamic reconfiguration (PPAR DR) function is used for continuing operation and use even after the logical domain is suspended and resumed.<br>- PRIMECLUSTER Global Link (NIC switching method) is used to switch or switch back a redundant NIC. |
| <b>Workaround</b>  | Perform the following procedure.<br>1. Copy the /kernel/drv/qlcnic.conf file to the /etc/driver/drv directory.<br>2. Add the "enable_multi_rds_sets = 0;" line to the copied file.<br>3. Restart the OS.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |

Table 3-44 Problems That Might Occur With Oracle Solaris 11 and Workarounds (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | -                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>In an environment with Oracle VM Server for SPARC 3.4 (SRU 11.3.8.7.0) or later installed on the control domain, the deleteboard(8) command may fail if all of the following conditions are met.</p> <p>[Problem occurrence conditions]</p> <ol style="list-style-type: none"> <li>1) The number of logical domains is larger than the number of existing CPU cores in the physical partition after the deleteboard(8) command is executed.</li> <li>2) A logical domain is partially allocated two or more CPU cores.</li> <li>3) The physical partition was dynamically reconfigured by the deleteboard(8) command with the -m unbind=resource option specified.</li> </ol> <p>[Example]</p> <pre>XSCF&gt; deleteboard -y -v -c disconnect -m unbind=resource 01-0 PSB#01-0 will be unconfigured from PPAR immediately. Continue?[y n] :y Start unconfigure preparation of PSB. [1200sec] (Omitted) end PSB#01-0 could not be unconfigured from PPAR-ID 0 due to operating system or Logical Domains Manager error.</pre> |
| <b>Workaround</b>  | <p>Change the PPAR DR policy to "targeted".</p> <p>[Example]</p> <pre>primary# svcadm disable ldmd primary# svccfg -s ldmd setprop ldmd/fj_ppar_dr_policy=targeted primary# svcadm refresh ldmd primary# svcadm enable ldmd</pre>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Bug</b>         | -                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | <p>A logical domain may panic if all of the following conditions are met.</p> <ol style="list-style-type: none"> <li>1) A logical domain is running SRU 11.3.12.4.0 or later.</li> <li>2) A CPU chip is degraded.</li> <li>3) A logical domain is rebooted.</li> </ol> <p>[Panic message example]</p> <pre>panic[cpuxxx]/thread=xxxxxxxxxxxx: Could not compute mpo_lgroup[]</pre>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |

# Problems That Might Occur With Oracle Solaris 10 and Workarounds

The following table lists the problems that might occur with Oracle Solaris 10 and workarounds for them.

Table 3-45 Problems That Might Occur With Oracle Solaris 10 and Workarounds

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|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>15738030</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | If both of the following conditions are satisfied, a control domain panic may occur with "BAD TRAP: type=31". <ul style="list-style-type: none"><li>- The operating system of the control domain is Oracle Solaris 10.</li><li>- As a result of executing <code>ldm list-domain -o memory primary</code>, the RA (real address) is greater than <code>0x200000000000</code>.</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | This has been modified with patch 148888-03 for Oracle Solaris 10. Perform the following procedure. <ol style="list-style-type: none"><li>1. Execute <code>ldm list-domain -o memory primary</code> to display the value of SIZE.</li><li>2. Execute <code>ldm start-reconf primary</code> to enter the delayed reconfiguration mode.</li><li>3. Execute <code>ldm remove-memory 256M primary</code> to reduce the assigned memory.</li><li>4. Execute <code>ldm set-memory &lt;SIZE value in step 1&gt; primary</code> to return the assigned memory back to the original size.</li><li>5. Restart the Oracle Solaris of the control domain.</li><li>6. Execute <code>ldm list-domains -o memory primary</code> to confirm that the RA is smaller than <code>0x200000000000</code>.</li><li>7. Execute <code>ldm add-spconfig &lt;configuration information name&gt;</code> to save the configuration information to the XSCF.</li></ol> |

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# Problems Resolved in Oracle Solaris 11.4

The following table lists the problems resolved in Oracle Solaris 11.4. You might encounter them in supported releases earlier than Oracle Solaris 11.4.

Table 3-46 Problems Resolved in Oracle Solaris 11.4

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|                    |                                                                                                                                                                                                                                                                                                           |
|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>25028104</b>                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                         |
| <b>Description</b> | If the root complex is dynamically added/deleted by dynamic reconfiguration of the physical partition, the PCIe end point device under the added/deleted root complex may not be reflected to the PCIe end point device configuration information shown by the <code>XSCF showpparinfo(8)</code> command. |
| <b>Workaround</b>  | [How to restore]<br>Execute the <code>svcadm</code> command in the control domain to restart the <code>ldoms/ldmd</code> services.<br><code># svcadm restart ldoms/ldmd</code>                                                                                                                            |

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Table 3-46 Problems Resolved in Oracle Solaris 11.4 (*continued*)

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>26822709</b><br><b>26934364</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>On the control domain with SRU 11.3.8.7.0 (Oracle VM Server for SPARC 3.4) or later installed, dynamic reconfiguration of the physical partition with the XSCF deleteboard(8) command may fail.</p> <p>This phenomenon may occur when the following conditions are met.</p> <ol style="list-style-type: none"> <li>1) Memory degradation has occurred (*1).</li> <li>2) The PPAR DR policy is set to "ratio" or "auto" (*2).</li> </ol> <p>Or</p> <ol style="list-style-type: none"> <li>1) The hvdump-reboot is set to "off" (*3).</li> <li>2) The physical partition has been reset due to the XSCF reset command with the xir option or a hardware error.</li> <li>3) The PPAR DR policy is set to "ratio" or "auto" (*2).</li> </ol> <p>*1 How to check the memory degradation<br/> primary# <b>ldm list-devices -a -S memory</b><br/> If the STATUS value is "fail" in the output, you can determine that the memory region has been degraded by SIZE from the PA (physical address) displayed on the same line.</p> <p>*2 How to check the PPAR DR policy<br/> primary# <b>svccfg -s ldmd listprop ldmd/fj_ppar_dr_policy</b><br/> ldmd/fj_ppar_dr_policy astring auto<br/> In this case, the PPAR DR policy is set to "auto."</p> <p>*3 How to check hvdump-reboot<br/> primary# <b>ldm list-hvdump</b><br/> hvdump=on<br/> hvdump-reboot=off<br/> In this case, hvdump-reboot is set to "off."</p> |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.3.29.5.0.</p> <p>Change the PPAR DR policy to "targeted" in the following way.</p> <pre>primary# <b>svcadm disable ldmd</b> primary# <b>svccfg -s ldmd setprop ldmd/fj_ppar_dr_policy=targeted</b> primary# <b>svcadm refresh ldmd</b> primary# <b>svcadm enable ldmd</b></pre> <p>[How to restore]</p> <p>Restart the physical partition with the XSCF poweroff(8) and poweron(8) commands.<br/> Example: Physical partition is 0<br/> XSCF&gt; <b>poweroff -y -p 0</b><br/> XSCF&gt; <b>poweron -y -p 0</b></p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |

Table 3-46 Problems Resolved in Oracle Solaris 11.4 (*continued*)

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|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>26822709</b><br><b>26934364</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | <p>On the control domain with SRU 11.3.2.4.0 (Oracle VM Server for SPARC 3.3) or later installed, the Oracle Solaris ldm remove-memory command with the -g option for memory operation may fail.</p> <p>This phenomenon may occur when the following conditions are met.</p> <ol style="list-style-type: none"> <li>1) Memory degradation has occurred (*1).</li> </ol> <p>Or</p> <ol style="list-style-type: none"> <li>1) hvdump-reboot is set to "off" (*2).</li> <li>2) The physical partition has been reset due to the XSCF reset(8) command with the xir option or a hardware error.</li> </ol> <p>*1 How to check the memory degradation<br/>primary# <b>ldm list-devices -a -S memory</b><br/>If the STATUS value is "fail" in the output, the memory region has been degraded by SIZE from the PA (physical address) displayed on the same line.</p> <p>*2 How to check hvdump-reboot<br/>primary# <b>ldm list-hvdump</b><br/>hvdump=on<br/>hvdump-reboot=off<br/>In this case, hvdump-reboot is set to "off."</p>                          |
| <b>Workaround</b>  | This has been modified with SRU 11.3.29.5.0.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Bug</b>         | <b>26822709</b><br><b>26934364</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | <p>On the control domain with SRU 11.3.2.4.0 (Oracle VM Server for SPARC 3.3) or later installed, the Oracle Solaris ldm list-rsrc-group command may display memory amounts with incorrect values.</p> <p>This phenomenon may occur when the following conditions are met.</p> <ol style="list-style-type: none"> <li>1) Memory degradation has occurred (*1).</li> </ol> <p>Or</p> <ol style="list-style-type: none"> <li>1) hvdump-reboot is set to "off" (*2).</li> <li>2) The physical partition has been reset due to the XSCF reset(8) command with the xir option or a hardware error.</li> </ol> <p>*1 How to check the memory degradation<br/>primary# <b>ldm list-devices -a -S memory</b><br/>If the STATUS value is "fail" in the output, you can determine that the memory region has been degraded by SIZE from the PA (physical address) displayed on the same line.</p> <p>*2 How to check hvdump-reboot<br/>primary# <b>ldm list-hvdump</b><br/>hvdump=on<br/>hvdump-reboot=off<br/>In this case, hvdump-reboot is set to "off."</p> |
| <b>Workaround</b>  | This has been modified with SRU 11.3.29.5.0.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |

Table 3-46 Problems Resolved in Oracle Solaris 11.4 (*continued*)

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|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>24567071</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>If dynamic PCIe bus assignment and the dynamic reconfiguration function for PCIe end points are executed in the following procedure, only one of the two onboard ixgbe ports of the SPARC M12 is recognized.</p> <ol style="list-style-type: none"> <li>1. Assign the PCIe bus that is connected to the two onboard ixgbe ports to the root domain by using the dynamic PCIe bus assignment function.</li> <li>2. Delete the two onboard ixgbe ports under the PCIe bus assigned in step 1 from the root domain by using the dynamic reconfiguration function for PCIe end points.</li> <li>3. Assign the two ixgbe ports deleted in step 2 to an I/O domain by using the dynamic reconfiguration function for PCIe end points.</li> <li>4. Delete the two ixgbe ports added in step 3 from the I/O domain by using the dynamic reconfiguration function for PCIe end points.</li> <li>5. Return the two ixgbe ports deleted in step 4 to the root domain by using the dynamic reconfiguration function for PCIe end points.</li> </ol> |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.3.17.5.0. Apply SRU 11.3.17.5.0 or later to the control and root domains.</p> <p>[How to restore]</p> <p>Using the dynamic PCIe bus assignment function, delete the PCIe bus from the root domain and then assign the PCIe bus to the root domain again.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |

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|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |           |          |  |  |    |      |           |       |               |     |   |          |               |       |   |          |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------|----------|--|--|----|------|-----------|-------|---------------|-----|---|----------|---------------|-------|---|----------|
| <b>Bug</b>         | <b>24754492</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |           |          |  |  |    |      |           |       |               |     |   |          |               |       |   |          |
| <b>Model</b>       | <b>SPARC M12-2, SPARC M12-2S, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |           |          |  |  |    |      |           |       |               |     |   |          |               |       |   |          |
| <b>Description</b> | <p>The logical domain panic may occur or the ldm daemon may generate a core dump when you execute the ldm shrink-socket command to remove memory from logical domains if both of the following conditions are met.</p> <ul style="list-style-type: none"> <li>- Oracle Solaris is running in logical domains.</li> <li>- Multiple memory blocks on the same CPU socket are allocated to logical domains.</li> </ul> <p>You can check memory allocation of each CPU socket's memory with the ldm list-socket command.</p> <p>In the following example, two memory blocks on CPU socket #0 are allocated to logical domain domain-A.</p> <pre># ldm list-socket -o memory domain-A</pre> <table border="0" style="margin-left: 40px;"> <tr> <td colspan="4">MEMORY</td> </tr> <tr> <td style="padding-right: 20px;">PA</td> <td style="padding-right: 20px;">SIZE</td> <td style="padding-right: 20px;">SOCKET_ID</td> <td>BOUND</td> </tr> <tr> <td>0x7e045000000</td> <td>16G</td> <td>0</td> <td>domain-A</td> </tr> <tr> <td>0x7e08d000000</td> <td>1536M</td> <td>0</td> <td>domain-A</td> </tr> </table> | MEMORY    |          |  |  | PA | SIZE | SOCKET_ID | BOUND | 0x7e045000000 | 16G | 0 | domain-A | 0x7e08d000000 | 1536M | 0 | domain-A |
| MEMORY             |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |           |          |  |  |    |      |           |       |               |     |   |          |               |       |   |          |
| PA                 | SIZE                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | SOCKET_ID | BOUND    |  |  |    |      |           |       |               |     |   |          |               |       |   |          |
| 0x7e045000000      | 16G                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          | 0         | domain-A |  |  |    |      |           |       |               |     |   |          |               |       |   |          |
| 0x7e08d000000      | 1536M                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 0         | domain-A |  |  |    |      |           |       |               |     |   |          |               |       |   |          |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.3.14.6.0.</p> <p>Execute the ldm remove-memory command in place of the ldm shrink-socket command to remove memory from logical domains.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |           |          |  |  |    |      |           |       |               |     |   |          |               |       |   |          |

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Table 3-46 Problems Resolved in Oracle Solaris 11.4 (*continued*)

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|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>24973255</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M12-2, SPARC M12-2S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>Even when the following settings are implemented for Oracle Solaris kernel zones, warm/live migration of the Oracle Solaris kernel zone that is set with <code>cpu-arch=sparc64-class1</code> from a SPARC M12 system to a SPARC M10 system may fail.</p> <pre> /etc/system file ----- set enable_1ghz_stick = 1 set uhrt_enable=0x0 ----- </pre> <p>For details on settings in the <code>/etc/system</code> file, see "8.2.3 Notes on Oracle Solaris Kernel Zones" in the <i>Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide</i>.</p> <p>This bug occurs when the OS of the Oracle Solaris kernel zone is Oracle Solaris 11.3 SRU 11.3.15.4.0 or Oracle Solaris 11.3 SRU 11.3.16.3.0.</p> |
| <b>Workaround</b>  | This has been modified with SRU 11.3.17.5.0. Apply SRU 11.3.17.5.0 to the Oracle Solaris kernel zone for which a warm/live migration is performed.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Bug</b>         | <b>21654442</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>If the Virtual SCSI Host Bus Adapter function is used, the following warning message may be output for the root domain or an I/O domain.</p> <p>[Example]</p> <pre> vsan: WARNING: vsan_walk_match_func: iport-dip(40010ef2fd8): dip(40010ef29b8): prop(class) not found </pre>                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.2.11.5.0.</p> <p>This message has no effect on the functioning of the system, ignore it.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |

Table 3-46 Problems Resolved in Oracle Solaris 11.4 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>21779989</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>If you execute the <code>ldm list-hba</code> command supported in Oracle VM Server for SPARC 3.3 in a system with the PCI expansion unit connected, the device alias of SLOT4 and later of the PCI expansion unit is displayed by mistake.</p> <p>[Example]</p> <pre># ldm list-hba -l -t -d primary NAME                               VSAN ----                               ---- /SYS/MBU/SASHBA/HBA0/PORTf [/pci@8000/pci@4/pci@0/pci@0/scsi@0/iprota@f] : /SYS/PCI1/SLOT2/HBA0/PORT0/0/f       SLOT4 is correct. [/pci@8100/pci@4/pci@0/pci@1/pci@0/pci@0/pci@0/pci@1/pci@0/pci@10/pci@0/pci@0/LSI,sas@0/iprota@f]       init-port w500605b0045c8a90       Transport Protocol SAS [/pci@8100/pci@4/pci@0/pci@1/pci@0/pci@0/pci@0/pci@1/pci@0/pci@10/pci@0/pci@0/LSI,sas@0/iprota@f/smp@w50080e52b93fd03f       tport w50080e52b93fd03f c31t50000394281BBA1Ad0s0       tport w50000394281bba1a       lun 0 [/pci@8100/pci@4/pci@0/pci@1/pci@0/pci@0/pci@0/pci@1/pci@0/pci@10/pci@0/pci@0/LSI,sas@0/iprota@f/enclosure@w50080e52b93fd03d,0       tport w50080e52b93fd03d       lun 0 : </pre> |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.2.11.5.0.</p> <p>This problem may occur even after SRU 11.3.4.5.0 is applied. In such a case, follow the procedure in "10.12 Saving/Restoring Logical Domain Configuration Information in an XML File" in the <i>Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 System Operation and Administration Guide</i> to save and then restore the logical domain configuration information.</p> <p>[Alternative method]</p> <p>If you execute vHBA related commands such as <code>ldm add-vsan</code>, specify the device path instead of the device alias.</p> <p>[Example]</p> <pre># ldm add-vsan /pci@8100/pci@4/pci@0/pci@1/pci@0/pci@0/pci@0/pci@1/pci@0/pci@10/pci@0/pci@0/LSI,sas@0/iprota@f vsan0 ldom1</pre>                                                                                                                                                                                                                                                                                                                                                   |

Table 3-46 Problems Resolved in Oracle Solaris 11.4 (*continued*)

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>20646928</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>If you perform dynamic deletion with the dynamic reconfiguration function of the physical partition for a system board with the system volume located in the following environment, system board deletion processing may fail.</p> <p>[Problem occurrence conditions]</p> <ol style="list-style-type: none"> <li>1) A ZFS mirror of the system volume has been created using multiple disks, and</li> <li>2) the mirroring disk and mirrored disk are located on different system boards, and</li> <li>3) the system board to which the system volume is assigned has been dynamically deleted.</li> </ol> <p>[Example of output message]</p> <pre>XSCF&gt; deleteboard -c disconnect -m unbind=resource 00-0 PSB#00-0 will be unconfigured from PPAR immediately. Continue?[y n] :y All domains are temporarily suspended, proceed?[y n] :y Start unconfigure preparation of PSB. [1200sec] 0end Unconfigure preparation of PSB has completed. Start unconfiguring PSB from PPAR. [7200sec] 0....\ The removal of PCIE0 from the domain primary failed. Error message from svc:/ldoms/agents in domain primary: ERROR: devices or resources are busy. end PSB#00-0 could not be unconfigured from PPAR-ID 0 due to operating system or Logical Domains Manager error.</pre> |
| <b>Workaround</b>  | This has been modified with SRU 11.3.5.6.0.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Bug</b>         | <b>25976819</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | In an environment with Oracle VM Server for SPARC 3.3 or later installed on the control domain, a logical domain may not recognize the replacement CPU after a faulty CPU was automatically replaced.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.3.23.5.0.</p> <p>[How to restore]</p> <p>Reboot the logical domain where the problem occurred.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |

Table 3-46 Problems Resolved in Oracle Solaris 11.4 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>26052693</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M12-2S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | <p>In an environment with Oracle VM Server for SPARC 3.4.0.3 (SRU 11.3.18.6.0) or later installed on the control domain, if you reboot a logical domain after dynamically reconfiguring physical partitions with the XSCF deleteboard(8) command, then a panic on the logical domain may persist.</p> <p>[Panic message example]<br/> panic[cpu0]/thread=xxxxxxx: tilelet_assign_cb: assigning pfns [4a0000, 4a8000) to mgid 39, mnnodeid 57: pachunk 12 already assigned to mgid 3b, mnnodeid 59</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.3.23.5.0.</p> <p>[How to restore]<br/> Take action as follows for each logical domain where a panic occurred.</p> <ul style="list-style-type: none"> <li>- If a panic occurs on the control domain <ol style="list-style-type: none"> <li>1. Stop the physical partition.</li> <li>2. With the addboard(8) command of the XSCF, return the physical partition to the state before the reconfiguration.</li> <li>3. Start the physical partition again.</li> </ol> <p>[Example]<br/> XSCF&gt; <b>poweroff -y -p 0</b><br/> XSCF&gt; <b>addboard -y -c configure -m bind=none -p 0 01-0</b><br/> XSCF&gt; <b>poweron -y -p 0</b></p> </li> <li>- If a panic occurs on a guest domain<br/> Unbind the resource, and bind the resource again.</li> </ul> <p>[Example]<br/> primary# <b>ldm stop-domain guest</b><br/> primary# <b>ldm unbind-domain guest</b><br/> primary# <b>ldm bind-domain guest</b><br/> primary# <b>ldm start-domain guest</b></p> |
| <b>Bug</b>         | <b>26176351</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>On a logical domain with SRU 11.3.23.5.0 or later installed, even when the power-saving operation of the physical partition is set to "elastic", the CPU frequency may not decrease. This is because Solaris Power Aware Dispatcher (PAD) does not work for the CPU that the logical domain uses.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.3.23.5.0.</p> <p>Install SRU 11.3.23.5.0 or later on the control domain.</p> <p>[How to restore]<br/> After starting all of the logical domains on which SRU 11.3.23.5.0 or later is installed, set the power-saving operation to "disabled" with the XSCF setpparmode(8) command. Then, set the power-saving operation to "elastic" again.</p> <p>[Example]<br/> XSCF&gt; <b>setpparmode -p 0 -y -m powermgmt_policy=disabled</b><br/> XSCF&gt; <b>setpparmode -p 0 -y -m powermgmt_policy=elastic</b></p>                                                                                                                                                                                                                                                                                                                                                                                                                                          |

Table 3-46 Problems Resolved in Oracle Solaris 11.4 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>27112303</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | <p>In an environment with Oracle VM Server for SPARC 3.1.0.1 or later installed on the control domain, the power-saving operation of the physical partition does not work as "elastic" if the following conditions are met:</p> <ol style="list-style-type: none"> <li>1) the power-saving operation of the physical partition is set to "elastic"; and</li> <li>2) the physical partition is operating; and</li> <li>3) the physical partitions have been dynamically reconfigured with the XSCF deleteboard(8) command or addboard(8) command.</li> </ol> <p>Or</p> <ol style="list-style-type: none"> <li>3) the number of CPU Activations has been changed with the XSCF setcod(8) command.</li> </ol> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>[How to restore]</p> <p>Set the power-saving operation to "disabled" with the XSCF setpparmode(8) command and then set the power-saving operation to "elastic" again.</p> <p>[Example]</p> <p>- SPARC M12 system</p> <pre>XSCF&gt; setpparmode -p 0 -y -m powermgmt_policy=disabled XSCF&gt; setpparmode -p 0 -y -m powermgmt_policy=elastic</pre> <p>- SPARC M10 system</p> <pre>XSCF&gt; setpparmode -p 0 -y -m elastic=off XSCF&gt; setpparmode -p 0 -y -m elastic=on</pre>                                                                                                                                                                                 |

## Problems Resolved in Oracle Solaris 11.3

The following table lists the problems resolved in Oracle Solaris 11.3. You might encounter them in supported releases earlier than Oracle Solaris 11.3.

Table 3-47 Problems Resolved in Oracle Solaris 11.3

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>15813959</b><br><b>15813960</b><br><b>(7196117)</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | A PCI expansion unit is added using Oracle Solaris hotplug(1M) in a SPARC M12/M10 system. However, devices on the PCI expansion unit are not recognized.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | <p>Before you add a PCI expansion unit by hotplug(1M), add the following line in the /etc/system file in advance and restart Oracle Solaris.</p> <pre>set pcicfg:pcicfg_slot_busnums = 4</pre> <p>Note that the system does not recognize a device of a PCI expansion unit if you add the PCI expansion unit by PHP to a root complex that has been added by either of the following: the dynamic reconfiguration of the physical partition, or the Dynamic PCIe bus assignment. If this problem occurs, restart the logical domain to which the PCI expansion unit is assigned to make the system recognize the device of the PCI expansion unit.</p> |
| <b>Bug</b>         | <b>17430911</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |

Table 3-47 Problems Resolved in Oracle Solaris 11.3 (*continued*)

**Description** When you change the power-saving operation of the physical partition from "elastic" to "disabled," the CPU frequency assigned to the logical domain may not increase.

**Workaround** This has been modified with SRU 11.2.8.4.0 (Oracle VM Server for SPARC 3.2). Install SRU 11.2.8.4.0 or later on the control domain.  
[How to restore]  
Execute the Oracle Solaris `svcadm restart ldmd` command in the control domain to restart the `ldmd` services.  
primary# **svcadm restart ldmd**

**Bug** **17561541**

**Model** **SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S**

**Description** In a SPARC M10 environment with XCP 2230 or later applied, or in a SPARC M12 environment with XCP 3021 or later applied, suppose that the `ldm add-io` command is executed after the `ldm remove-io` command was executed during delayed reconfiguration. Then, the `ldmd` daemon may cause a core dump and restart.

**Workaround** This has been modified with SRU 11.2.8.4.0 and Oracle VM Server for SPARC 3.2 for Oracle Solaris 10. During delayed reconfiguration, execute the `ldm remove-io` command after executing the `ldm add-io` command.

**Bug** **18502702**

**Model** **SPARC M10-1, SPARC M10-4, SPARC M10-4S**

**Description** If the SunVTS 7.0 `ps17.1` test is started on a SPARC M10 system with SPARC64 X+ processors, it may end with an error.

**Workaround** This has been modified with SRU 11.2.1.5.0, and patch 151265-03 for Oracle Solaris 10.

**Bug** **18595023**

**Model** **SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S**

**Description** If the `ldm list-io` command is executed after PCI cards, which support the SR-IOV function, are mounted in PCI expansion unit SLOT4 or higher, the pseudonym of the physical function of the PCI cards mounted in SLOT4 or higher is mistakenly shown as SLOT2. Moreover, the virtual functions created from the physical functions of the PCI cards that are mounted on SLOT4 or higher cannot be assigned to logical domains.  
[Example of command output]  
# **ldm ls-io -l**

```

NAME TYPE BUS DOMAIN STATUS
---- -
...
/SYS/PCI1/SLOT5 PCIE PCIE1 primary OCC [pci@8100/pci@4/pci@0/pci@1/pci@0/pci@0/pci@0/pci@1/pci@0/pci@10/pci@0/pci@1]
network@0
network@0,1
...
/SYS/PCI1/SLOT2/IOVNET.PF0 PF PCIE1 primary
[pci@8100/pci@4/pci@0/pci@1/pci@0/pci@0/pci@0/pci@1/pci@0/pci@10/pci@0/pci@1/network@0]
maxvfs = 7
...

```

**Workaround** This has been modified with SRU 11.2.2.5.0, and patch 150817-03 for Oracle Solaris 10.

Table 3-47 Problems Resolved in Oracle Solaris 11.3 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>18615814</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | <p>An I/O domain may output the following message, and Oracle Solaris panic may occur if a system board is deleted by executing dynamic reconfiguration of physical partitions (PPAR DR) or a PCIe end point device is dynamically removed from the I/O domain by executing the <code>ldm remove-io</code> command.</p> <pre>panic[cpuX]/thread=XXXXXXXXXXXX: mutex_exit: not owner, lp=XXXXXXXX owner=X thread=XXXXXXXXXXXX</pre>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.2.8.4.0.</p> <p>Execute the <code>svcadm(1M)</code> command on the I/O domain to disable the <code>intrd(1M)</code> service before deleting the system board by executing dynamic reconfiguration of physical partitions (PPAR DR) or before removing the PCIe end point device from the I/O domain.</p> <pre># svcadm disable intrd</pre> <p>Enable the <code>intrd(1M)</code> service after the process of the <code>ldm remove-io</code> command is completed.</p> <pre># svcadm enable intrd</pre>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Bug</b>         | <b>18665751</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | <p>When using XCP 2210, the Dynamic Resource Management (DRM) feature of Oracle VM Server for SPARC does not work.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.2.8.4.0 and Oracle VM Server for SPARC 3.2 for Oracle Solaris 10.</p> <p>Update the XCP firmware to XCP 2220 or later.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Bug</b>         | <b>18747641</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | <p>Core dumps may be produced or wrong calculation results may be obtained or a panic may occur when a program, which performs double-precision floating point instructions after enabling SPARC64 X/SPARC64 X+ processor-specific options and 4-byte boundary alignment (*1) and compiled with Oracle Solaris Studio compiler version 12.3 2013/06/17 or newer, is executed on a SPARC M10 system with Oracle Solaris 11.1 or newer.</p> <p>*1 The 4-byte boundary alignment is enabled by default when creating 64-bit programs.</p> <p>In the case of 32-bit programs, it is enabled if "<code>-xmalign=N</code>s (N=1,2,4,8,16)" or "<code>-fast</code>" is not specified.</p> <p>[Procedure of checking compiler version]<br/> The "<code>-V</code>" option shows version information. The date is output at the end of version notation. The compiler version that corresponds to this bug is 2013/06/17 or newer.</p> <pre>\$ cc -V cc: Sun C 5.12 SunOS_sparc Patch 148917-06 2013/06/17 \$ f95 -V (f90 and f77 are also same.) f95: Sun Fortran 95 8.6 SunOS_sparc Patch 148517-05 2013/06/17 \$ CC -V CC: Sun C++ 5.12 SunOS_sparc Patch 148506-11 2013/06/17</pre> |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.2.4.6.0.</p> <p>Recompile the program with the following "<code>-xarch</code>" flag.</p> <pre>-xarch=sparcima</pre>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |

Table 3-47 Problems Resolved in Oracle Solaris 11.3 (*continued*)

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|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>19074260</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | <p>The following messages may be output in the log of ldoms/ldmd services (/var/svc/log/ldomslmd:default.log), and the communication between the ldmd daemon and the XSCF may be disconnected during or after physical partition dynamic reconfiguration (PPAR DR).</p> <p>[Example of message]</p> <p>Sep 18 13:31:37 warning: Device busy: open_ldc_channel: Open of/devices/virtual-devices@100/channel-devices@200/virtual-channel@3:spds failed</p> <p>After that time, processes that need to communicate with the XSCF, such as PPAR DR and the ldm list-sconfig command, will fail.</p> |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.2.8.4.0.</p> <p>[How to restore]</p> <p>Execute Oracle Solaris svcadm(1M) command to restart the ldoms/ldmd services.</p> <pre># svcadm restart ldmd</pre>                                                                                                                                                                                                                                                                                                                                                                                                |

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|                    |                                                                                                                                                                                                                                   |
|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>19310540</b>                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                               |
| <b>Description</b> | <p>If the addboard(8) command is executed in the "factory-default" configuration, CPU cores may not be assigned to the control domain.</p>                                                                                        |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.2.8.4.0 and Oracle VM Server for SPARC 3.2 for Oracle Solaris 10.</p> <p>Add the CPU cores or threads which were not added, using the ldm add-core command or the ldm add-vcpu command.</p> |

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|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>19310550</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>On a physical partition, to which 8 or more system boards have been assigned, when collecting dump files of the hypervisor which is executed as the ldoms/ldmd service is started, the following console messages is output by the ldoms/ldmd service, and it may fall back to maintenance mode.</p> <p>[Example of message]</p> <p>Feb 28 16:19:39 svc.startd[11]: ldoms/ldmd:default failed: transitioned to maintenance (see 'svcs -xv' for details)</p>                                  |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.2.8.4.0 and Oracle VM Server for SPARC 3.2 for Oracle Solaris 10.</p> <p>[How to restore]</p> <p>Use the following process to change the timeout value of starting the ldoms/ldmd service to 600.</p> <pre># svccfg -s ldmd listprop : start/timeout_seconds count <u>180</u> : # svccfg -s ldmd setprop start/timeout_seconds=600 # svccfg -s ldmd listprop : start/timeout_seconds count <u>600</u> : # svcadm refresh ldmd # svcadm restart ldmd</pre> |

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Table 3-47 Problems Resolved in Oracle Solaris 11.3 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>19358400</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | If the root complex is dynamically added/deleted, the PCIe end point device configuration information shown by the <code>showpparinfo(8)</code> command will not reflect the PCIe end point device under the added/deleted root complex.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | This has been modified with SRU 11.2.9.5.0.<br>[How to restore]<br>By restarting the logical domain that added/deleted the root complex dynamically, the <code>showpparinfo(8)</code> command displays the correct configuration information.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Bug</b>         | <b>19424242</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | On a system to which Oracle VM Server for SPARC 3.1.0.1 or later is applied, the following event may occur: If all CPUs or memory in an I/O domain are degraded due to a CPU or memory failure, the <code>ldmd</code> service abnormally terminates, and, as a result, the <code>ldm(1M)</code> command terminates with an error.                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Workaround</b>  | This has been modified with SRU 11.2.8.4.0 and Oracle VM Server for SPARC 3.2 for Oracle Solaris 10.<br>[How to restore]<br>Replace the faulty CPU or memory.<br>If you want to boot Oracle Solaris while leaving the faulty CPU or memory installed, perform the following procedure on the XSCF:<br>1. Power off the physical partition (PPAR) by the <code>poweroff(8)</code> command.<br>2. Execute the <code>setdomainconfig(8)</code> command to place the PPAR in the factory-default state.<br><code>XSCF&gt; setdomainconfig -p ppar_id -c default</code><br>3. Execute the <code>poweron(8)</code> command to activate the PPAR.<br>Oracle Solaris reboots in a configuration that includes only the control domain (factory-default). |
| <b>Bug</b>         | <b>19424359</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | If the domain configuration is restored in the degraded configuration, both of the following settings are reset to their default values: the setting specifying whether to enable/disable hypervisor dump collection and the setting specifying whether to enable/disable automatic reboot during hypervisor dump collection.<br>[Default values]<br>Hypervisor dump collection: Enabled<br>Automatic reboot during hypervisor dump collection: Disabled                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | This has been modified with SRU 11.2.8.4.0 and Oracle VM Server for SPARC 3.2 for Oracle Solaris 10.<br>[How to restore]<br>After executing Oracle VM Server for SPARC <code>ldm(1M)</code> command to change the hypervisor dump setting, save the domain configuration information.<br><code># ldm set-hvdump hvdump=XXXX hvdump-reboot=YYYY</code><br><code># ldm add-spconfig ZZZZ</code><br>After replacing the faulty component, execute the <code>setdomainconfig(8)</code> command to initiate a reboot with the original domain configuration.                                                                                                                                                                                          |

Table 3-47 Problems Resolved in Oracle Solaris 11.3 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>19513561</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | Oracle VM Server for SPARC ldm daemon may repeat core dump if suspend process of the appropriate domain fails during live migration.                                                                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | This has been modified with SRU 11.2.8.4.0 and Oracle VM Server for SPARC 3.2 for Oracle Solaris 10.<br>[How to restore]<br>Restart the physical partition according to the following steps.<br>1. Execute the poweroff(8) command to power off the physical partition (PPAR).<br>2. Execute the poweron(8) command to restart PPAR.                                                                                                                           |
| <b>Bug</b>         | <b>19680186<br/>19454809</b>                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | If Oracle Solaris 11.2 and later is running and the system board is deleted by dynamic reconfiguration of physical partitions (PPAR DR), Oracle Solaris may panic.                                                                                                                                                                                                                                                                                             |
| <b>Workaround</b>  | This has been modified with SRU 11.2.10.5.0.<br>Add the following setting to /etc/system of all logical domains, and restart Oracle Solaris:<br><b>set lgrp_topo_levels=1</b><br>Be sure to delete set lgrp_topo_levels=1 in /etc/system before applying SRU 11.2.10.5.0 or later.                                                                                                                                                                             |
| <b>Bug</b>         | <b>19728345</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | The physical partition dynamic reconfiguration (PPAR DR) fails if the ldoms/ldmd services are restarted because of Oracle Solaris panic and the like during PPAR DR.                                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | This has been modified with SRU 11.2.8.4.0 and Oracle VM Server for SPARC 3.2 for Oracle Solaris 10.<br>[How to restore]<br>Hypervisor abort may be caused from the operation of adding/removing memory to/from PPAR DR or a logical domain after the ldoms/ldmd services are recovered. Therefore, execute the poweroff(8) command of the XSCF firmware to power off the physical partition (PPAR), then execute the poweron(8) command to power on the PPAR. |
| <b>Bug</b>         | <b>19913088</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | If a root complex with PCI expansion unit connected is added dynamically to the logical domain with the ldm add-io command, the logical domain may output the following message, causing Oracle Solaris to panic.<br>panic[cpuX]/thread=XXXXXXXXXXXX: bad stack overflow at TL 1                                                                                                                                                                               |
| <b>Workaround</b>  | This has been modified with SRU 11.2.10.5.0.<br>Before adding the root complex to the logical domain dynamically, add the following setting to /etc/system, and then reboot Oracle Solaris.<br>set default_stksize = 0xa000                                                                                                                                                                                                                                    |

Table 3-47 Problems Resolved in Oracle Solaris 11.3 (continued)

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|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>20061005<br/>19200041</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>Description</b> | <p>If you use the ipadm(1M) command or the ifconfig(1M) command on the guest domain that has the physical device after you delete the system board dynamically with the deleteboard(8) command, the guest domain may output the following message, causing Oracle Solaris to panic.</p> <pre>panic[cpuXX]/thread=XXXXXXXXXXXXX: assertion failed: obj-&gt;afo_corep == NULL, file: ../../common/os/numaio.c, line: 724</pre>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.2.10.5.0.</p> <p>If you delete the system board dynamically with the deleteboard(8) command, execute the following command before you execute the ipadm(1M) command or the ifconfig(1M) command on the guest domain.</p> <pre># modunload -i 0</pre>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Bug</b>         | <b>20458698</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Description</b> | <p>No response time from the migration source domain may become long because a different process from the original live migration is performed during live migration. Network services and the like operating on the migration source domain may time out because of no response. This case occurs if the migration source domain meets both of the following conditions.</p> <ul style="list-style-type: none"> <li>- The difference between the maximum RA of the migration source domain (actual address) and its minimum RA cannot be divided by 64 MB</li> <li>- The remainder is 32 MB or less when the difference between the maximum RA of the migration source domain and its minimum RA is divided by 64 MB</li> </ul> <p>The maximum RA and the minimum RA of the domain can be checked with the following command.</p> <pre>[Example] # ldm list-domain -o memory domain-name NAME domain-name MEMORY RA          PA          SIZE 0x10000000 0x7b0fc000000 1G     minimum RA 0x400800000 0x7f01a080000 11G     (a)          (b)</pre> <p>The maximum RA is the sum of (a) + (b), which will be 0x6c0800000.<br/> <math>0x400800000 + 0x2c0000000(11G) = 0x6c0800000</math><br/>     The difference between the maximum RA and the minimum RA is 27400 MB.<br/> <math>0x6c0800000 - 0x10000000 = 0x6b0800000 = 27400 \text{ MB}</math><br/>     Therefore, the remainder is 8 MB in this example.<br/> <math>27400 \text{ MB} / 64 \text{ MB} = 428</math> and the remainder is 8 MB</p> |
| <b>Workaround</b>  | This has been modified with SRU 11.2.11.5.0.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |

Table 3-47 Problems Resolved in Oracle Solaris 11.3 (*continued*)

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|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>20878144</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | <p>For Oracle Solaris 11.2 SRU 11.2.8.4.0 or later, "OS Started. No state support" is displayed by the <code>showdomainstatus(8)</code> command or in the event log when Oracle Solaris starts. This message indicates that the status of a logical domain has changed to Oracle Solaris.</p> <p>The following is an example of the message.</p> <pre>XSCF&gt; showlogs event Date Message --- Omitted --- Mar 27 15:55:31 ** Event: SCF:PPARID 0 GID 00000000 state change (OpenBoot Running) Mar 27 15:55:32 ** Event: SCF:PPARID 0 GID 00000000 state change (OpenBoot Primary Boot Loader) Mar 27 15:55:33 ** Event: SCF:PPARID 0 GID 00000000 state change (OpenBoot Running OS Boot) Mar 27 15:55:35 ** Event: SCF:PPARID 0 GID 00000000 state change (OS Started. No state support) Mar 27 15:55:36 ** Event: SCF:PPARID 0 GID 00000000 state change (OS Started. No state support) Mar 27 15:56:42 ** Event: SCF:PPARID 0 GID 00000000 state change (Solaris booting) Mar 27 15:57:37 ** Event: SCF:PPARID 0 GID 00000000 state change (Solaris booting) Mar 27 15:57:37 ** Event: SCF:PPARID 0 GID 00000000 state change (Solaris running) XSCF&gt; showdomainstatus -p 0 2015-MM-DD hh:mm:ss Logical Domain Name Status primary OS Started. No state support.</pre> |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.2.11.5.0.</p> <p>Ignore this message since it does not affect the system operation.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |

Table 3-47 Problems Resolved in Oracle Solaris 11.3 (*continued*)

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|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>20974426</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | <p>In an environment with Oracle VM Server for SPARC 3.2 applied to the control domain and configuration information already saved in the XSCF, if the SPARC M10 system chassis or physical partition (PPAR) is stopped or started, the SPARC M10 system chassis or PPAR may not be able to start with the saved configuration information.</p> <p>This problem occurs when the configuration information is saved by any of the following means:</p> <ul style="list-style-type: none"> <li>- <code>ldm add-spconfig -r</code> command</li> <li>- Automatic recovery using automatic recovery policy 3 of Oracle VM Server for SPARC <code>ldmd</code> daemon (automatic saving of configuration information)</li> </ul> <p>You can check the automatic recovery policy of the <code>ldmd</code> daemon with the following command. The default for the automatic recovery policy is 1 (display warning messages in log files)</p> <p>[Example]</p> <pre># svccfg -s ldmd listprop ldmd/autorecovery_policy ldmd/autorecovery_policy integer 3</pre> |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.2.11.5.0.</p> <p>[How to restore]</p> <ul style="list-style-type: none"> <li>- If the <code>ldm add-spconfig -r</code> command was executed, delete the saved configuration information, and overwrite it by saving the current configuration.</li> </ul> <p>[Example]</p> <pre># ldm remove-spconfig CONF-A # ldm add-spconfig CONF-A</pre> <ul style="list-style-type: none"> <li>- If the automatic recovery policy is set to 3, change the automatic recovery policy to 1 by performing the following procedure.</li> </ul> <p>[Example]</p> <pre># svccfg -s ldmd setprop ldmd/autorecovery_policy=1 # svcadm refresh ldmd</pre> <p>If the SPARC M10 system chassis or PPAR cannot start with the saved configuration information, start the system in the factory-default configuration, and then restore the configuration information already saved in the XML file.</p>                                                                                                                                |
| <b>Bug</b>         | <b>21106074</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Description</b> | <p>If any of <code>aes-128-ccm</code>, <code>aes-192-ccm</code>, and <code>aes-256-ccm</code> is specified for the encryption algorithm, accessing the ZFS data set with encryption enabled may cause a system panic. The default encryption algorithm is <code>aes-128-ccm</code>. If ZFS is encrypted with no encryption algorithm specified, <code>aes-128-ccm</code> is assumed specified.</p> <p>[Panic message example]</p> <pre>panic[cpu34]/thread=2a1053d9c20: bad floating point trap at TL 1 %tl %tpc %tnpc %tstate %tt 1 00000000123eabc0 00000000123eabc4 8880001600 077 %gl: 00 %ccr: 88 %asi: 80 %cwp: 0 %pstate: 16 (Omitted)</pre>                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.2.12.5.0.</p> <p>Add the following statements to the <code>/etc/system</code> file, and reboot.</p> <pre>set auxv_cap_exclude_hw1=0x10000 set auxv_cap32_exclude_hw1=0x10000</pre>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |

Table 3-47 Problems Resolved in Oracle Solaris 11.3 (*continued*)

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|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>21306352</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>The physical partition dynamic reconfiguration (PPAR DR) feature may fail if used to delete a system board in an environment containing a root domain (not a control domain) running Oracle Solaris 11.2 SRU 11.2.9.5.0 or later.</p> <p>[Example]<br/> XSCF&gt; <b>deleteboard -y -c disconnect -m unbind=resource 01-0</b><br/> PSB#01-0 will be unconfigured from PPAR immediately. Continue?[y n] :<b>y</b><br/> Start unconfigure preparation of PSB. [1200sec]<br/> 0.end<br/> Unconfigure preparation of PSB has completed.<br/> Start unconfiguring PSB from PPAR. [7200sec]<br/> 0..... 30..... 60..... 90.....-<br/> end<br/> Timeout detected during communicate with Logical Domains Manager.<br/> XSCF&gt;</p> |
| <b>Workaround</b>  | <p>You can avoid this problem by deleting the PCIe bus on the target system board from the domain before the PPAR DR feature deletes the system board.</p> <p>[Example]<br/> primary# <b>ldm remove-io PCIE8 domainX</b><br/> :<br/> primary# <b>ldm remove-io PCIE15 domainY</b><br/> XSCF&gt; <b>deleteboard -y -c disconnect -m unbind=resource 01-0</b><br/> [How to restore]<br/> After deleting the PCIe bus on the target system board from the domain, re-execute the deleteboard command on the XSCF.</p> <p>[Example]<br/> primary# <b>ldm remove-io PCIE8 domainX</b><br/> :<br/> primary# <b>ldm remove-io PCIE15 domainY</b><br/> XSCF&gt; <b>deleteboard -y -c disconnect -m unbind=resource 01-0</b></p>        |

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## Problems Resolved in Oracle Solaris 11.2

The following table lists the problems resolved in Oracle Solaris 11.2. You might encounter them in supported releases earlier than Oracle Solaris 11.2.

Table 3-48 Problems Resolved in Oracle Solaris 11.2

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|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>15812880</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | <p>If you try to access, via telnet or ssh, a domain where 8000 GB (about 7.8 TB) or greater memory is mounted, the following message appears on the destination control domain console and the access fails.</p> <ul style="list-style-type: none"> <li>- For ssh</li> </ul> <pre>error: /dev/ptmx: Not enough space error: session_pty_req: session 0 alloc failed</pre> <ul style="list-style-type: none"> <li>- For telnet</li> </ul> <pre>telnetd: open /dev/ptmx: Not enough space</pre> |
| <b>Workaround</b>  | <p>This has been modified with Oracle Solaris 11.1 SRU 3.5.1, and patch 148888-04 for Oracle Solaris 10.</p> <p>Execute the following command to change ptmx_ptymax:</p> <p>[Example]</p> <pre># echo "ptms_ptymax/Z 0x400000"   mdb -kw ptms_ptymax: 0 = 0x400000</pre>                                                                                                                                                                                                                       |

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|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>15822113</b>                                                                                                                                                                                                                                         |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                   |
| <b>Description</b> | <p>If ldm add-vcpu and ldm remove-vcpu are repeatedly executed in a shell script, the process that is being run may cause a core dump and abnormally terminate.</p>                                                                                     |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.1.7.5.0.</p> <p>If this defect occurs because SRU is not applied, execute the command again.</p> <p>In addition, when executing ldm remove-vcpu, execute it in such a condition that the process load is low.</p> |

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Table 3-48 Problems Resolved in Oracle Solaris 11.2 (*continued*)

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|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>15823255</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | <p>An Oracle Solaris panic may occur if the CPU allocation is changed using Oracle Solaris <code>psradm(1M)</code> command or <code>psrset(1M)</code> command or the configuration of a virtual CPU is dynamically changed using Oracle VM Server for SPARC <code>ldm</code> command under the environment that meets the following two conditions.</p> <ul style="list-style-type: none"> <li>- The environment where the physical partition (PPAR) is composed of two or more SPARC M10-4S chassis.</li> <li>- The environment where the following <code>lgroup</code> exists when the <code>lgrpinfo</code> command is executed on the control domain or logical domain.</li> </ul> <p>Among the <code>lgroups</code> that are displayed as "lgroup XX (intermediate):", only one number is displayed before (CPU) of the "Lgroup resources:" field. This number is not displayed before (memory).</p> <p>[Example]</p> <pre># /usr/bin/lgrpinfo ... lgroup 12 (intermediate):     Children: 10, Parent: 0     CPUs: 0 1     Memory: installed 520M, allocated 494M, free 26M     Lgroup resources: 1 (CPU); 10 11 (memory)     Latency: 21 ...</pre> |
| <b>Workaround</b>  | <p>This has been modified with Oracle Solaris 11.1 SRU 5.5, and patch 150400-01 for Oracle Solaris 10.</p> <p>Add the following line to <code>/etc/system</code> and restart Oracle Solaris:</p> <pre>set lgrp_topo_levels=2</pre>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Bug</b>         | <b>15825208</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>In SPARC M10 Systems, the <code>scp(1)</code>, <code>sftp(1)</code>, and <code>ssh(1)</code> commands of Oracle Solaris may generate an error or installation of Oracle RAC may fail.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Workaround</b>  | <p>This has been modified with Oracle Solaris 11.1 SRU 1.4.</p> <p>See "An error occurs in executing the <code>scp(1)</code>, <code>sftp(1)</code>, or <code>ssh(1)</code> command of Oracle Solaris or the installation of Oracle RAC fails (CR:15825208)."</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Bug</b>         | <b>15826052</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | <p>You cannot use the PCI hot plug (PHP) function to add a Quad Gigabit Ethernet card (SE1X7GQ2F) to a PCI Express slot of a PCI expansion unit.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | <p>This has been modified with XCP 2050 and SRU 11.1.6.4.0.</p> <p>For XCP 2050, see the description of RTIF2-130528-001.</p> <p>If XCP and SRU are not applied, stop the logical domain to which you want add the PCI card before adding it.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |

Table 3-48 Problems Resolved in Oracle Solaris 11.2 (*continued*)

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|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>15840018</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | After the firmware is updated to XCP 2031 or later on the SPARC M10 or to XCP 3021 or later on the SPARC M12, the following message appears at Oracle Solaris startup.<br>NOTICE: skipping unsupported token: fforclnum                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Workaround</b>  | This has been modified with SRU 11.1.6.4.0, and patch 148888-03 for Oracle Solaris 10.<br>Ignoring this message does not have an impact on the system.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Bug</b>         | <b>15851224</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | When starting the I/O domain, the following message may be output and the panic may persist.<br>recursive rw_enter, lp=XXXXXXXXX wwwh=XXXXXXXXX thread=XXXXXXXXX                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Workaround</b>  | This has been modified with SRU 11.1.12.5.0, and patch 150840-01 for Oracle Solaris 10.<br>Add the following to /etc/system of the I/O domain and restart Oracle Solaris:<br>forceload: drv/vpci<br><br>Note that if starting of the I/O domain is disabled, you must change to inactive state the guest domain to which the virtual disk (vdisk), which is a target of the virtual disk service (vds) of the I/O domain, is assigned, with the ldm stop-domain and ldm unbind-domain commands. This will enable starting of the I/O domain. After starting the I/O domain, set the above.                                                                                                                                                                         |
| <b>Bug</b>         | <b>15851441</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | When the memory fails and is degraded, the following message may be output at Oracle Solaris startup, and the panic may persist:<br>tilelet_assign_fini_cb(): tile 0xX in memgrp X was unused<br><br>This may also occur when you set the mirror mode for the memory after saving the logical domain setting with ldm add-spconfig.                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Workaround</b>  | This has been modified with SRU 11.1.11.4.0.<br>See "If Oracle Solaris is activated during memory degradation, a panic may occur (CR:15851441)."                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Bug</b>         | <b>15858713</b><br><b>16769782</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Description</b> | If memory fault occurs and all memories allocated to guest domains are degraded, Oracle VM Server for SPARC ldmd daemon generates a core dump and terminates abnormally, and the ldm(1M) command ends with an error.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | This has been modified with SRU 11.1.10.5.0. On Oracle Solaris 10, it has been modified with Oracle VM Server for SPARC 3.1.<br>If this defect occurs because SRU is not applied, replace the memory having an error.<br>If you want to start Oracle Solaris while the memory with an error remains mounted, start it using the following procedure from the XSCF.<br>1. Execute the poweroff(8) command to power off the physical partition (PPAR).<br>2. Execute the setdomainconfig(8) command to place the PPAR in the factory-default state.<br>XSCF> <b>setdomainconfig -p ppar_id -c default</b><br>3. Execute the poweron(8) command to restart PPAR.<br>Oracle Solaris reboots in a configuration that includes only the control domain (factorydefault). |

Table 3-48 Problems Resolved in Oracle Solaris 11.2 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>15887244</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | When you start the SunVTS 7.0 ps14 and ps15 tests in a SPARC M10 system, they may terminate with an error.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Workaround</b>  | This has been modified with Oracle Solaris 11.1 SRU 4.6, and patch 149395-02 for Oracle Solaris 10.<br>There is no workaround other than applying the modification.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Bug</b>         | <b>16238762</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | If a system board is added to the PPAR by dynamic reconfiguration of physical partitions after booting the Oracle Solaris in the control domain with the factory-default configuration, or if CPUs on the system board are added to a domain by the <code>ldm add-vcpu</code> command after the system board is added by dynamic reconfiguration of physical partitions, the system will panic with the following message:<br>panic[cpuX]/thread=XXXXXXXXXX: mpo_cpu_add: Cannot read MD                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | This has been modified with SRU 11.1.7.5.0, and patch 150400-12 for Oracle Solaris 10. Meanwhile, when this fix has been applied to the system, if a system board is added to the system by means of the dynamic reconfiguration of physical partitions, the following message may be output but as it has no effect on the system, ignore it.<br>WARNING: mpo_cpu_add: defaulting to lgroup x for CPU x<br>For the control domain or the domain whose CPU is added by the <code>ldm add-vcpu</code> command, add the following line to the domain's <code>/etc/system</code> file and reboot the Oracle Solaris.<br><code>set suspend_count = 1</code>                                                                  |
| <b>Bug</b>         | <b>16292272</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | If you configure many guest domains in a system where 16 BBs compose one physical partition (PPAR), it takes time to perform binding for the guest domains.<br>The time taken by new binding processing for each guest domain is approximately (number of guest domains that are already bound + 1) x 6 + 10 seconds.<br>Therefore, if there are no domains for which binding has been performed, and binding is performed for the guest domains one by one through the <code>ldm bind-domain</code> command, the required time is obtained by summing the times taken to perform binding for all of them.                                                                                                               |
| <b>Workaround</b>  | This has been improved with SRU 11.1.16.4.0, and patch 150011-03 for Oracle Solaris 10. We recommend that you do not configure the system with a single PPAR but divide it into multiple PPARs and then configure guest domains in each PPAR.<br>Using the recommended configuration described above, not only mitigates the described phenomenon but also helps improve fault tolerance.<br>If a virtual network switch (vsw) is configured, you can reduce the time for binding in half by setting <code>inter-vnet-link</code> to off. For notes when <code>inter-vnet-link</code> is set to off, see the <i>Oracle VM Server for SPARC Administration Guide</i> or <i>Oracle VM Server for SPARC Release Notes</i> . |

Table 3-48 Problems Resolved in Oracle Solaris 11.2 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>17510986</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | If the guest domain is Oracle Solaris 11.1 and SRU 11.1.9.5.1 or later is applied to it, or if the guest domain is Oracle Solaris 10 and 150400-01 or later is applied to it, system panic may occur during live migration or dynamic reconfiguration of physical partitions.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Workaround</b>  | This has been modified with SRU 11.1.14.5.0, and patch 150400-07 for Oracle Solaris 10.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Bug</b>         | <b>17627526</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>Messages like the following are output to the console at the time of starting Oracle Solaris and the ldoms/ldmd service is put to maintenance mode.</p> <p>[Example of message]</p> <pre>Jan 20 16:01:37 svc.startd[11]: svc:/ldoms/ldmd:default: Method "/opt/SUNWldm/bin/ldmd_start" failed with exit status 96. Jan 20 16:01:38 svc.startd[11]: ldoms/ldmd:default misconfigured: transitioned to maintenance (see 'svcs -xv' for details) At this time, messages like the following will be registered to the log file (/var/svc/log/ldomslmdm: [Example of message] [ Jan 21 20:08:55 Executing start method ("/opt/SUNWldm/bin/ldmd_start"). ] ldmd cannot communicate with the hypervisor as the required device does not exist: /devices/virtual-devices@100/channel-devices@200/virtual-channel@0:hvctl</pre> |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.1.19.6.0, and patch 150840-04 for Oracle Solaris 10.</p> <p>[How to restore]</p> <p>After confirming that the device file in question exists, restore the ldoms/ldmd service using the svcadm(1M) command of Oracle Solaris.</p> <pre># ls -l /devices/virtual-devices@100/channel-devices@200/virtual-channel@0:hvctl crw----- 1 root sys 148, 2048 Jan 21 20:08 /devices/virtual-devices@100/channel-devices@200/virtual-channel@0:hvctl # svcadm clear ldmd</pre>                                                                                                                                                                                                                                                                                                                |
| <b>Bug</b>         | <b>17709858</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | <p>If the system board is deleted by dynamic reconfiguration of the physical partition, any of the following phenomena may occur.</p> <ul style="list-style-type: none"> <li>- The following message is output from the logical domain, and Oracle Solaris enters the panic state. Fatal error has occurred in: PCIe fabric.(0xxx)(0xxx)</li> <li>- There is a hang during the device resume processing of a logical domain, and deleteboard(8) ends with an error due to a timeout.</li> </ul>                                                                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | This has been modified with SRU 11.1.15.4.0.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |

Table 3-48 Problems Resolved in Oracle Solaris 11.2 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | <b>17777004</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>Description</b> | If the logical domain has degraded memory because of a memory failure, when dynamic reconfiguration of the physical partition is executed with the deleteboard(8) command, Oracle Solaris on the domain with the degraded memory may enter the panic state.                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | This has been modified with SRU 11.1.17.5.0, and patch 150817-02 for Oracle Solaris 10. Before executing dynamic reconfiguration of the physical partition, check whether memory degradation has occurred from the control domain. If memory degradation has occurred, delete the associated memory area in advance.<br>[How to check]<br>Execute "ldm list-devices -a -S memory". If the resulting STATUS column displays "fail", the memory area has been degraded by SIZE from PA (physical address) displayed on the same line.<br>[How to delete a memory area]<br>Execute "ldm remove-memory <total value of above SIZES> <domain name>". |
| <b>Bug</b>         | <b>18055846</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M12-2, SPARC M12-2S, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | When adding a PCI expansion unit by using PCI hot plug (PHP), Oracle Solaris may panic, producing the following message:<br>panic[cpuX]/thread=XXXXXXXXXX: Fatal error has occurred in: PCIe fabric.(0xX)(0xXX)                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Workaround</b>  | This has been modified with Oracle Solaris 11.2, and patch 150400-18 for Oracle Solaris 10.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Bug</b>         | <b>18112775</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Description</b> | When Oracle VM Server for SPARC 3.1.0.1/SRU 11.1.14.5.0 is installed on the control domain, if dynamically assigned memory is reduced from a guest domain, which is running Oracle Solaris 10, with the ldm set-memory or the ldm remove-memory command, Oracle VM Server for SPARC ldm daemon on the control domain may produce a core dump and it may be restarted.                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | This has been modified with SRU 11.1.17.5.0, and patch 150817-02 for Oracle Solaris 10. Check the memory size that has been assigned to a logical domain with the ldm list-domain command and reduce it to the required size in several steps, using the ldm remove-memory command. It is recommended to reduce memory size by less than 256 MB at one time, but memory may also be reduced by a larger amount at one time. Meanwhile, the reduction may fail due to the occurrence of the same symptom while performing this action. In such a case, reduce the memory in smaller units.                                                       |
| <b>Bug</b>         | <b>18009532</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M12-2S, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | If dynamic reconfiguration is performed on a physical partition which has a dual channel 10Gbps FCoE card (SP1X7FAR2F/SP1X7FAS2F/7101673 (7101674)+7101677 (7101678)) mounted on it, system panic may occur due to the suspend processing of the inactivated qlcnic driver.                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Workaround</b>  | This has been modified with SRU 11.1.19.6.0, and patch 149167-03 for Oracle Solaris 10. Before dynamically reconfiguring a physical partition on which this specific card is mounted, activate any unplumbed qlcnic interface with the ifconfig plumb command.                                                                                                                                                                                                                                                                                                                                                                                  |

Table 3-48 Problems Resolved in Oracle Solaris 11.2 (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Bug</b>         | -                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M12-1, SPARC M12-2, SPARC M12-2S, SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Description</b> | <p>If Java VM is started on a SPARC M12 system or on a SPARC M10 system with the SPARC64 X+ processor, the following message may be output along with the production of a core dump.</p> <p>[Example of message]</p> <pre># A fatal error has been detected by the Java Runtime Environment: # # Internal Error (output.cpp:1576), pid=1310, tid=91 # guarantee((int)(blk_starts[i+1] - blk_starts[i]) &gt;= (current_offset - blk_offset)) failed: shouldn't increase block size # &lt;...&gt; # Abort (core dumped)</pre> <p>Moreover, due to the production of the core dump by Java VM, the following events may occur:</p> <ol style="list-style-type: none"> <li>1. On a SPARC M12 system or on a SPARC M10 system with the SPARC64 X+ processor, the [Add Asset] operation on Oracle Solaris fails.</li> <li>2. On a SPARC M12 system or on a SPARC M10 system with the SPARC64 X+ processor, the [Install Server] operation for Oracle Solaris installation fails.</li> <li>3. On a SPARC M12 system or on a SPARC M10 system with the SPARC64 X+ processor, Enterprise Controller and Proxy Controller (Ops Center management server) installation on the domains fail.</li> </ol> |
| <b>Workaround</b>  | This has been modified with SRU 11.1.17.5.0.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Bug</b>         | -                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Description</b> | <p>If dynamic reconfiguration is performed on a physical partition which has a Dual 10Gbps FCoE card (SE0X7EC12F/SE0X7EF12F/SG-PCIEFCOE2-Q-TA (SG-XPCIEFCOE2-Q-TA, 7105382)/SGPCIEFCOE2-Q-SR (SG-XPCIEFCOE2-Q-SR,7105381)) mounted on it, system panic may occur due to the processing of the qlge driver.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Workaround</b>  | <p>This has been modified with SRU 11.1.8.4.0, and patch 145648-04 for Oracle Solaris 10. There is no workaround other than applying the modification.</p> <p>If this specific card is mounted on a system, conduct any reconfiguration in deactivated state instead of dynamic reconfiguration.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |

## An Error Occurs in Executing the scp(1), sftp(1), or ssh(1) Command of Oracle Solaris or Installation of Oracle RAC Fails (CR:15825208)

### [Workaround]

Using the following procedure, change the setting so that the AES\_CTR, AES\_CBC\_MAC, and AES\_CFB128 algorithms are not used with the assist function of the encryption unit.

- If you use it with the client function (scp(1), sftp(1), ssh(1), etc.):
  1. **Add the content of the setting to the target file as one line. A space is necessary only between "Cipher" and "aes128-cbc."**
    - Target file
    - Settings for entire system: /etc/ssh/ssh\_config
    - Settings per user: \$HOME/.ssh/ssh\_config

- Setting contents

```
Ciphers aes128-cbc, aes192-cbc, aes256-cbc, 3des-bc, arcfour128,
arcfour256, arcfour, blowfish-cbc
```

- If you use it with the server function (such as Oracle Solaris sshd daemon)
  1. **Add the content of the setting to the target file as one line.**
    - Target file  
/etc/ssh/ssh\_config
    - Setting contents (recommended)

```
Ciphers 3des-cbc, arcfour128, arcfour256, arcfour, blowfish-cbc
```

2. **Restart the service with the following command:**

```
svcadm restart svc:/network/ssh:default
```

- If the problem cannot be resolved with any of the client function and server function:
  1. **Add the following, in place of the above content of the setting:**

```
UseOpenSSLEngine no
```

## If Oracle Solaris is Activated During Memory Degradation, a Panic May Occur (CR:15851441)

[Workaround]

If a panic occurs due to a memory failure, replace the failed memory.

If a panic occurs due to the memory mirror mode setting, start the system from the XSCF by using the following procedure.

Also, if a panic occurs due to a memory failure, starting the system from the XSCF by using the procedure shown below may prevent the problem, but this method is not always reliable. If a panic recurs even after the system is started by using the following procedure, replace the failed memory.

1. **Execute the poweroff(8) command to power off the physical partition (PPAR).**
2. **Execute the setdomainconfig(8) command to place the PPAR in the factory-default state.**

```
XSCF> setdomainconfig -p ppar_id -c default
```

3. **Execute the poweron(8) command to activate the PPAR.**

Oracle Solaris reboots in a configuration that includes only the control domain (factorydefault).

If you set the mirror mode for the memory, configure a logical domain after setting the mirror mode in the factory-default condition. Then, save the settings with `ldm add-spconfig`.

If the settings were saved with the mirror mode not set, do not specify them in the `ldm set-spconfig` command or the `setdomainconfig(8)` command of the XSCF for an environment using the mirror mode.



# Chapter 4

## Information on SPARC M10-1 Hardware

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This chapter describes special instructions and problems concerning the SPARC M10-1 hardware.

- [Notes and Restrictions](#)
- [Problems With Hardware and Workarounds](#)

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## Notes and Restrictions

### Notes on Using an Internal SAS Drive

We recommend using an internal SAS drive of the SPARC M10 systems after enabling the Oracle Solaris MPxIO setting.

For details on differences based on an internal SAS drive type and Oracle Solaris release, reasons to recommend enabling the MPxIO, effects of a disabled MPxIO, setting procedures, confirmation procedures, and so on, see "[Notes on Internal SAS Disk Drives and Oracle Solaris MPxIO Settings](#)" in "[Notes and Restrictions](#)" in "[Chapter 3 Information on Software](#)."

### Notes on Using an External DVD Drive

USB bus power drive is not supported for external DVD drives connected via USB.

### Notes on Using USB Memory

Use USB memory as follows. Install Oracle Solaris from USB memory, save/restore the system with Oracle Solaris Unified Archives (integrated archives), and save data with XSCF commands.

Before executing an Oracle Solaris or XSCF command that specifies USB memory as the data save destination, connect the USB memory used as the media.

The saved data contains system-related information. When using USB memory, you need to take care to manage the USB memory that has the saved data, from the standpoint of maintaining security.

- Fujitsu does not guarantee the operation of the USB memory currently sold by any manufacturer.
- USB memory is not suitable for long-term data retention. Use magnetic tape or disk units for long-term data retention.
- Some types of the USB memory used are thought to possibly cause errors such as a data read/write failure. If such an error occurs, immediately stop using the USB memory.
- Connect USB memory directly to a server or XSCF USB port. A connection via a USB hub, extension cable, etc. may cause an error.
- Do not disconnect USB memory during access. Doing so may result in an error or a command being unable to respond.
- To connect or disconnect USB memory in the OpenBoot PROM state, stop the SPARC M10 system and then perform the operation. If you perform the operation without stopping the system, the OpenBoot PROM may become non-responsive

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## Problems With Hardware and Workarounds

There is no problem that has been confirmed as of this moment.

# Chapter 5

## Information on SPARC M10-4 Hardware

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This chapter describes special instructions and problems concerning the SPARC M10-4 hardware.

- [Notes and Restrictions](#)
- [Problems With Hardware and Workarounds](#)

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## Notes and Restrictions

### Notes on Using an Internal SAS Drive

We recommend using an internal SAS drive of the SPARC M10 systems after enabling the Oracle Solaris MPxIO setting.

For details on differences based on an internal SAS drive type and Oracle Solaris release, reasons to recommend enabling the MPxIO, effects of a disabled MPxIO, setting procedures, confirmation procedures, and so on, see "[Notes on Internal SAS Disk Drives and Oracle Solaris MPxIO Settings](#)" in "[Notes and Restrictions](#)" in "[Chapter 3 Information on Software](#)."

### Notes on Using an External DVD Drive

USB bus power drive is not supported for external DVD drives connected via USB.

### Notes on Using USB Memory

Use USB memory as follows. Install Oracle Solaris from USB memory, save/restore the system with Oracle Solaris Unified Archives (integrated archives), and save data with XSCF commands.

Before executing an Oracle Solaris or XSCF command that specifies USB memory as the data save destination, connect the USB memory used as the media.

The saved data contains system-related information. When using USB memory, you need to take care to manage the USB memory that has the saved data, from the standpoint of maintaining security.

- Fujitsu does not guarantee the operation of USB memory currently sold by any manufacturer.
- USB memory is not suitable for long-term data retention. Use magnetic tape or disk units for long-term data retention.
- Some types of the USB memory used are thought to possibly cause errors such as a data read/write failure. If such an error occurs, immediately stop using the USB memory.
- Connect USB memory directly to a server or XSCF USB port. A connection via a USB hub, extension cable, etc. may cause an error.
- Do not disconnect USB memory during access. Doing so may result in an error or a command being unable to respond.
- To connect or disconnect USB memory in the OpenBoot PROM state, stop the SPARC M10 system and then perform the operation. If you perform the operation without stopping the system, the OpenBoot PROM may become non-responsive

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## Problems With Hardware and Workarounds

There is no problem that has been confirmed as of this moment.

# Chapter 6

## Information on SPARC M10-4S Hardware

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This chapter describes special instructions and problems concerning the SPARC M10-4S hardware.

- [Notes and Restrictions](#)
- [Problems With Hardware and Workarounds](#)

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## Notes and Restrictions

### Notes on Using an Internal SAS Drive

We recommend using an internal SAS drive of the SPARC M10 systems after enabling the Oracle Solaris MPxIO setting.

For details on differences based on an internal SAS drive type and Oracle Solaris release, reasons to recommend enabling the MPxIO, effects of a disabled MPxIO, setting procedures, confirmation procedures, and so on, see "[Notes on Internal SAS Disk Drives and Oracle Solaris MPxIO Settings](#)" in "[Notes and Restrictions](#)" in "[Chapter 3 Information on Software](#)."

### Notes on Using an External DVD Drive

USB bus power drive is not supported for external DVD drives connected via USB.

### Notes on Using USB Memory

Use USB memory as follows. Install Oracle Solaris from USB memory, save/restore the system with Oracle Solaris Unified Archives (integrated archives), and save data with XSCF commands.

Before executing an Oracle Solaris or XSCF command that specifies USB memory as the data save destination, connect the USB memory used as the media. The saved data contains system-related information. When using USB memory, you need to take care to manage the USB memory that has the saved data, from the standpoint of maintaining security.

- Fujitsu does not guarantee the operation of the USB memory currently sold by any manufacturer.
- USB memory is not suitable for long-term data retention. Use magnetic tape or disk units for long-term data retention.
- Some types of the USB memory used are thought to possibly cause errors such as a data read/write failure. If such an error occurs, immediately stop using the USB memory.
- Connect USB memory directly to a server or XSCF USB port. A connection via a USB hub, extension cable, etc. may cause an error.
- Do not disconnect USB memory during access. Doing so may result in an error or a command being unable to respond.
- To connect or disconnect USB memory in the OpenBoot PROM state, stop the SPARC M10 system and then perform the operation. If you perform the operation without stopping the system, the OpenBoot PROM may become non-responsive

## Restrictions on Replacing a Crossbar Box

Replacement of a crossbar box using the `replacefru(8)` command is not supported at present. To replace a crossbar box, perform the following procedure:

1. **Execute the `showhardconf` command to confirm that the crossbar box to be replaced is not the master chassis.**

The crossbar box with the "Role:Master" indication is the master chassis.

```
XSCF> showhardconf
 :
XBBOX#80 Status:Normal; Role:Master; Ver:2038h; Serial:2111206001;
 :
```

2. **If the crossbar box to be replaced is the master chassis, first execute the `switchscf` command to switch the box to standby.**

```
XSCF> switchscf -y -t Standby
```

3. **The master XSCF is switched. Log in to the XSCF again.**
4. **Power off the physical partition (PPAR) that uses the crossbar box, and then power off the crossbar box to be replaced.**  
Confirm that the Power-LED on the operation panel of the crossbar box is turned off.
5. **Disconnect the power supply to the crossbar box to be replaced, and then**

**replace it.**

---

**Note** - Although error logs for the crossbar box to be replaced are registered when the input power supply is disconnected, they can safely be ignored.

---

6. **Connect the power cord of the crossbar box to the input power supply.**

For details, see "5.3 Connecting Cables to a Crossbar Box" in the *Fujitsu M10-4S/SPARC M10-4S Installation Guide*.

Wait until STANDBY-LED on the operation panel of the crossbar box turns on.

7. **Execute the diagxbu command to perform diagnosis of the newly installed crossbar box.**

```
XSCF> diagxbu -y -b XX -t YY -t ZZ
```

Specify the BB\_IDs (00 to 15) of the SPARC M10-4S that is powered off in XX, YY, and ZZ.

8. **Execute the showlogs command to confirm that no error occurred during the diagnosis.**

```
XSCF> showlogs error
```

9. **Confirm that there are no faulty components.**

```
XSCF> showstatus
```

## Restrictions on Adding Expansion Rack 2

Addition of a crossbar box using the addfru(8) command is not supported at present. To add a crossbar box, see "9.4 Adding Expansion Rack 2" in the *Fujitsu M10-4S/SPARC M10-4S Installation Guide* and read steps 17 and 18 as follows:

17. **Connect all the power cords of the crossbar box and SPARC M10-4S to the input power supply.**

For details, see "5.1 Connecting Cables to the SPARC M10-4S" and "5.3 Connecting Cables to a Crossbar Box."

18. **Update the firmware to the same version as that of the master XSCF.**

- XCP 2040 or earlier

```
XSCF> getflashimage file:///media/usb_msd/images/XCPxxxx.tar.gz
XSCF> flashupdate -c update -m xcp -s version
```

- XCP 2041 or later

```
XSCF> flashupdate -c sync
```

---

## Problems With Hardware and Workarounds

There is no problem that has been confirmed as of this moment.

## Information on the PCI Expansion Unit

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This chapter describes special instructions and problems concerning the PCI expansion unit.

- [PCI Expansion Unit Firmware Versions and Support Information](#)
  - [Direct I/O Function for the PCI Expansion Unit](#)
  - [Problems With the PCI Expansion Unit and Workarounds](#)
- 

## PCI Expansion Unit Firmware Versions and Support Information

The following lists the PCI expansion unit firmware versions that have been released for the SPARC M10 systems.

**Table 7-1** PCI Expansion Unit Firmware Versions

| Version | Main Support Information                                 |
|---------|----------------------------------------------------------|
| 1310    | - SPARC M12 support<br>- Dual 40Gb Ethernet card support |
| 1220    | Improvement of XCP PCI expansion unit firmware defects   |
| 1210    | F160 card support                                        |
| 1200    | Improvement of XCP PCI expansion unit firmware defects   |
| 1180    | Improvement of XCP PCI expansion unit firmware defects   |
| 1170    | Improvement of XCP PCI expansion unit firmware defects   |
| 1150    | Improvement of XCP PCI expansion unit firmware defects   |
| 1130    | Improvement of XCP PCI expansion unit firmware defects   |
| 1120    | Improvement of XCP PCI expansion unit firmware defects   |
| 1110    | Improvement of XCP PCI expansion unit firmware defects   |
| 1100    | SPARC M10 support                                        |

---

# Direct I/O Function for the PCI Expansion Unit

For XCP 2044 or later on the SPARC M10-1/M10-4 and for XCP 2050 or later on the SPARC M10-4S, the direct I/O function of Oracle VM Server for SPARC is supported for the PCI expansion unit. This makes it possible to assign an I/O domain for each slot of the PCI expansion unit. For details on the direct I/O function of Oracle VM Server for SPARC, see the *Oracle VM Server for SPARC Administration Guide* for the version used.

If the PCI expansion unit is connected to SPARC M10-4/M10-4S, make the setting below before using the direct I/O function. For SPARC M10-1, the `setpciboxdio` setting need not be made. The direct I/O function can be used simply by connecting the PCI expansion unit to SPARC M10-1.

## Setting/Displaying the Direct I/O Function

To set the direct I/O function for the PCI expansion unit, use the `setpciboxdio(8)` command of the XSCF firmware. To confirm the present settings, use the `showpciboxdio(8)` command.

For details on the `setpciboxdio(8)` and `showpciboxdio(8)` commands, see the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual* or the man page for each command.

---

# Problems With the PCI Expansion Unit and Workarounds

This section describes problems regarding the PCI expansion unit as well as the workarounds for them for each version of the PCI expansion unit firmware.

## Problems and Their Workarounds for All Versions of the PCI Expansion Unit Firmware

The following table shows problems that might occur in any versions of Oracle Solaris being supported and workarounds for them.

Table 7-2 Problems and Their Workarounds for All Versions of the PCI Expansion Unit Firmware

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-170215-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>Suppose that the PCIe root complex connecting the PCI expansion unit is dynamically added to a logical domain with the <code>ldm add-io</code> command. In this case, any of the following phenomena may occur on the PCI cards mounted in slots of the PCI expansion unit.</p> <ul style="list-style-type: none"> <li>- The following error may be registered:<br/>             [Error message example]<br/>             FRU: /BB#0/PCI#0/PCIBOX#0000/PCI#11<br/>             Msg: PCICARD failed</li> <li>- There may be "lane degrade".<br/>             To check whether there is "lane degrade", execute the <code>prtdiag</code> command of Oracle Solaris and check "Cur Speed/Width."</li> </ul> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>             [How to restore]<br/>             Restart the logical domain connected to this PCI expansion unit.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>RTI No.</b>     | <b>RTIF2-130724-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>If a 6 Gbps SAS card is mounted in the PCI expansion unit with PCI hot plug (PHP), linkup with PCI Express 8 Lane may not succeed, and Speed may be displayed as "5.0GTx4" or "5.0GTx2" by the <code>prtdiag</code> command of Oracle Solaris.</p> <p>[prtdiag output example]<br/>             /SYS/PCIO PCIE LSI,sas-pciex1000,72 LSI,2008 5.0GTx2<br/>             /pci@8000/pci@4/pci@0/pci@8/pci@0/pci@0/pci@0/pci@1/pci@0/pci@8/LSI,sas@0</p>                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | <p>Restart the physical partitions (PPARs) or I/O domain, or reinstall the 6Gbps SAS card using PHP.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>RTI No.</b>     | <b>RTIF2-130724-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>The following error may be registered for the PCI cards mounted in slots 6, 7, 10, and 11 of the PCI expansion unit when the physical partitions (PPARs) are started.</p> <p>[Error message example]<br/>             FRU: /MBU/PCI#0/PCIBOX#0000/PCI#7<br/>             Msg: PCICARD failed</p>                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | <p>When the device can be recognized from Oracle Solaris, ignore this error message.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>RTI No.</b>     | <b>RTIF2-130703-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Description</b> | <p>When installing a PCI expansion unit using PCI hot plug (PHP), among PCI expansion unit slots 6, 7, 10 and 11, those without HBA will have command execution result "disconnected" instead of "empty" for the Oracle Solaris <code>cfgadm(1M)</code> command.</p> <p>Although the display is not appropriate, the slots work normally if you mount HBA. This phenomenon does not occur if you restart the logical domain after connecting the PCI expansion unit.</p>                                                                                                                                                                                                                                    |
| <b>Workaround</b>  | <p>This is just a display problem and does not affect operation.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |

Table 7-2 Problems and Their Workarounds for All Versions of the PCI Expansion Unit Firmware (*continued*)

|                    |                                                                                                                                                                               |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130703-002</b>                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                              |
| <b>Description</b> | When you install a PCI expansion unit with PCI hot plug (PHP), there may be lane degrade.                                                                                     |
| <b>Workaround</b>  | When using PHP, execute <code>cfgadm -c configure</code> instead of <code>cfgadm -c connect</code> .                                                                          |
| <b>RTI No.</b>     | <b>RTIF2-130703-003</b>                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                              |
| <b>Description</b> | When you install a PCI expansion unit using PCI hot plug (PHP), a PCI-Express correctable error may occur and cause degradation of the PCI expansion unit at the next reboot. |
| <b>Workaround</b>  | If there a fault error message is output after installing a PCI expansion unit with PHP, reinstalled the PCI expansion unit.                                                  |
| <b>RTI No.</b>     | <b>RTIF2-130703-004</b>                                                                                                                                                       |
| <b>Model</b>       | <b>SPARC M10-4, SPARC M10-4S</b>                                                                                                                                              |
| <b>Description</b> | When you install a PCI expansion unit with PCI hot plug (PHP), the PCI expansion unit may not be recognized.                                                                  |
| <b>Workaround</b>  | If the PCI expansion unit is not recognized after installing it with PHP, reinstall the PCI expansion unit.                                                                   |

## Problems Resolved in Version 1310 of the PCI Expansion Unit Firmware

The following table lists the problems resolved in version 1310 of the PCI expansion unit firmware.

Table 7-3 Problems Resolved in Version 1310 of the PCI Expansion Unit Firmware

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-170214-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Description</b> | When the physical partition (PPAR) connecting the PCI expansion unit is powered on, a link card error may be erroneously detected.<br>[Error message example]<br><code>XSCF&gt; showlogs error</code><br>Date: Jun 03 19:09:19 JST 2016<br>Code: 80000480-009d01009fff00d500-11000057d000000000000000<br>Status: Alarm Occurred: Jun 03 19:09:16.185 JST 2016<br>FRU: /BB#0/PCI#9/LINK,/BB#0/PCI#9/LINK/MGCBL,/BB#0/PCI#9/PCIBOX#0000/LINKBD,*<br>Msg: TWI access error |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Power off and then on again the PPAR connected to the PCI expansion unit.                                                                                                                                                                                                                                                                                                                                      |

Table 7-3 Problems Resolved in Version 1310 of the PCI Expansion Unit Firmware (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-170209-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>In either of the following cases, in the PCI expansion unit, the POWER LED of the PCIe slot where the PCIe card is not mounted goes on by mistake.</p> <ul style="list-style-type: none"> <li>- The SPARC M10-4S (system board) connected to the PCI expansion unit was added by the addboard(8) command using dynamic reconfiguration (DR) of physical partitions (PPARs) in a system having a building block configuration.</li> <li>- The PCIe root complex connecting the PCI expansion unit was dynamically added/deleted using the ldm command of Oracle VM Server for SPARC.</li> </ul> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>[How to restore]</p> <p>Execute any of the following actions to bring the state of the POWER LED of a PCIe slot back to normal.</p> <ul style="list-style-type: none"> <li>- Reboot Oracle Solaris.</li> <li>- Power off the PPAR.</li> <li>- Incorporate a PCIe card using PHP</li> </ul>                                                                                                                                                                                                                                                               |

## Problems Resolved in Version 1220 of the PCI Expansion Unit Firmware

The following table lists the problems resolved in version 1220 of the PCI expansion unit firmware.

Table 7-4 Problems Resolved in Version 1220 of the PCI Expansion Unit Firmware

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                             |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-161014-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | The PCI expansion unit firmware may be unable to detect power supply unit (PSU) failures in the PCI expansion unit.                                                                                                                                                                                                                                                                                                                         |
| <b>Workaround</b>  | <p>Use one of the following methods to determine whether the PSU of the PCI expansion unit has failed. If a PSU failure has been determined, replace the PSU.</p> <ul style="list-style-type: none"> <li>- The PSU LED goes on (amber) or blinks (green) while the physical partition (PPAR) is running.</li> <li>- After the PPAR is powered off, a PSU failure is registered in an XSCF log when the PPAR is powered on again.</li> </ul> |

# Problems Resolved in Version 1200 of the PCI Expansion Unit Firmware

The following table lists the problems resolved in version 1200 of the PCI expansion unit firmware.

Table 7-5 Problems Resolved in Version 1200 of the PCI Expansion Unit Firmware

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-150225-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | <p>If the input voltage drops instantaneously on the system with the PCI expansion unit connected, a failure of the power supply unit (PSU) of the PCI expansion unit may be detected by mistake, and the CHECK LED (amber) of the PCI expansion unit may go on. You can confirm this symptom by referring to the showlogs error -v command. If the execution result of the showlogs error -v command is "Msg:PSU failed" or "PSU failed(PSU shortage)" and the fourth line of the Diagnostic Code has the following values, the symptom corresponds to this problem.</p> <ol style="list-style-type: none"> <li>1) Fourth byte: the leftmost (x80) bit is 0 (any of 0*, 1*, 2*, 3*, 4*, 4*, 6*, and 7*), and</li> <li>2) Fourth byte: the seventh (x02) bit from the left is 0 (any of *0, *1, *4, *5, *8, *9, *c, and *d), and</li> <li>3) Fifth byte: the leftmost (x80) bit is 0 (any of 0*, 1*, 2*, 3*, 5*, 4*, 6*, and 7*), and</li> <li>4) Seventh byte:             <ul style="list-style-type: none"> <li>For the PCI expansion unit firmware version 1120 or earlier: Any value</li> <li>For the PCI expansion unit firmware version 1130 or later: the left-most (x80) bit is 0 and the second (x40) bit from the left is 0 (any of 0*, 1*, 2*, and 3*).</li> </ul> </li> </ol> <p>Date: Jan 13 15:19:40 JST 2015<br/>           Code: 80000408-00cb000000ff0000ff-11000025600100000000000000<br/>           Status: Alarm Occurred: Jan 13 15:19:40.742 JST 2015<br/>           FRU: /MBU/PCI#1/PCIBOX#1111/PSU#0<br/>           Msg: PSU failed<br/>           Diagnostic Code:<br/>           00013131 31310000 0000<br/>           00010000 00000000 0000<br/>           00010000 00000000 0000<br/>           01410800 00000000 00000000 00000000<br/>           00000000 00000000 0000</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.<br/>           [How to restore]<br/>           Use either of the following procedures.</p> <ul style="list-style-type: none"> <li>- How to restore 1             <ul style="list-style-type: none"> <li>Turn off the power to the SPARC M10 chassis connected to the PCI expansion unit. Then, disconnect both PCI expansion unit power cables (AC OFF). Then, wait 30 seconds before reconnecting the cables (AC ON).</li> </ul> </li> <li>- How to restore 2             <ul style="list-style-type: none"> <li>Perform pseudo active replacement of the PSU in which the error has been detected (temporarily remove the PSU and then reinstall it). When performing pseudo active replacement of the PSU, use the ioxadm(8) command. Note that the fieldeng privilege is required to execute the ioxadm(8) command.</li> </ul> </li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |

# Problems Resolved in Version 1180 of the PCI Expansion Unit Firmware

The following table lists the problems resolved in version 1180 of the PCI expansion unit firmware.

Table 7-6 Problems Resolved in Version 1180 of the PCI Expansion Unit Firmware

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|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-141111-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Description</b> | <p>In a PCI expansion unit, a power supply unit (PSU) failure is erroneously detected if you perform the following operations in the condition that a power cable connected to either of PSUs in a redundant configuration is pulled out.</p> <ul style="list-style-type: none"><li>- Power-on of a physical partition (PPAR)</li><li>- Mount a PCI expansion unit in PCI hot plug (PHP)</li></ul> <p>Also, CHECK LED (amber) of the PCI expansion unit chassis may go on.<br/>You can confirm this symptom with the showlogs error -v command.<br/>If the result of the showlogs error -v command shows "Msg:PSU failed", and the fifth (x08) bit from left of the second byte of the fourth line of Diagnostic Code shows 1 (any of *8, *9, *a, *b, *c, *d, *e, *f), it corresponds to this failure.</p> <p>[Error message example]<br/>If the left of the second byte of the fourth line of Diagnostic Code shows "49":<br/>XSCF&gt; <b>showlogs error -v</b><br/>Date: Nov 06 17:10:14 JST 2014<br/>Code: 80000408-00d400000ff0000ff-110000256100000000000000<br/>Status: Alarm Occurred: Nov 06 17:10:10.509 JST 2014<br/>FRU: /BB#0/PCI#9/PCIBOX#2005/PSU#1<br/>Msg: PSU failed<br/>Diagnostic Code:<br/>00093131 31310100 0000<br/>00090000 00000000 0000<br/>00090000 00000000 0000<br/>01492800 00000038 00000000 00000000<br/>00000000 00000000 0000</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <ul style="list-style-type: none"><li>- How to restore 1<br/>Turn off the power to the SPARC M10 chassis connected to the PCI expansion unit. Then, disconnect both PCI expansion unit power cables (AC OFF). Then, wait 30 seconds before reconnecting the cables (AC ON).</li><li>- How to restore 2<br/>Perform pseudo active replacement of the PSU in which the error has been detected (temporarily remove the PSU and then reinstall it). When performing pseudo active replacement of the PSU, use the ioxadm(8) command. Note that the fieldeng privilege is required to execute the ioxadm(8) command.</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |

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Table 7-6 Problems Resolved in Version 1180 of the PCI Expansion Unit Firmware (*continued*)

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|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140715-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>Description</b> | In a PCI expansion unit, if the power supply unit (PSU) has a redundant configuration and the power cable connected to one of the power supply units is pulled out, a PSU error may be erroneously detected, and the PCI expansion unit chassis CHECK LED (amber) may go on. At this time, executing the showlogs error command outputs the "Msg: PSU failed" log.                                                                                                                                                                                                                                                                                                                                                          |
| <b>Workaround</b>  | There is no effective workaround.<br>[How to restore]<br>Use either of the following procedures. <ul style="list-style-type: none"><li>- How to restore 1<br/>Turn off the power to the SPARC M10 chassis connected to the PCI expansion unit. Then, disconnect both PCI expansion unit power cables (AC OFF). Then, wait 30 seconds before reconnecting the cables (AC ON).</li><li>- How to restore 2<br/>Perform pseudo active replacement of the PSU in which the error has been detected (temporarily remove the PSU and then reinstall it). When performing pseudo active replacement of the PSU, use the ioxadm(8) command. Note that the fieldeng privilege is required to execute the ioxadm(8) command.</li></ul> |

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## Problems Resolved in Version 1170 of the PCI Expansion Unit Firmware

The following table lists the problems resolved in version 1170 of the PCI expansion unit firmware.

Table 7-7 Problems Resolved in Version 1170 of the PCI Expansion Unit Firmware

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-140902-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>If input power for the PCI expansion unit is disconnected (AC OFF) or power failure occurs, a PSU "Information" level error log is supposed to be registered. However, a failure level error log "Alarm" is incorrectly registered. At this point, CHECK LED on the front of the PCI expansion unit lights up, "Faulted" is displayed on the PSU status, and the error message "AC Fail" is displayed.</p> <p>You can check the error message of this phenomenon by executing the showlogs error command, and check the status by executing the showhardconf command.</p> <p>[Error message example]<br/> XSCF&gt; <b>showlogs error</b><br/> Date: Jul 10 16:25:02 JST 2014<br/> Code: 80000400-00d4000000ff0000ff-1100002a0000000000000000<br/> Status: Alarm Occurred: Jul 10 16:24:57.269 JST 2014<br/> FRU: /BB#0/PCI#8/PCIBOX#2003/PSU#1<br/> Msg: AC FAIL<br/> [PSU status example]<br/> XSCF&gt; <b>showhardconf</b><br/> --- Omitted ---<br/> PCIBOX#2003; Status:Normal; Ver:1150h; Serial:2121152003;<br/> --- Omitted ---<br/> * PSU#1; Status:Faulted; Serial:FEJD1201000170;</p> |
| <b>Workaround</b>  | Update the PCI expansion unit firmware to 1170 or later, and the XCP firmware to XCP 2220 or later. There is no impact on system operation from this phenomenon.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>RTI No.</b>     | <b>RTIF2-140902-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>Link card fault may be incorrectly registered if logs related to one PCI expansion unit are registered more than once in a short time (5 to 10 minutes) when one or more physical partitions (PPAR) are operating in the configuration with PCI expansion units connected. For details on how to check this symptom, see "<a href="#">Confirmation Method of RTIF2-140902-002.</a>"</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Workaround</b>  | Ignore this error message as it has no effect on the system.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |

Table 7-7 Problems Resolved in Version 1170 of the PCI Expansion Unit Firmware (*continued*)

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131224-003</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>Description</b> | <p>The following error messages related to the link board may be displayed if the link board with Part Number "CA20365-B60X007AD/7061035" is connected to the PCI expansion unit.</p> <p>[Error message example 1]<br/>FRU: /BB#0/PCI#3/PCIBOX#1234/IOB,/BB#0/PCI#3/PCIBOX#1234/LINKBD<br/>Msg: PCI access error</p> <p>[Error message example 2]<br/>FRU: /BB#0/PCI#3/PCIBOX#1234/IOB<br/>Msg: PCI access error</p> <p>[Error message example 3]<br/>FRU:<br/>/BB#0/PCI#3/LINK,/BB#0/PCI#3/LINK/LINKCBL#-/,/BB#0/PCI#3/PCIBOX#1234/LINKBD,*<br/>Msg: PCI Express lane is degraded</p> <p>The Part Number can be checked in the "Part Num" line upon executing the <code>ioxadm -v list</code> command.</p> <p>[Example]<br/><code>XSCF&gt; ioxadm -v list</code><br/>Location Type FW Ver Serial Num Part Num State<br/>--- Omitted ---<br/>PCIBOX#9011/LINKBD BOARD - PP134701CJ CA20365-B60X 007AD/7061035 On</p> |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |

## Confirmation Method of RTIF2-140902-002

The phenomenon of RTIF2-140902-002 can be checked in the following ways.

1. **Executing the `showlogs error -v` command outputs the following.**

- FRU displays "LINK" or "LINKBD"
- Msg displays "TWI access error"
- The first byte in the fourth line of the Diagnostic Code is "07"

[Example of incorrect registration]

```

XSCF> showlogs error -v
Date: Dec 20 10:50:05 JST 2013
Code: 80000480-001f010021ff00cc01-11000057d000000000000000
Status: Alarm Occurred: Dec 20 10:49:59.136 JST 2013
FRU: /MBU/PCI#1/LINK,/MBU/PCI#1/LINK/MGCBL,/MBU/PCI#1/PCIBOX#3001/LINKBD,*
Msg: TWI access error
Diagnostic Code:
00010000 00000000 0000
00010000 00000000 0000
00013330 30310000 0000
07100000 00000000 00000000 00000000
^^
00000000 00000000 0000
```

2. **When the showlogs event command or showlogs error command is executed on the same PCI expansion unit within 10 minutes, the multiple logs shown below are registered.**

- 12 or more event logs related to the PCIe card are registered upon executing the showlogs event command. The number of event logs is calculated as follows depending on the event type.

- PCIe card insertion event: 2

- Event other than PCIe card insertion: 1

For example, if a PCIe card is repeatedly inserted and removed four times, eight message lines are displayed. However, the number of registered event logs is 12, calculated by multiplying three events by four. This applies to this case.

[Example of PCIe card insertion/removal]

```
XSCF> showlogs event
Dec 20 10:49:59 JST 2013 Attach operation (/BB#0/PCI#1/
PCIBOX#3001/PCI#1) Dec 20 10:49:59 JST 2013 Detach
operation (/BB#0/PCI#1/PCIBOX#3001/PCI#1)
--- Omitted ---
```

- Six or more logs are registered by the PCI expansion unit firmware (the first byte in the third field of Code is 11) upon executing the showlogs error command.

[Example of log registration by PCI expansion unit firmware]

```
XSCF> showlogs error
Date: Jun 06 10:55:28 JST 2014
Code: 80000400-00d4000000ff0000ff-1100002a0000000000000000
^^
Status: Alarm Occurred: Jun 06 10:55:28.028 JST 2014
FRU: /BB#0/PCI#1/PCIBOX#3001/PSU#0
Msg: AC FAIL
Date: Jun 06 10:55:34 JST 2014
Code: 10000400-00d4000000ff0000ff-1100002b0000000000000000
^^
Status: Information Occurred: Jun 06 10:55:34.479 JST 2014
FRU: /BB#0/PCI#1/PCIBOX#3001/PSU#0
Msg: PSU RECOVERY
--- Omitted ---
```

# Problems Resolved in Version 1150 of the PCI Expansion Unit Firmware

The following table lists the problems resolved in version 1150 of the PCI expansion unit firmware.

Table 7-8 Problems Resolved in Version 1150 of the PCI Expansion Unit Firmware

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131224-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | Oracle Solaris may hang while starting up, if using the direct I/O feature, in respect to the PCI expansion unit. The probability of the occurrence of this symptom increases with the number of PCI cards that are mounted in the PCI expansion unit.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Workaround</b>  | There is no effective workaround.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>RTI No.</b>     | <b>RTIF2-131224-002</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>Description</b> | <p>When the PCI expansion unit and the chassis of the SPARC M10 system are connected by optical cables in a low-temperature environment, an optical cable error log may be registered. You can confirm this symptom with the showlogs error -v command.</p> <p>If the result of the showlogs error -v command is "Msg: LINKCBL failed", and the 3rd byte of the 4th line of the Diagnostic Code is either "10", "50" or "60", it corresponds to this symptom.</p> <p>[Error message example]</p> <p>In case the 3rd byte of the 4th line of the "Diagnostic Code" is "10":</p> <pre>XSCF&gt; showlogs error -v Date: Dec 17 15:50:11 JST 2013 Code: 10000400-009eff0000ff0000ff-11000044000000000000000000 Status: Information Occurred: Dec 17 15:50:06.930 JST 2013 FRU: /BB#0/PCI#3/LINK/LINKCBL#1 Msg: LINKCBL failed Diagnostic Code: 00030001 00000000 0000 00030000 00000000 0000 00030000 00000000 0000 00001000 00000000 00000000 00000000 00000000 00000000 0000</pre> |
| <b>Workaround</b>  | Ignore this error message as it has no effect on the system.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |

# Problems Resolved in Version 1130 of the PCI Expansion Unit Firmware

The following table lists the problems resolved in version 1130 of the PCI expansion unit firmware.

Table 7-9 Problems Resolved in Version 1130 of the PCI Expansion Unit Firmware

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-131120-001</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Description</b> | <p>If an external factor causes an instantaneous voltage drop or the like, an error in the power supply unit (PSU) of the PCI expansion unit is erroneously detected, and the error log of "Msg: PSU failed" may be registered. Moreover, if the erroneous detection of this PSU error occurs simultaneously in two PSUs of the PCI expansion unit, the physical partition (PPAR) is shut down.</p> <p>You can use the showlogs error -v command to check whether this problem is due to an instantaneous voltage drop caused by an external factor. Suppose that the execution result for this command is "Msg: PSU failed" and that the first, second, and third bytes on the fourth line of the Diagnostic Code have the following values. The problem may be due to, for example, an instantaneous voltage drop caused by an external factor.</p> <p>First byte; "00"<br/>         Second byte; The sixth (x04) bit from the left is 0 (any of *0, *1 *2, *3, *8, *9, *a, or *b).<br/>         Third byte; The third (x20) bit from the left is 1 (any of 2*, 3*, 6*, 7*, a*, b*, e*, or f*).<br/>         "*" for the second and third bytes indicates any value.</p> <p>&lt;Example&gt; The first to third bytes on the fourth line of the Diagnostic Code are "000120"<br/>         XSCF&gt; <b>showlogs error -v</b><br/>         Date: Oct 30 10:27:17 JST 2013<br/>         Code: 80000408-00cb000000ff0000ff-110000246101000000000000<br/>         Status: Alarm Occurred: Oct 30 10:27:17.597 JST 2013<br/>         FRU: /MBU/PCI#1/PCIBOX#7010/PSU#1<br/>         Msg: PSU failed<br/>         Diagnostic Code:<br/>         00013730 31300100 0000<br/>         00010000 00000000 0000<br/>         00010000 00000000 0000<br/>         00012000 00000000 00000000 00000000<br/>         00000000 00000000 0000</p> |
| <b>Workaround</b>  | <p>There is no effective workaround.</p> <p>If the value of the first to third bytes on the fourth line of Diagnostic Code does not match the value in the [Description] column, assume that a PSU failure occurred.</p> <p>[How to restore]</p> <ul style="list-style-type: none"> <li>-- If this event occurs in a single PSU             <ul style="list-style-type: none"> <li>- If the error log "Msg: PSU RECOVERY" is registered, the system has already been restored from an instantaneous voltage drop. The system can be operated continuously.</li> <li>- If the error log "Msg: PSU RECOVERY" is not registered, a PSU failure has occurred. Replace the PSU.</li> </ul> </li> <li>-- If this event occurs in two PSUs             <ul style="list-style-type: none"> <li>The PPAR is shut down. Reactivate the PPAR. If the PCI expansion unit cannot be powered on, it indicates that a PSU has failed. Replace the PSU.</li> </ul> </li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |

# Problems Resolved in Version 1120 of the PCI Expansion Unit Firmware

The following table lists the problems resolved in version 1120 of the PCI expansion unit firmware.

Table 7-10 Problems Resolved in Version 1120 of the PCI Expansion Unit Firmware

---

|                    |                                                                                                                                                                                   |
|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130703-009</b>                                                                                                                                                           |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                     |
| <b>Description</b> | When physical partitions (PPARs) or the I/O domain is started, the 6Gbps SAS card mounted in the PCI expansion unit may not be successfully linked up with the PCI Express 8lane. |
| <b>Workaround</b>  | If the 6Gbps SAS card is not successfully linked up with the PCI Express 8lane, restart the PPARs or I/O domain, or reinstall the 6Gbps SAS card using PCI hot plug (PHP).        |

---

# Problems Resolved in Version 1110 of the PCI Expansion Unit Firmware

The following table lists the problems resolved in version 1110 of the PCI expansion unit firmware.

Table 7-11 Problems Resolved in Version 1110 of the PCI Expansion Unit Firmware

---

|                    |                                                                                                                                                                                                          |
|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130703-007</b>                                                                                                                                                                                  |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                            |
| <b>Description</b> | When physical partitions (PPARs) or the I/O domain is started, a link card may be erroneously detected as having an error.<br><br>[Example of message]<br>FRU: /BB#0/PCI#1/LINK<br>Msg: TWI access error |
| <b>Workaround</b>  | The display of this error message does not affect the operation.                                                                                                                                         |

---

|                    |                                                                                                                                                                                                |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130703-008</b>                                                                                                                                                                        |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                  |
| <b>Description</b> | When physical partitions (PPARs) or the I/O domain is started, a PCI-Express correctable error may occur and cause degradation of the PCI expansion unit at the next reboot of Oracle Solaris. |
| <b>Workaround</b>  | If the error message is output, restart the PPARs or I/O domain, or reinstall the PCI expansion unit using PCI hot plug (PHP).                                                                 |

---

# Problems Resolved in Version 1100 of the PCI Expansion Unit Firmware

The following table lists the problems resolved in version 1100 of the PCI expansion unit firmware.

Table 7-12 Problems Resolved in Version 1100 of the PCI Expansion Unit Firmware and Workarounds for Them

|                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>RTI No.</b>     | <b>RTIF2-130703-005</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | <p>When physical partitions (PPARs) or the I/O domain is started, the PCIe card or link board mounted in the PCI expansion unit may be erroneously detected as having an error.</p> <p>[Example of PCIe card message]<br/>FRU: /BB#0/PCI#3/PCIBOX#1234/PCI#3<br/>Msg: PCICARD failed</p> <p>[Example of link board message]<br/>FRU: /BB#0/PCI#0/PCIBOX#1234/LINKBD<br/>Msg: TWI access error</p>                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | <p>The display of this error message does not affect the operation.</p> <p>When this problem occurs in the link board, the CHECK LED on the PCI expansion unit turns on. However, if this problem does not occur at the next start of the PPAR, the CHECK LED turns off.</p>                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>RTI No.</b>     | <b>RTIF2-130703-006</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>Model</b>       | <b>SPARC M10-1, SPARC M10-4, SPARC M10-4S</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>Description</b> | <p>If the serial number of the PCI expansion unit that is displayed upon the execution of the <code>ioxadm -v list</code> command consists entirely of 0's ("0000000000"), the following occurs: Hardware errors detected after the input power to the PCI expansion unit is turned on but before the power to the first physical partition (PPAR) is turned on are not registered as errors.</p>                                                                                                                                                                                                                                                                      |
| <b>Workaround</b>  | <p>Do not replace an I/O board together with a fan backplane. Also, do not mount in the PCI expansion unit an I/O board or fan backplane that has been used previously in another PCI expansion unit.</p> <p>If the serial number of the PCI expansion unit that is displayed in response to execution of the <code>ioxadm -v list</code> command consists entirely of 0's ("0000000000"), execute the <code>ioxadm(8)</code> command of the XSCF firmware to restore the serial number.</p> <p>In this case, you need the fieldeng privilege.</p> <p>For details, see "<a href="#">Restoring the Serial Number of the PCI Expansion Unit (RTIF2-130703-006)</a>."</p> |

## Restoring the Serial Number of the PCI Expansion Unit (RTIF2-130703-006)

If the serial number of the PCI expansion unit that is displayed in response to execution of the `ioxadm -v list` command consists entirely of 0's ("0000000000"), execute the `ioxadm(8)` command of the XSCF firmware to restore the serial number.

In this case, you need the fieldeng privilege.

```
XSCF> ioxadm [-fvAM] serial target serial_num
```

For target, specify the identifier of the target PCI expansion unit. In this case, "PCIBOX#0000" is specified. For serial\_num, specify the serial number of the PCI expansion unit before the change in the format of "nnnnnnnnnn". The serial number is found on the label on the chassis of the PCI expansion unit.

This command replaces the serial number and identifier of the PCI expansion unit. By executing the `ioxadm -v list` command, you can confirm that the serial number and identifier have been replaced.

In the following example, the serial number of the PCI expansion unit is "2121212006".

```
XSCF> ioxadm serial PCIBOX#0000 2121212006
XSCF> ioxadm -v list
```

| Location           | Type    | FW   | Ve | Serial Num     | Part Num               | State |
|--------------------|---------|------|----|----------------|------------------------|-------|
| PCIBOX#2006        | PCIBOX  | -    |    | 2121212006     |                        | On    |
| PCIBOX#2006/PSU#0  | PSU     | -    |    | FEJD1201000170 | CA01022-0750-D/        | On    |
| PCIBOX#2006/PSU#1  | PSU     | -    |    | FEJD1245001342 | CA01022-0750-D/7060988 | On    |
| PCIBOX#2006/IOB    | IOBOARD | 1110 |    | PP121001JM     | CA20365-B66X 007AF     | On    |
| PCIBOX#2006/LINKBD | BOARD   | -    |    | PP123300TR     | CA20365-B60X 001AA     | On    |
| PCIBOX#2006/FANBP  | FANBP   | -    |    | PP120904SY     | CA20365-B68X 004AC     | On    |
| BB#00-PCI#00       | CARD    | 1110 |    |                |                        | On    |

```
XSCF>
```

## Information on SPARC M10 Manuals

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This chapter describes the latest information that has been confirmed after SPARC M10 documentation is released and the contents of revisions in documentation.

- [Contents of Revisions in the Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual](#)

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### Contents of Revisions in the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual*

This section describes the contents of revisions in the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual*.

Note that the contents of revisions related to the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual* also apply to man pages unless otherwise specified. Furthermore, the descriptions have priority over information described in man pages.

**Table 8-1** Contents of Revisions in the *Fujitsu SPARC M12 and Fujitsu M10/SPARC M10 XSCF Reference Manual*

| Section Number or Command | Contents of Revision                                                                           |
|---------------------------|------------------------------------------------------------------------------------------------|
|                           | The following two commands are added.<br>sethwproperty(8) command<br>showhwproperty(8) command |

## sethwproperty(8) command

|                             |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
|-----------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>NAME</b>                 | sethwproperty - Sets the hardware property.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>SYNOPSIS</b>             | <b>sethwproperty</b> [[-q] -{y   n}] -p <i>ppar_id</i> -s <i>ssb_mitigation</i> ={enable   disable}<br><b>sethwproperty</b> -h                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>DESCRIPTION</b>          | sethwproperty is a command to set the hardware property for each physical partition (PPAR).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>Privileges</b>           | To execute this command, any of the following privileges is required.<br>platadm, fieldeng, pparadm<br>For details on user privileges, see setprivileges(8).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>OPTIONS</b>              | The following options are supported.<br>-h Displays the usage. Specifying this option with another option or operand causes an error.<br>-n Automatically responds to prompt with "n" (no).<br>-p <i>ppar_id</i> Specifies the ID of the PPAR that is to be configured. Depending on the system configuration, you can specify an integer from 0 to 15 for <i>ppar_id</i> .<br>-q Prevents display of messages, including prompt, for standard output.<br>-s <i>ssb_mitigation</i> ={enable   disable}<br>Sets the hardware-based mitigation mode for Speculative Store Bypass.<br>enable (Default)<br>Select to enable the hardware-based mitigation mode for CVE-2018-3639 (Spectre Variant4).<br>disable<br>Select to disable the hardware-based mitigation mode for CVE-2018-3639 (Spectre Variant4).<br>-y Automatically responds to prompt with "y" (yes). |
| <b>OPERANDS</b>             | None                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>EXTENDED DESCRIPTION</b> | This command cannot be set while the PPAR is in operation. If you specify a PPAR that is not powered off, the system returns an error.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>EXAMPLES</b>             | <b>EXAMPLE 1</b><br>Enable the hardware-based mitigation mode for PPAR-ID 0.<br>XSCF> <b>sethwproperty -p 0 -s ssb_mitigation=enable</b><br>SSB Mitigation for the PPAR will be changed to enabled.<br>Continue? [y   n] : <b>y</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| <b>EXIT STATUS</b>          | The following exit values are returned.<br>0 Indicates normal end.<br>>0 Indicates error occurrence.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>SEE ALSO</b>             | showhwproperty(8)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |

## showhwproperty(8) command

|                    |                                                                                                                                                                                                                                                                                                                  |
|--------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>NAME</b>        | showhwproperty - Displays the hardware property settings.                                                                                                                                                                                                                                                        |
| <b>SYNOPSIS</b>    | <b>showhwproperty</b> -p <i>ppar_id</i><br><b>showhwproperty</b> -h                                                                                                                                                                                                                                              |
| <b>DESCRIPTION</b> | showhwproperty displays the hardware property set for the physical partition (PPAR).                                                                                                                                                                                                                             |
| <b>Privileges</b>  | To execute this command, any of the following privileges is required.<br>platadm, fieldeng, pparadm<br>For details on user privileges, see setprivileges(8).                                                                                                                                                     |
| <b>OPTIONS</b>     | The following options are supported.<br>-h Displays the usage. Specifying this option with another option or operand causes an error.<br><br>-p <i>ppar_id</i> Specifies the ID of the PPAR to be displayed. Depending on the system configuration, you can specify an integer from 0 to 15 for <i>ppar_id</i> . |
| <b>OPERANDS</b>    | None                                                                                                                                                                                                                                                                                                             |
| <b>EXAMPLES</b>    | <b>EXAMPLE 1</b><br>Displays the hardware property of PPAR-ID 0.<br>XSCF> <b>showhwproperty -p 00</b><br>SSB Mitigation : enabled                                                                                                                                                                                |
| <b>EXIT STATUS</b> | The following exit values are returned.<br>0 Indicates normal end.<br>>0 Indicates error occurrence.                                                                                                                                                                                                             |
| <b>SEE ALSO</b>    | sethwproperty(8)                                                                                                                                                                                                                                                                                                 |

