

Supercomputer "Fugaku" and Beyond

June 23rd, 2020

Naoki Shinjo Senior Vice President, Head of Platform Development Unit, Corporate Executive Officer FUJITSU LIMITED

From "K computer" to Supercomputer "Fugaku"





- K computer was shut down in August, 2019 after 7 years of operation
- The last rack of Supercomputer "Fugaku" was delivered in May 2020
 - Software adjustment is ongoing for the general use scheduled in FY2021
- Fugaku tech FX1000 & FX700 are GA already, and A64FX CPU is supplied to HPE

Copyright 2020 FUJITSU LIMITED

Benchmark Results and "Fugaku" Specification



		K computer, SPARC64 VIIIfx	Fugaku, A64FX
System spec.	Instruction set architecture	SPARC-V9 HPC-ACE	Armv8.2-A SVE 512bit
	Double precision performance	11.28 Petaflops	537 Petaflops
	Half precision (AI) performance	-	2.15 Exaflops in half precision
	Total memory bandwidth	5.64 PB/s	163 PB/s
	Interconnect	Tofu interconnect	Tofu interconnect D
ISC 2020	TOP500 (Petaflops)	10.51 (#1 at June/Nov. of 2011)	415.5 (#1)
	HPCG (Petaflops)	0.6027 (#1 Nov. 2016~Nov. 2017)	13.366 (#1)
	HPL-AI (Petaflops)	_	1,421 (#1)
	Graph500 (GTEPS)	31,302.4 (2014~2019.6 #1)	70,980 (2020.6 #1)

- TOP500: Performance of solving the linear equation (dense matrix) Ax = b, which is common in science and engineering. The standard performance indicator for supercomputers
- HPCG: Performance of solving the linear equation with sparse coefficient matrix using Conjugate Gradient method
- HPL-AI: Performance of solving the linear equation utilizing lower precision floating point calc., like fp16, which AI often utilizes
- Graph500: Performance of big data processing. Integer and memory access speed related to the graph search/traverse are evaluated

Technical Innovations and Features of "Fugaku"



Faster applications: Leading-edge technology

- The world's first CPU w/ Arm's HPC extension, SVE, for which Fujitsu collaborated as a lead partner
- An arithmetic unit (512 bit SIMD) with 4x of K and half-precision (FP16) used in Al
- HBM2 for high memory BW
- Tofu interconnect D for efficient massively parallel processing (Enhanced Tofu for K)

Energy efficient and reliable: in-house hardware and software

- Original CPU, system, and software for stable and improved operation of large systems
- Efficient hardware with software control of power-saving features (power knob)
- Fujitsu's CPU design and leading-edge Si-tech to achieve world-class power efficiency

Ease and breadth of use: Adoption of industry standards

- Original CPU "A64FX" using Arm architecture widely used in smartphones and IOT devices
- Red Hat Enterprise Linux (RHEL 8.1), which is widely used for servers, etc.

"Fugaku" from Launch to Benchmark



■ Dec. 2nd, 2019 Shipment began

~ Lock-down around the world~

Supply chain (routes, etc.) was modified Work sharing w/ suppliers was optimized Manufacturing and assembly order were modified to minimize the impact of stockout Domestic production of substitutes was considered

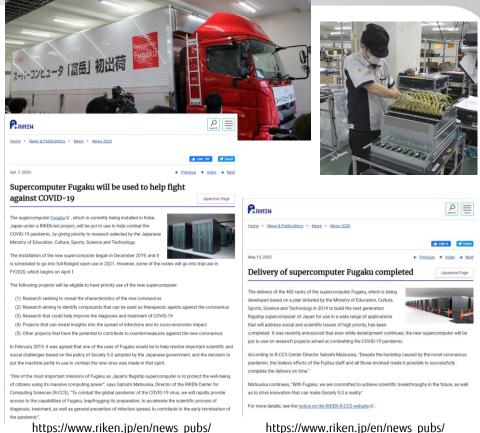
Apr. 7th, 2020 COVID-19 related research trial use of "Fugaku"

1/6 of computing resource of "Fugaku"

May 13th, 2020 Shipment completed

~ Benchmark run~

Work remotely, except for hardware replacement



news/2020/20200514 1/index.html

news/2020/20200407 1/index.html

COVID-19 Measures and Benchmark Results



■COVID-19 measures

Prevention of infection

Minimizing infection effects

- Work remotely, on-site to the minimum
- Avoid crowded trains. Sometimes walk instead
- Separate rooms and restrooms to reduce risk
- Thorough implementation of rules such as take & record temperature, sanitize hands, and wear mask
- Backup system dividing into more than two groups
- Range of disinfection and procedures were simulated

No infection, no delay, installation completed!

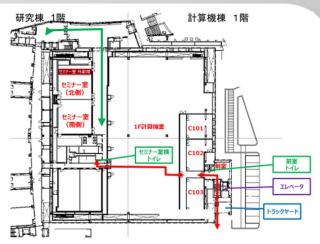
■Benchmark run succeeded thru *new normal* work style

RIKEN/Fujitsu benchmark teams

Fujitsu's engineers

Tight & close cooperation but remotely

RIKEN facilities operation team



22 guys in remote conference working on the benchmark at 21:50. May 29



"Fugaku" and Fujitsu Commercial Supercomputers



Lineup of supercomputers equipped with A64FX CPU

Fugaku "富岳"



Developed with RIKEN

- 150k nodes
- Water-cooled
- Tofu interconnect D
- Fujitsu / RIKEN original software

FUJITSU Supercomputer PRIMEHPC



PRIMEHPC FX1000 for highly scalable system

- 384 nodes/ Rack
- Water-cooled
- Tofu interconnect D
- Fujitsu original software



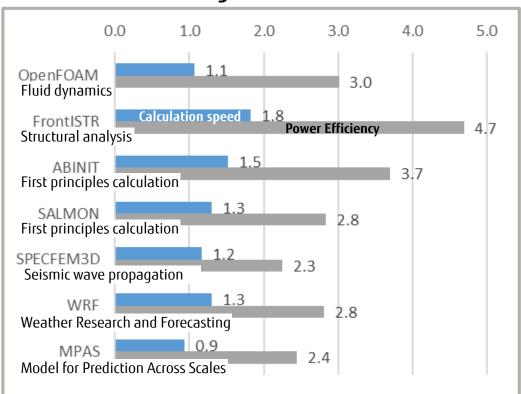
PRIMEHPC FX700 easier adoption w/ de facto tech

- 8 nodes/ 2U Chassis
- Air-cooled
- InfiniBand
- OSS/ ISV software

A64FX Performance and Power using OSS Real Apps



Performance scaling of a A64FX over 2x x86



- ■Latest x86 and A64FX w/ the same number of cores*
 - ■Up to 1.8 times faster
 - ■Up to 4.7 times lower energy
 - * A64FX : 48 cores ×1 CPU (2.2 GHz) x86 CPU : 24 cores ×2 CPU (2.9 GHz)
- Scalable performance obtained by
 - Enhanced microarchitecture for HPC
 - Energy-saving design & implementation

Commercial Applications



■ Fujitsu works with vendors to make commercial apps available for FX1000, also for FX700 and Fugaku with binary compatibility.

Available soon

In a research & development phase

(as of June 2020)

Engineering (Structural analysis, Fluid dynamics and Electronics)

LS-DYNA

(by Ansys, Inc.)

Poynting

(by Fujitsu Limited)

Chemistry*

Amber

Gaussian16

(by Gaussian, Inc.)

*Collaboration with Australian National University

(by Allied Engineering Co.)



(by Convergent Science)

Marc

(by MSC Software Ltd.)

VASP

ADVENTURECuster Altair Radioss™

(by Altair Engineering, Inc.)

HELYX

(by ENGYS Ltd. & VINAS Co., Ltd.)

scFLOW

(by Software Cradle Co., Ltd.)

Ansys Fluent

(by Ansys, Inc.)

(by JSOL Corporation)

Simcenter STAR-CCM+

(by Siemens Industry Software Inc.)

VPS (PAM-CRASH)

(by ESI Group)

**All application names used in this slide are trademarks or registered trademarks of their respective venders.

Global Partnership



- Supercomputer partnership with Cray (inherited to HPE)
 - HPE sells commercial supercomputer w/ A64FX

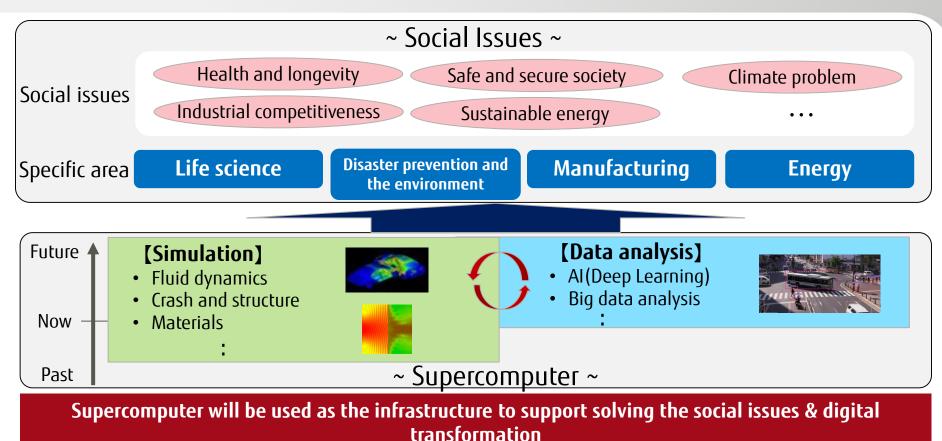
- Apps evaluation & ecosystem development on Arm SVE with overseas leading research institutes
 - Provides evaluation environment
 - Jülich Supercomputing Centre (Germany)
 - > EPCC (Britain), etc.
 - Collaboration to build up an ecosystem with Barcelona Supercomputing Center in Spain



https://www.cray.com/products/computing/ cs-series/fujitsu-arm-processor

Solving the Social Issues by Supercomputer





Summary



- Flagship supercomputer "Fugaku" is designed and developed thru co-design with RIKEN effort and Fujitsu's full range of over 40 years experience of development on hardware and software of supercomputers
- Ranked #1 in many benchmarks by design and implementation for high "Fugaku" application performance
- The world's fastest "Fugaku" performance is expected to help realize Society 5.0, which requires high speed simulation and AI capabilities
 - Advanced use for COVID-19 related research, for example
- Developing Fugaku tech, A64FX and PRIMEHPC FX1000 & FX700, into business globally

Fujitsu aims to provide a prosperous future through the fruits of supercomputers around the world. With the advent of the digital age, high speed simulation with Al capable supercomputers will be widely used in many fields.



shaping tomorrow with you