



Appendix

**Specifications of “MB95330H Series”**

SPEC	MB95F332H	MB95F333H	MB95F334H	MB95F332K	MB95F333K	MB95F334K
ROM capacity	8Kbyte	12Kbyte	20Kbyte	8Kbyte	12Kbyte	20Kbyte
RAM capacity	240byte	496byte	1008byte	240byte	496byte	1008byte
Low-voltage detection reset	No			Yes		
Reset input	Dedicated			Selected by software		
Operating volt.	2.4V~5.5V					
Clock	Selectable from external OSC clock, or On-chip CR OSC (after implementation: accuracy assurance $\pm 2\%$ )					
Motor control	Output 3-phase waves for brushless DC motor control					
Timers	8/16-bit composite timers(selectable from PWC,PWM, Capture), 8/16-bit PPG timer,16-bit reload timer, time-base timer					
Communication	LIN-UART,UART/SIO,I <sup>2</sup> C					
A/D converter	8ch (8-bit/10-bit resolution)					
Package	LQFP,32 pins SH-DIP,32 pins QFN,32 pins					