

Key specifications of the MB86H01 multi-decoder

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|------------------------|-----------------------|---|
| Internal CPU | | ARC Tangent-A4 (202.5MHz) ARM7TDMI-S™ (67.5MHz): for H.264 video decoding |
| Video | Profile | H.264 main profile / Level 3.0 decoder, MPEG-2 video main profile / main level decoder |
| | Video Encoder | Supports PAL/NTSC/SECAM, 5-channel video DAC embedded, supports Teletext/WSS/PDC/CC/VBID |
| | Interface | ITU-R BT.656 input, digital RGB888 output, YCrCb analogue SD output |
| Audio | Formats | MPEG-1/2 Layer I/II |
| | Channels | 2-channel |
| | Interface | L/R serial, I ² S, S/P-DIF |
| TS processing | Format | MPEG-2 TS |
| | Interface | 3 input streams, built-in DVB descrambler |
| | Encryption processing | 3DES encryption/decryption |
| DDR Memory Interface | | 16-bit DDR-SDRAM 135MHz, 128Mbit ~ 512Mbit SDRAM |
| Flash Memory Interface | | Supports Serial Flash, NOR Flash, NAND Flash |
| Display | | 6 planes: BG (back ground), video, OSD x 4 |
| USB | | USB 2.0 high-speed OTG controller (UPLI interface) |
| ATA | | Multiword DMA ATA interface |
| UPI | | NAND/NOR FLASH, common interface |
| Peripheral I/O | | UART, Smart Card x 2, I ² C, GPIO, PWM, IR Rx/Tx |
| Input Clock Frequency | | 27MHz |
| Operating Frequency | | Internal bus clock: 135MHz DDR memory interface: 135MHz |
| Package | | PBGA 256-pin, 27 x 27mm (ball pitch 1.27mm) FBGA 240-pin, 10 x10mm (ball pitch 0.5mm) |
| Operating Voltage | | Core: 1.2V, I/O: 3.3V DDR memory interface: 2.5V |

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