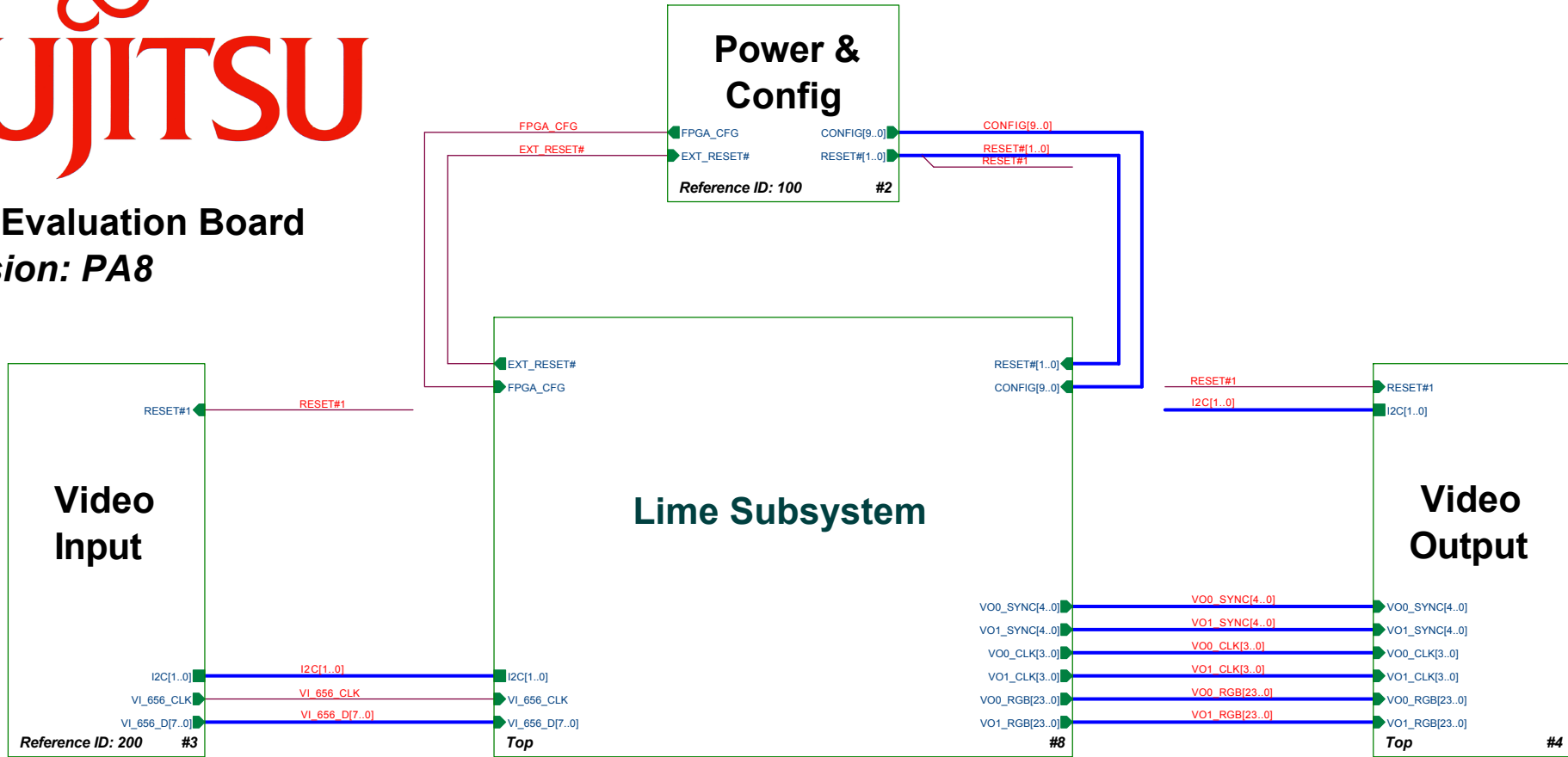


Lime Evaluation Board

Revision: PA8



Lime Evaluation Board	
Hierarchy	Part References
+---#1 Lime Eval Top	
---#2 Power	1xx
+---#3 Video Input	2xx
+---#4 Video Output	
+---#5 RGB Output	3xx
+---#6 DVI Output	4xx
+---#7 LVDS Output	5xx
+---#8 Lime Subsystem	
+---#9 Lime	6xx
+---#10 FPGA	7xx
+---#11 DRAM	8xx

I2C Address Map				
Device	Function	Write	Read	
Ux00 SAA7113	composite video input	0x48	0x49	
Ux00 XC2S200E*	video input selector and host CPU bus mux	0x60	0x61	
Ux00 SIL164	DVI transmitter #0	0x70	0x71	
Ux01 SIL164	DVI transmitter #1	0x72	0x73	

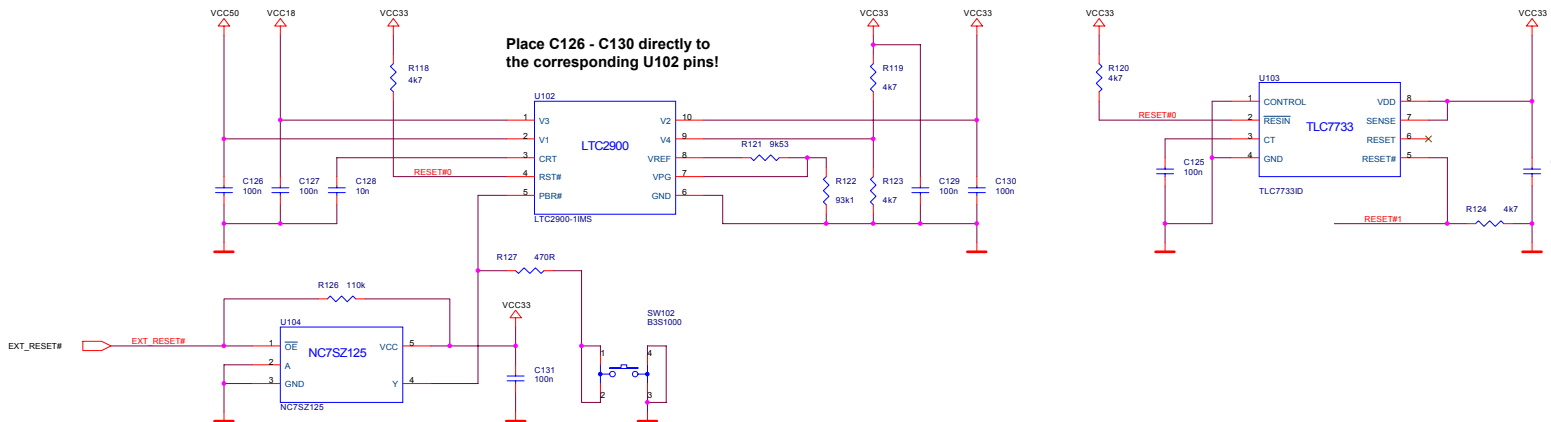
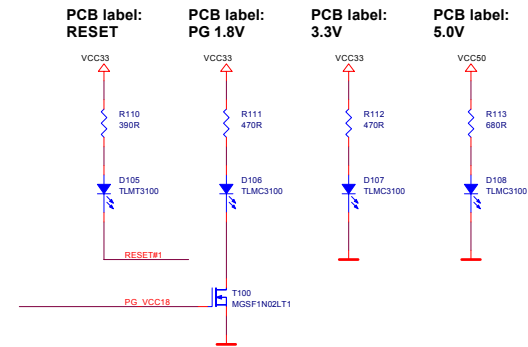
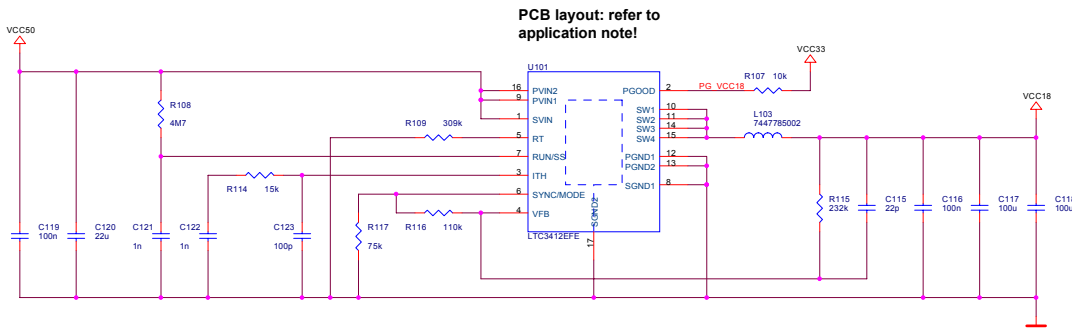
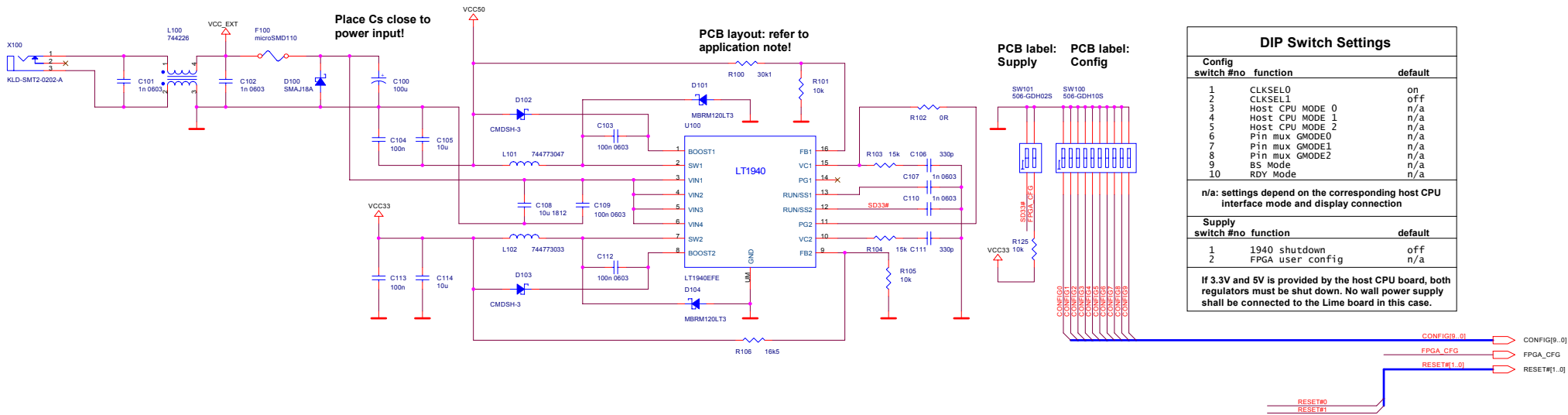
* I2C address of video input selector depends on FPGA implementation

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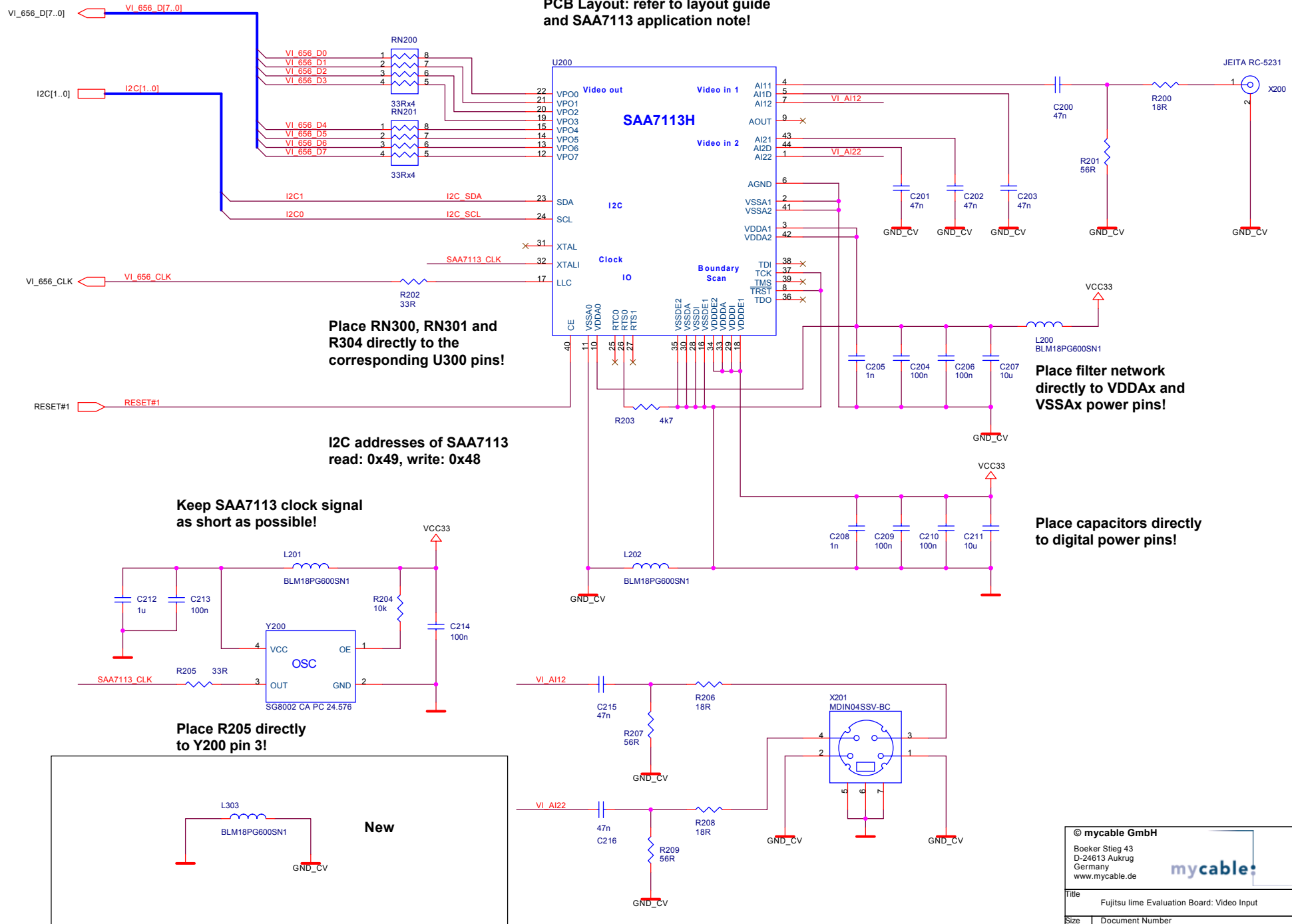
Title: Fujitsu Lime Evaluation Board

Size: A3	Document Number: 30200-001	Rev: PA8
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Date: Tuesday, December 12, 2006 Sheet 1 of 11



PCB Layout: refer to layout guide and SAA7113 application note!



Place RN300, RN301 and R304 directly to the corresponding U300 pins!

I2C addresses of SAA7113 read: 0x49, write: 0x48

Keep SAA7113 clock signal as short as possible!

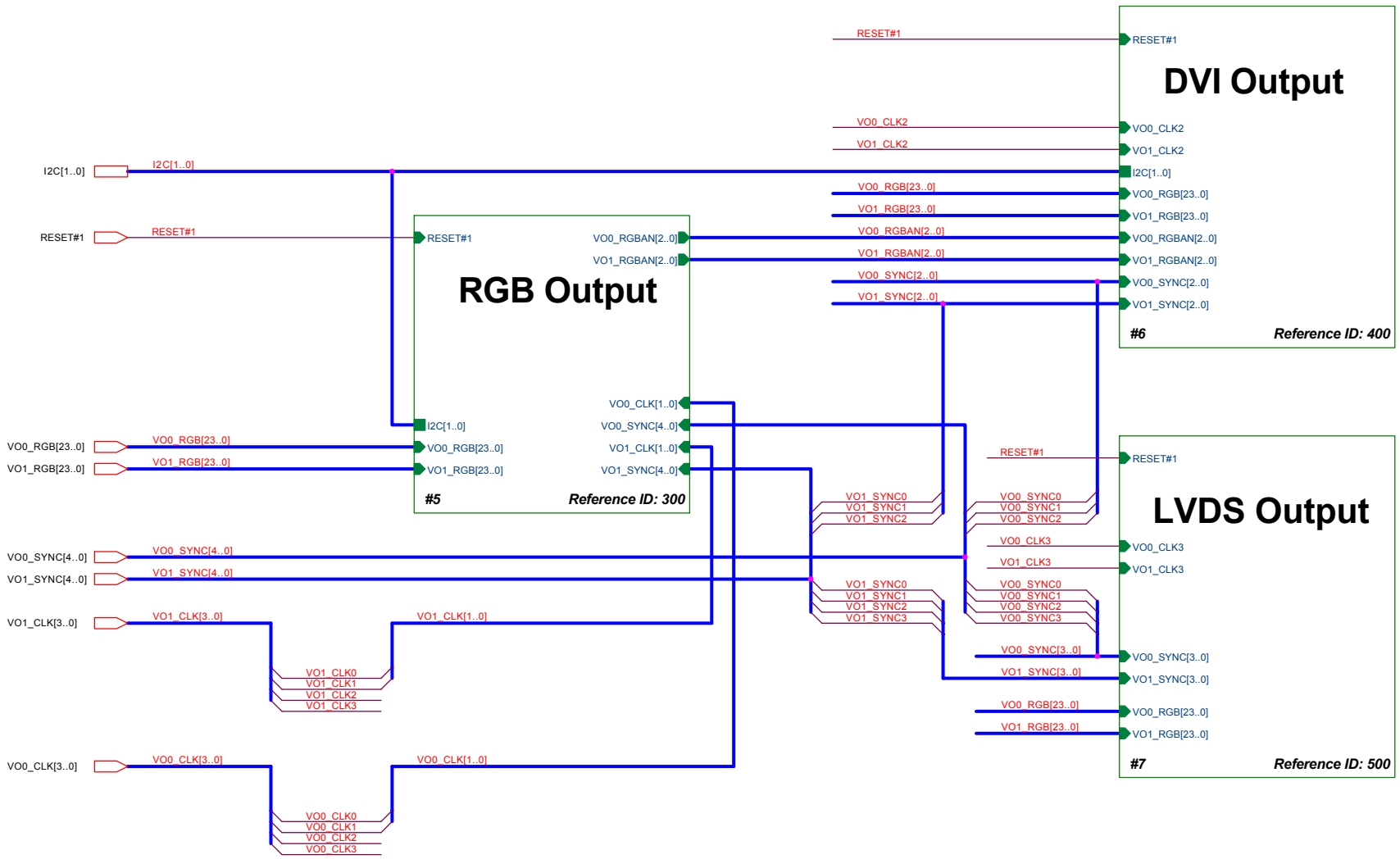
Place filter network directly to VDDAx and VSSAx power pins!

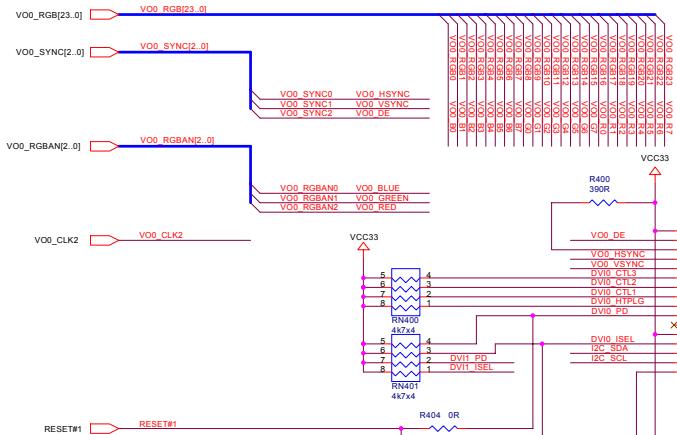
Place capacitors directly to digital power pins!

Place R205 directly to Y200 pin 3!

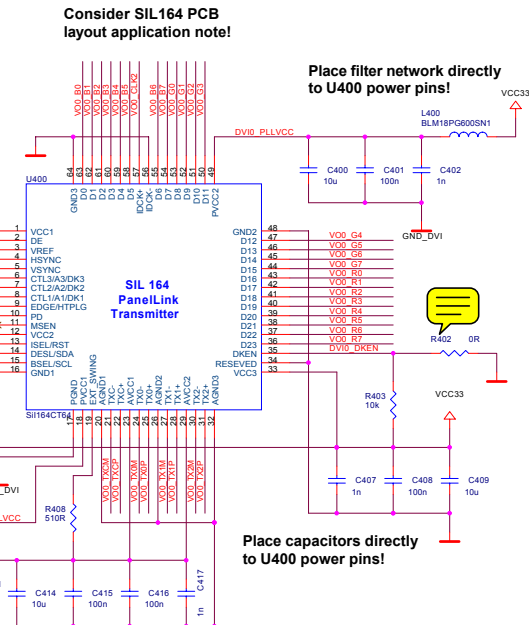
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Title	Fujitsu lime Evaluation Board: Video Input	
Size	Document Number	Rev
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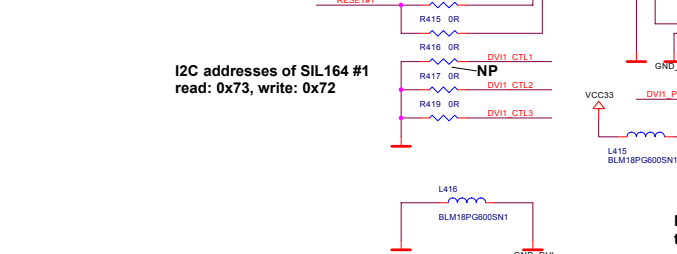
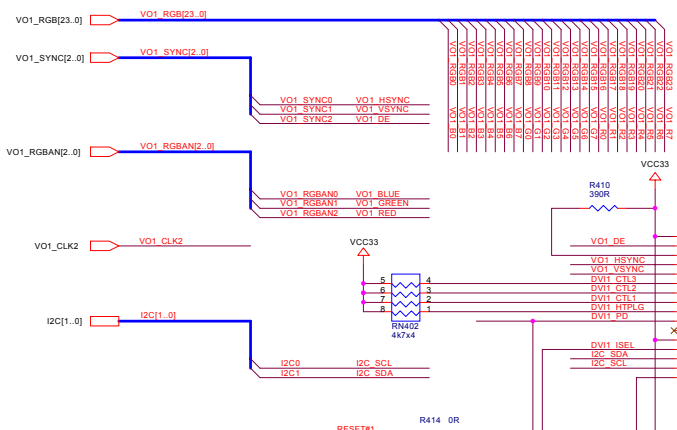
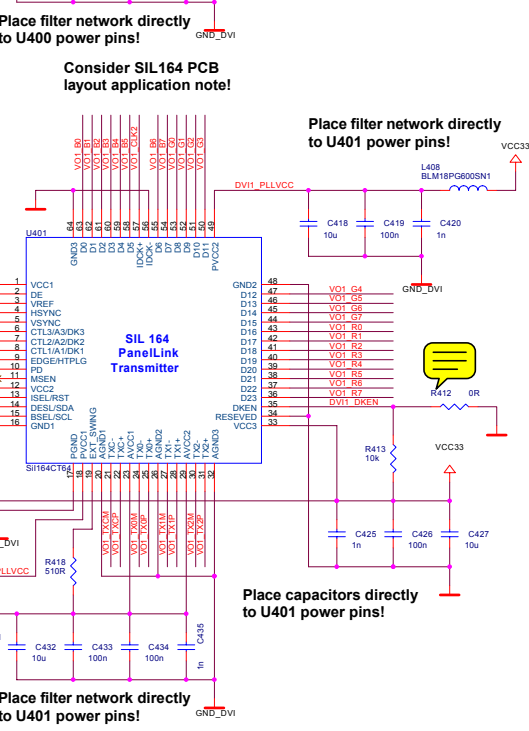




I2C addresses of SIL164 #0
read: 0x71, write: 0x70



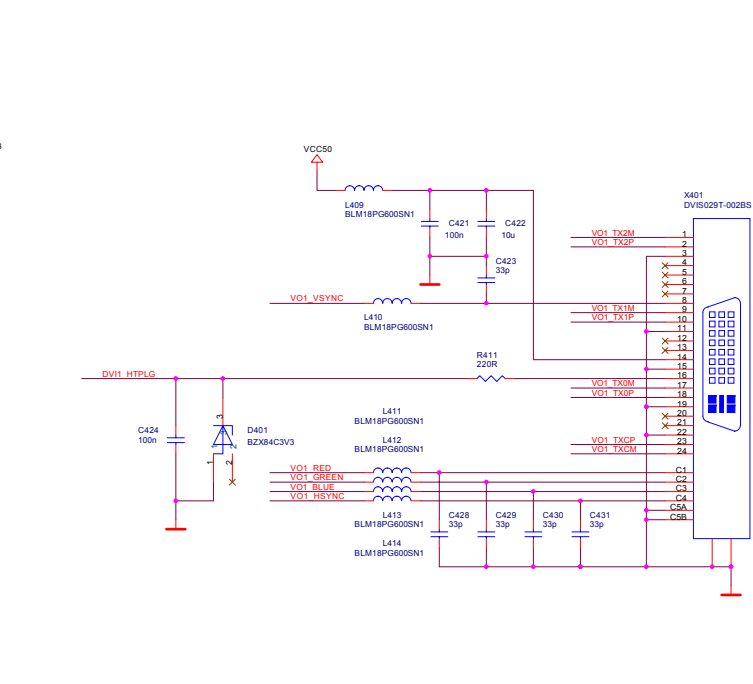
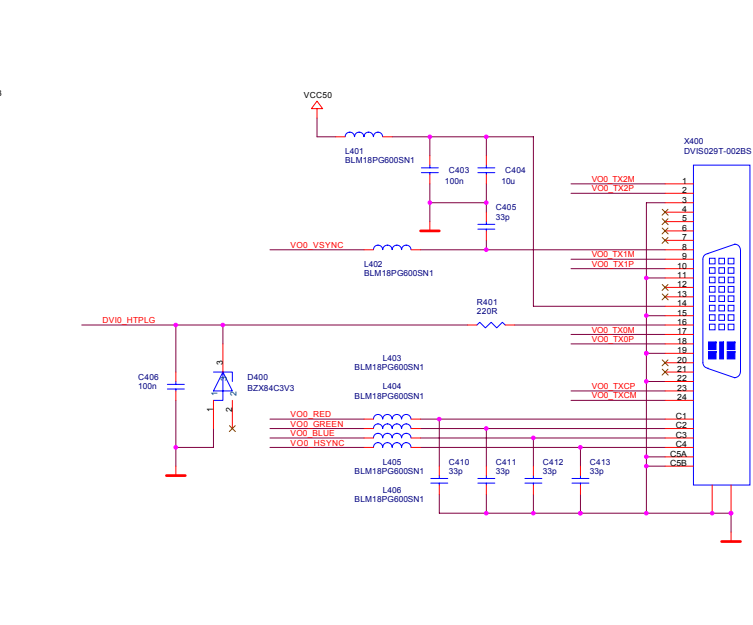
I2C addresses of SIL164 #1
read: 0x73, write: 0x72



Place filter network directly to U401 power pins!

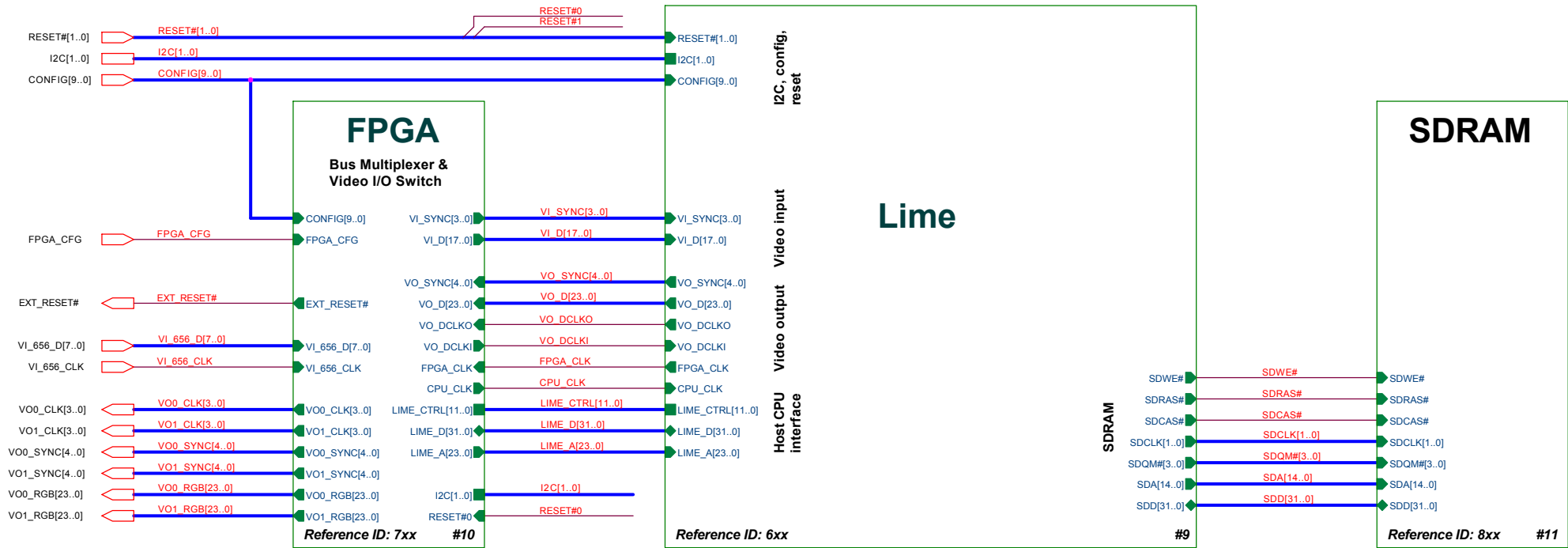
Place capacitors directly to U400 power pins!

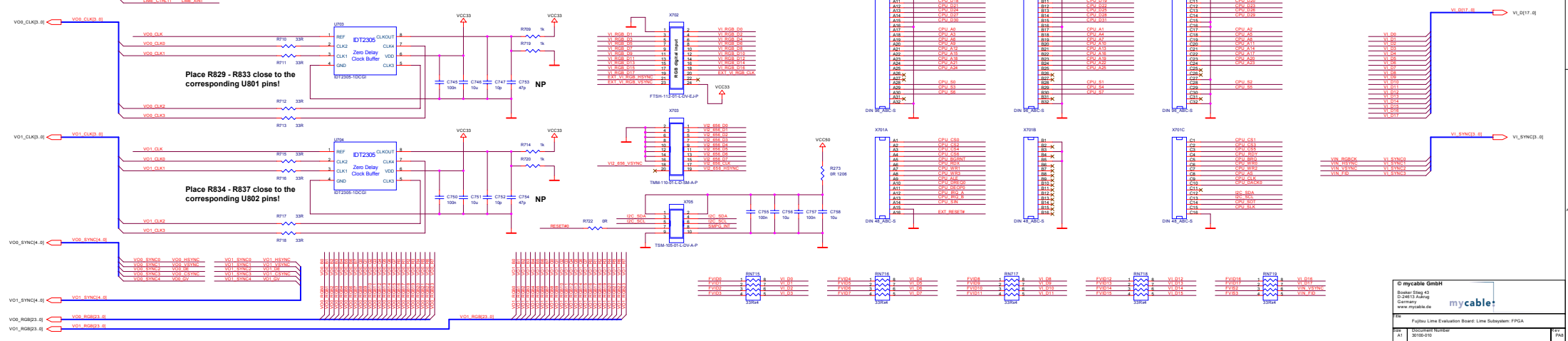
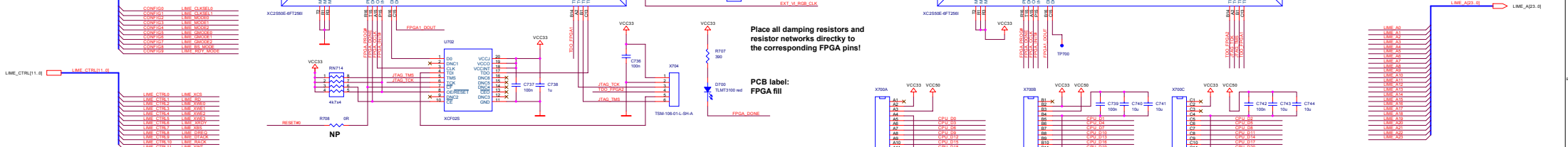
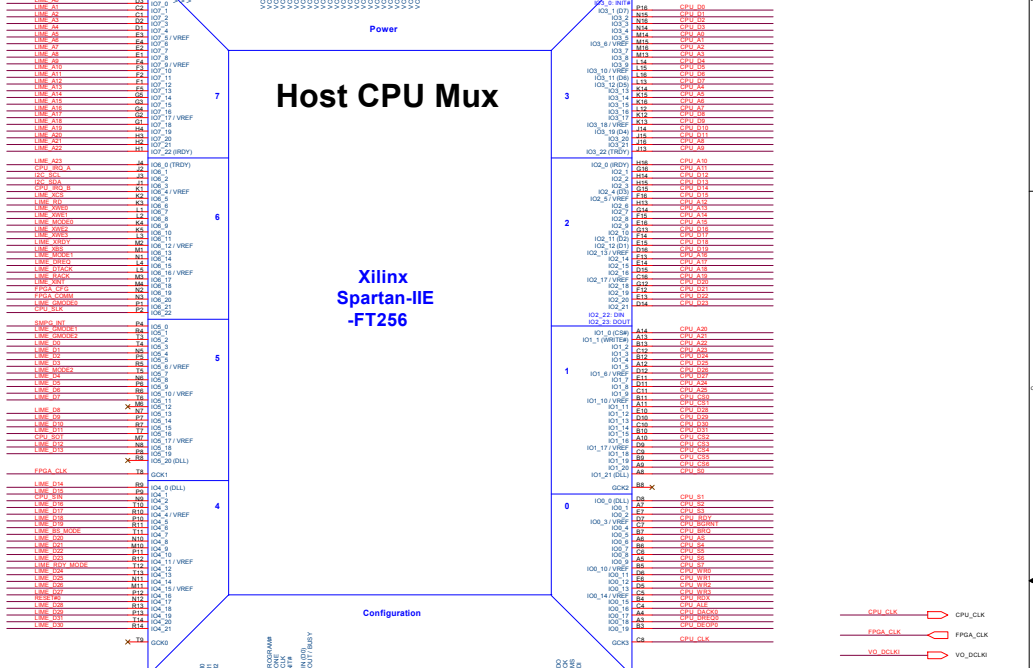
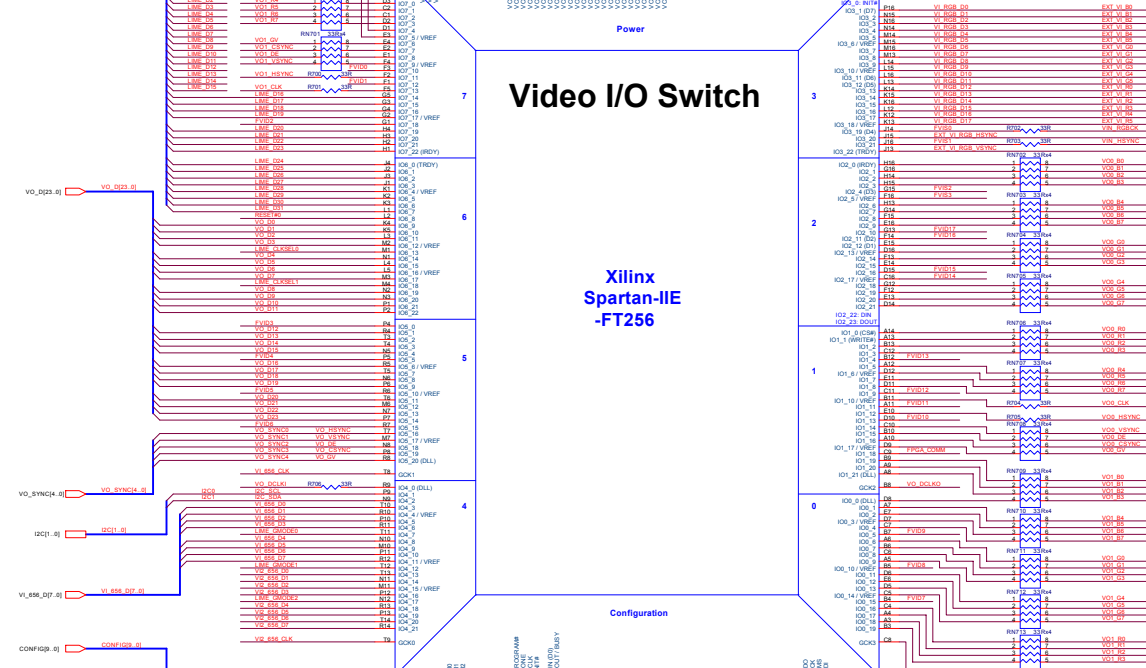
Place capacitors directly to U400 power pins!

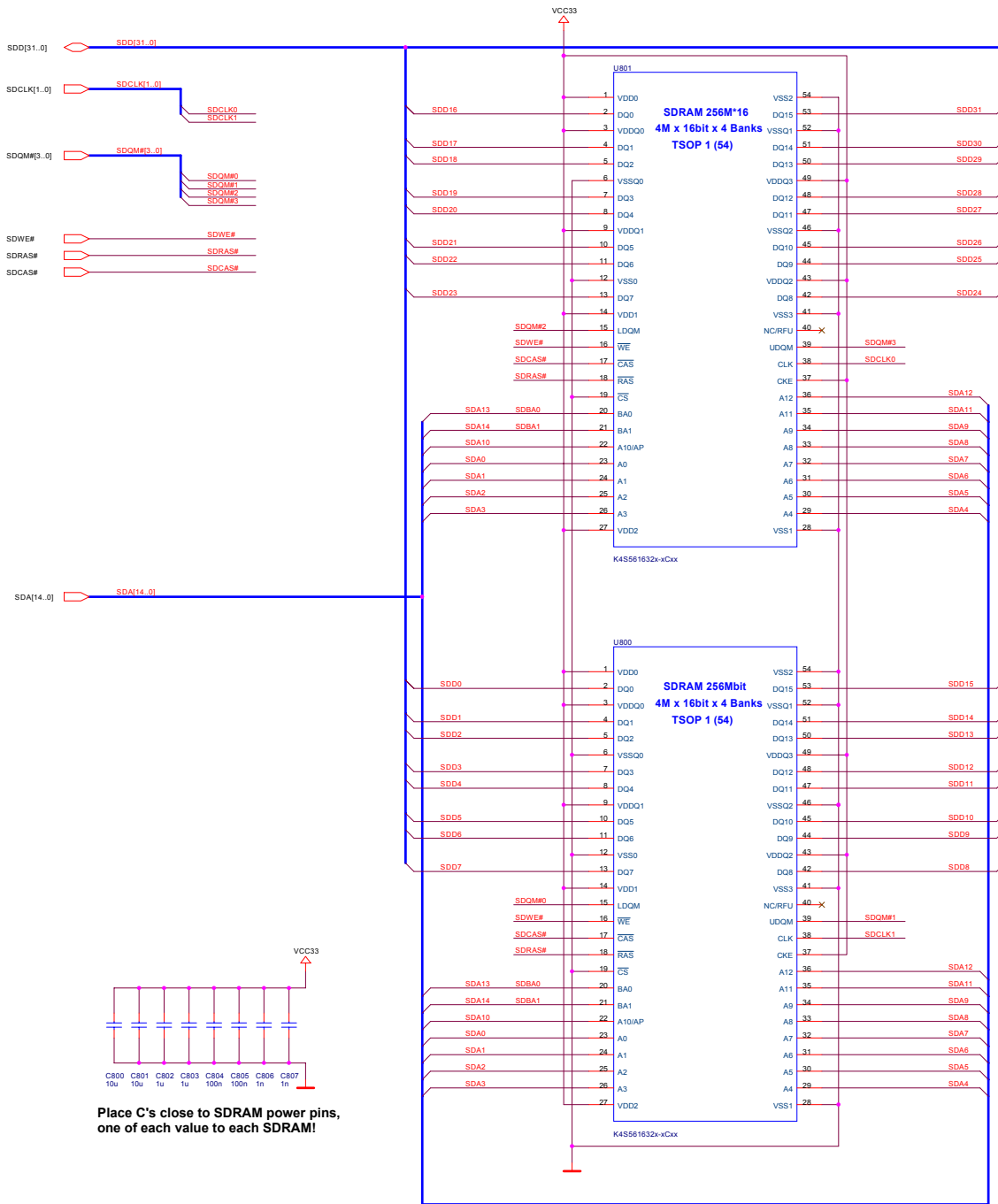


Place filter network directly to U401 power pins!

Place capacitors directly to U401 power pins!







Place C's close to SDRAM power pins,
one of each value to each SDRAM!