

Warranty and Disclaimer

To the maximum extent permitted by applicable law, Fujitsu Microelectronics Europe GmbH restricts its warranties and its liability for **all products** (e.g. software include or header files, application examples, application Notes, target boards, evaluation boards, engineering samples of IC's etc.), its performance and any consequential damages, on the use of the Product in accordance with (i) the terms of the License Agreement and the Sale and Purchase Agreement under which agreements the Product has been delivered, (ii) the technical descriptions and (iii) all accompanying written materials. In addition, to the maximum extent permitted by applicable law, Fujitsu Microelectronics Europe GmbH disclaims all warranties and liabilities for the performance of the Product and any consequential damages in cases of unauthorised decompiling and/or reverse engineering and/or disassembling. **Note, all these products are intended and must only be used in an evaluation laboratory environment.**

1. Fujitsu Microelectronics Europe GmbH warrants that the Product will perform substantially in accordance with the accompanying written materials for a period of 90 days from the date of receipt by the customer. Concerning the hardware components of the Product, Fujitsu Microelectronics Europe GmbH warrants that the Product will be free from defects in material and workmanship under use and service as specified in the accompanying written materials for a duration of 1 year from the date of receipt by the customer.
2. Should a Product turn out to be defect, Fujitsu Microelectronics Europe GmbH's entire liability and the customer's exclusive remedy shall be, at Fujitsu Microelectronics Europe GmbH's sole discretion, either return of the purchase price and the license fee, or replacement of the Product or parts thereof, if the Product is returned to Fujitsu Microelectronics Europe GmbH in original packing and without further defects resulting from the customer's use or the transport. However, this warranty is excluded if the defect has resulted from an accident not attributable to Fujitsu Microelectronics Europe GmbH, or abuse or misapplication attributable to the customer or any other third party not relating to Fujitsu Microelectronics Europe GmbH.
3. To the maximum extent permitted by applicable law Fujitsu Microelectronics Europe GmbH disclaims all other warranties, whether expressed or implied, in particular, but not limited to, warranties of merchantability and fitness for a particular purpose for which the Product is not designated.
4. To the maximum extent permitted by applicable law, Fujitsu Microelectronics Europe GmbH's and its suppliers' liability is restricted to intention and gross negligence.

NO LIABILITY FOR CONSEQUENTIAL DAMAGES

To the maximum extent permitted by applicable law, in no event shall Fujitsu Microelectronics Europe GmbH and its suppliers be liable for any damages whatsoever (including but without limitation, consequential and/or indirect damages for personal injury, assets of substantial value, loss of profits, interruption of business operation, loss of information, or any other monetary or pecuniary loss) arising from the use of the Product.

Should one of the above stipulations be or become invalid and/or unenforceable, the remaining stipulations shall stay in full effect.

Errata List:

February 14th, 2007

No	Item	Effected samples		
		DC : available beginning with date code xxxx X : effected --- : not effected		
		ES1 (MB86276) DC: 0552		
E1	Typo in Hardware Manual Rev 1.0	n/a		
E2	GMODE table incorrect in section 2.10 of Hardware Manual Rev 1.0	n/a		

E1:
Typo in Hardware Manual Rev 1.0

[back to top](#)

Detail

The description of the DCM0/1 (Display Control Mode 0/1) register contains a typo (Section 12.3, Page 182 of PDF). The description of Bit 0 to 1 should read:

- SYNC (Synchronize)
Set synchronization mode
- 0x** Non-interlace mode
 - 10 Interlace mode
 - 11 Interlace video mode0

E2:
GMODE table incorrect in section 2.10 in Hardware Manual Rev 1.0

[back to top](#)

Detail

The description of the Primary and Secondary RGB output formats for GMODE2 (3rd line) is incorrect. For L H L - Host Interface '16 bit CPU, I2C Slave' the Primary RGB output is RGB666 and the Secondary RGB output is RGB888.

General Purpose Mode Pins

GMODE	GMODE[2]	GMODE[1]	GMODE[0]	Host CPU Interface Mode (CPU Type Setting)	Pin Multiplex Functionality			GPIO
					Primary Display	Secondary Display	Video Capture	
0	L	L	L	8H3 (L L L) 8H4 (L L H) V832 (L H L) 8PARCite (L H H) Gen 32bit AD MUX CPU (H H L) I2C Slave (H H H)	RGB888	n/a	Native RGB666	n/a
1	L	L	H	8H3 (L L L) 8H4 (L L H) V832 (L H L) 8PARCite (L H H) Gen 32bit AD MUX CPU (H H L) I2C Slave (H H H)	RGB888	n/a	RBT666/601	GPIO[4:0]
2	L	H	L	General 16bit CPU (H L L) General 16bit AD MUX (H L H) I2C Slave (H H H)	RGB666	RGB888	RBT666/601	GPIO[2:0]
3	L	H	H	General 16bit CPU (H L L) General 16bit AD MUX (H L H) I2C Slave (H H H)	RGB666	RGB666	Native RGB666	n/a
4	H	L	L	RESERVED	RESERVED	RESERVED	RESERVED	RESERVED
5	H	L	H					
6	H	H	L					
7	H	H	H					