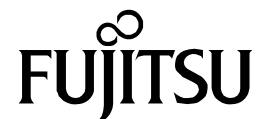


MB86R01 Application Note

DDR2 Controller

Self Refresh in STOP Mode

April, 2009 The 1.1 edition



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Revision History

Date	Ver.	Contents
2008/05/16	1.0	Newly issued
2008/07/10	1.0a	Added notes to i page
2009/04/22	1.1	Added description of "Resetting auto refresh interval"

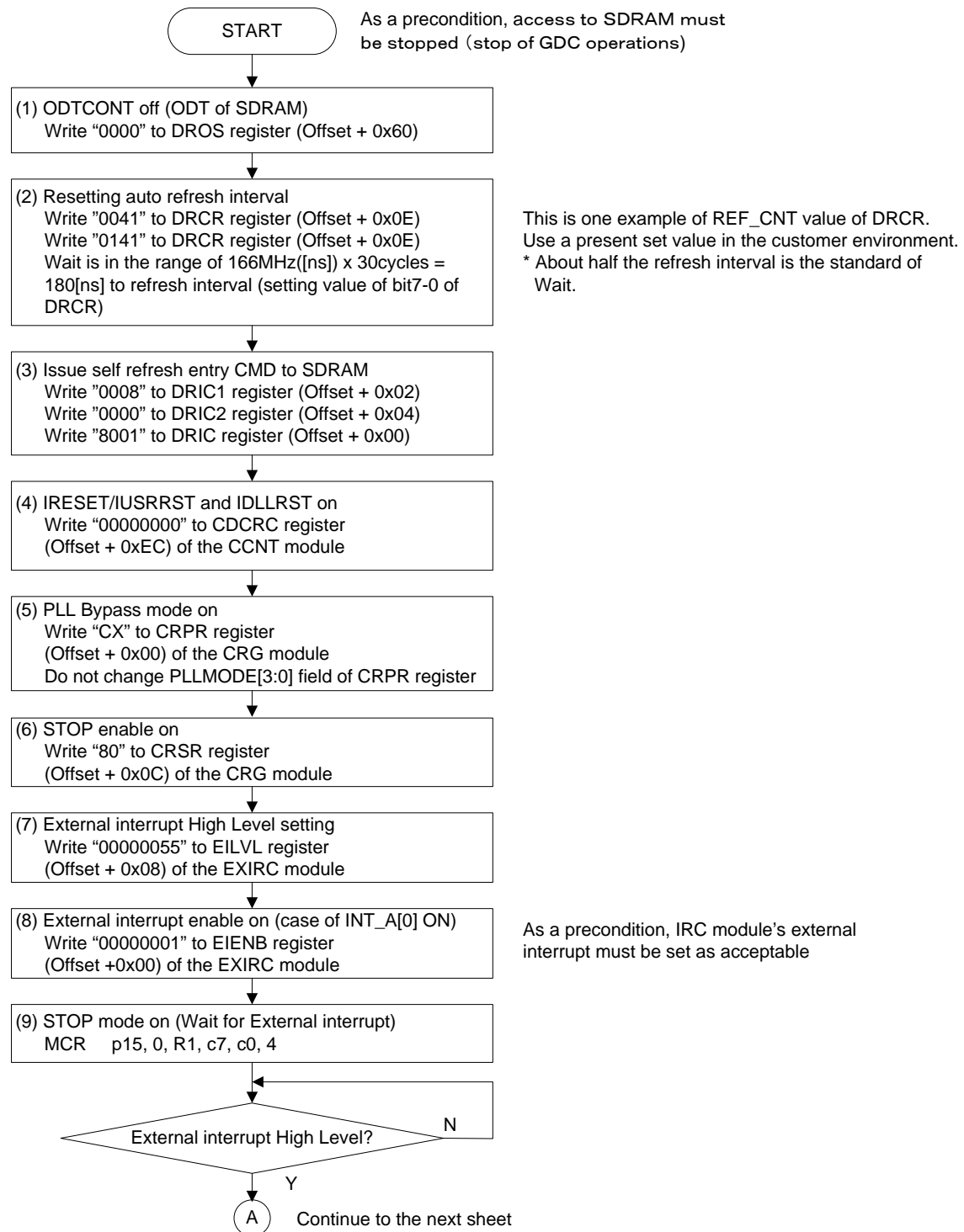
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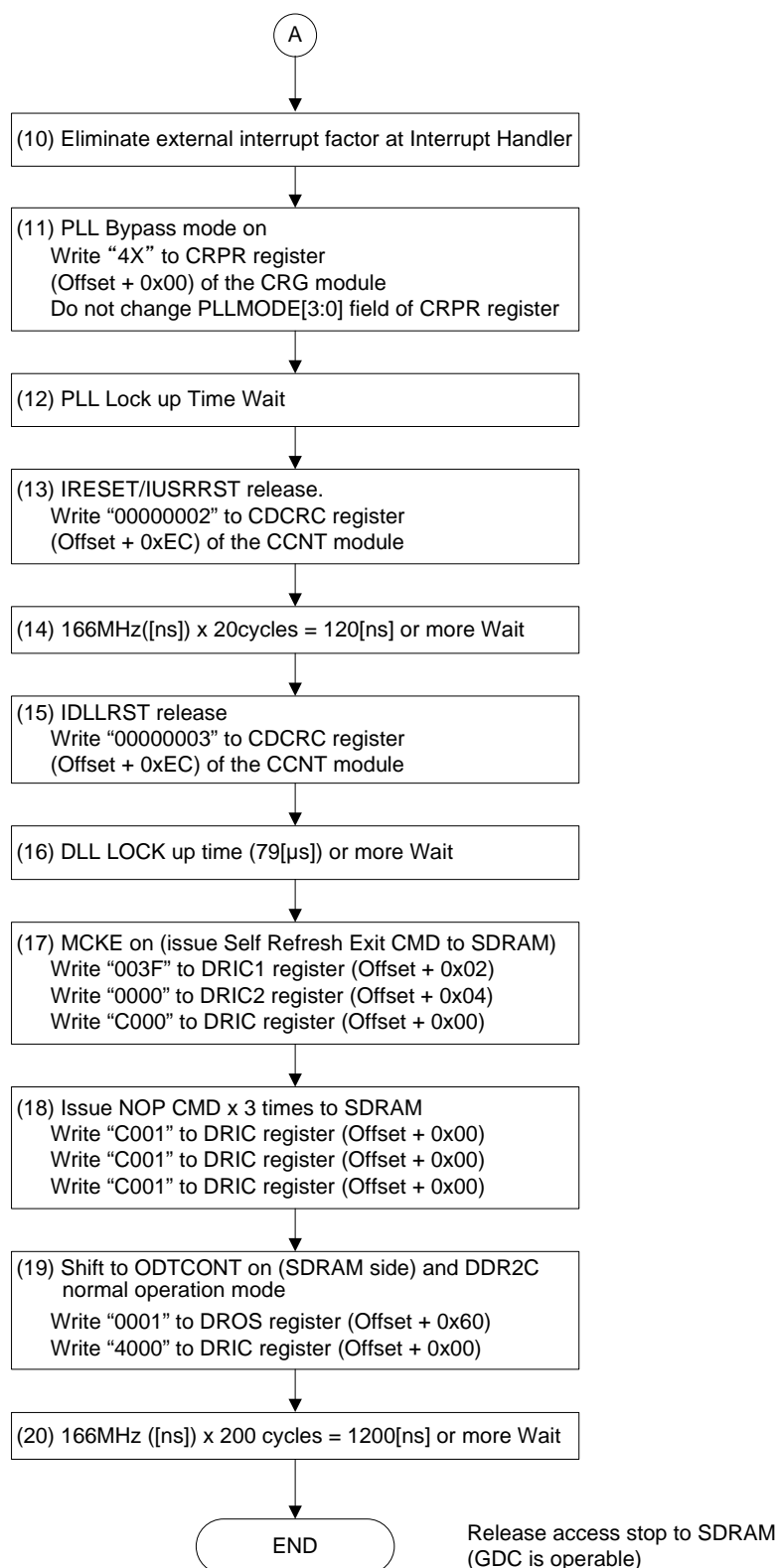
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1. DDR2 controller

1.1. Self refresh setting sample at STOP mode

In MB86R01, DDR2DRAM contents are able to be retained by self refresh command to DDR2SDRAM during STOP mode. Example of self refresh setting is shown below.






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