1. Video Capture Function

Some devices of Coral family have the video capture function.

The digital video stream of ITU RBT-656 or RGB666 format conformity is inputted. The each images are stored into the graphics memory, then read them to display on the L1-layer.

1.1 Image processing flow

The below is shown the image processing flow.

Note:

1. The Coral-LB is able to input ITU RBT-656 stream only and doesn’t support up-scaling function.

2. The image data don’t go through the up-scaling module when the scaling ratio is down.
- Non-interlace interpolation processing

When VI of a video capture mode register (VCM) is 0, an interlace screen is interpolated vertically using the data in the same field. A screen is doubled vertically. When VI is 1, it is not interpolated vertically.

- Horizontal low-pass filter processing

As a preprocessing when scaling down a picture horizontally, a low-pass filter can be covered horizontally. Regardless of scaling up and scaling down of a picture, ON/OFF is possible for a level low path filter (LPF).

The horizontal low-pass filter consists of FIR filters of five taps. A coefficient is specified in the following register.

<table>
<thead>
<tr>
<th>CHLPF_Y</th>
<th>Horizontal LPF Luminance element and RGB element coefficient code</th>
</tr>
</thead>
<tbody>
<tr>
<td>CHLPF_C</td>
<td>Horizontal LPF chrominance element coefficient code</td>
</tr>
</tbody>
</table>

The coefficient is specified by the coefficient code in two bits independently by luminance (Y) signal and chrominance (Cb and Cr) signals. The coefficient is a symmetric coefficient.

<table>
<thead>
<tr>
<th>CHLPF_x</th>
<th>K0</th>
<th>K1</th>
<th>K2</th>
<th>K3</th>
<th>K4</th>
</tr>
</thead>
<tbody>
<tr>
<td>00</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>01</td>
<td>0</td>
<td>1/4</td>
<td>2/4</td>
<td>1/4</td>
<td>0</td>
</tr>
<tr>
<td>10</td>
<td>0</td>
<td>3/16</td>
<td>10/16</td>
<td>3/16</td>
<td>0</td>
</tr>
<tr>
<td>11</td>
<td>3/32</td>
<td>8/32</td>
<td>10/32</td>
<td>10/32</td>
<td>3/32</td>
</tr>
</tbody>
</table>

Horizontal LPF becomes turning off (through) because of the setting of the coefficient code "00".

**Note:**
- In the case of Native RGB mode (NRGB=1), only a setup of CHLPF_Y code becomes effective.

- Down and Up scaling processing of horizontal direction

Please set bit15-00 of capture scale register (CSC) to do the down and up scaling processing of horizontal direction.

Horizontal direction is scaled down before writing in VRAM. Horizontal direction is scaled up after reading from VRAM.

The interpolation filter processing of luminance (Y) signal is done by cubic interpolation (Cubic Interpolate) method. The interpolation filter processing of chrominance (Cb and Cr) signal is done by BiLinear interpolation (BiLinear Interpolate) method. The
interpolation filter processing of Native-RGB signal is done by cubic interpolation (Cubic Interpolate) method.

- **Vertical low-pass filter processing**

  The low-pass filter can be put on the vertical direction as a preprocessing when the image is scaled down to the vertical direction. Vertical low-pass filter (LPF) can be set to turning on regardless of the scaling up or down of the vertical direction.

  A vertical low-pass filter is composed of the FIR filter of three taps. The coefficient is specified by the following register.

  - `CVLPF_Y` Vertical LPF Luminance element and RGB element coefficient code
  - `CVLPF_C` Vertical LPF chrominance element coefficient code

  The coefficient is specified by the coefficient code in two bits independently by luminance (Y) signal and chrominance (Cb and Cr) signals. The coefficient is a symmetric coefficient.
Vertical LPF becomes turning off (through) because of the setting of the coefficient code "00".

Note:

- In the case of Native RGB mode (NRGB=1), only a setup of CVLPF_Y code becomes effective.

- Down and up scaling processing of Vertical direction

Please set bit31-16 of capture scale register (CSC) to do the down and up scaling processing in the vertical direction.

The vertical direction is scaled down before writing in VRAM. The vertical direction is scaled up after reading from VRAM.

The interpolation filter processing of luminance (Y) signal is done by cubic interpolation (Cubic Interpolate) method. The interpolation filter processing of chrominance (Cb and Cr) signal is done by BiLinear interpolation (BiLinear Interpolate) method. The interpolation filter processing of Native-RGB signal is done by cubic interpolation (Cubic Interpolate) method.
1.2 Various parameters

The following shows the various parameters from the original image to displayed image.
1) Processing with the Capture Scaler

Compresses image to $\text{sc}_x \times \text{sc}_y$ size compared to the original image ($\text{cap}_x \times \text{cap}_y$). Vertical and horizontal compression ratios are set with the CSC register’s VSCI, VSCF, HSCI, HSCF. Refer to the *Scaling section* in the *Hardware manual* for details regarding methods of calculating setting values.

Also, the starting and ending points set by CIHSTR, CIVSTR, CIHEND and CIVEND are clipped at the frame after scaling and written to the buffer.

2) Processing with the Buffer Controller

Stores images passed through the buffer region scaler set by the CBOA/CBLA/CBW registers. The buffer is controlled using the ring buffer method.

3) Processing with the Display Controller

The display controller displays the data stored in the capture buffer in the L1 layer. The display controller always displays the latest captured frames.

In the WEAVE mode, the display controller generates frames from the odd and even fields for display.
1.3 Synchronization of frames

The capture buffer controller writes the sequentially captured image to the buffer. On the other hand, the display controller reads the captured-latest frames for display. The result is that frames can be skipped or the same frame can be displayed two or more times.

1) When capturing faster than display:

- In the operation as shown in the above figure, when the “Display pointer” reaches the last of frame 4, if the “Capture pointer” reaches the last of frame 6, in the worst case, a frame can be skipped. This prevents the “Capture pointer” from passing the “Display pointer.”
- To avoid passing the “Capture pointer,” it is necessary to allocate enough margin (2.2 frames) to the buffer.

2) When capturing more slowly than display:
1.4 Parameter setting values

The following determines each parameter.
The following are variable definitions.

(xs, ys): Image read starting point
(ws, hs): Original image size
(wd, hd): Size to display in window

1) Selecting non-interlacing

The non-interlacing mode is determined by the input image format and the display output size.

n: Raster count around the input image field.

\[ n \geq \text{hd} \]: BOB and WEAVE conversion unnecessary
\[ n < \text{hd} \leq 2n \]: BOB or WEAVE

When not using BOB and WEAVE, displays the odd field and even field alternately in the same position. However, in such cases, this is appropriate for displaying in comparatively small windows.

2) Setting compression rate

Refer Scaling section in the Hardware Manual to set compression rate.

\[ rh = \frac{ws}{wd} \]
\[ rv = \frac{hs}{hd} \]

Compression rate integers are set to HSCI and VSCI. Fractions are set to HSCF and VSCF.

\[ \text{HSCI} \times 2048 + \text{HSCF} = \text{floor}(rh\times2048+0.5) \]
\[ \text{VSCI} \times 2048 + \text{VSCF} = \text{floor}(rv\times2048+0.5) \]

*floor calculates the floor function (discard) of the value .

3) Setting image range to store in the capture buffer

\[ \text{CIHSTR} = \text{floor}(xs/rh+0.5) \]
\[ \text{CIVSTR} = \text{floor}(ys/rv+0.5) \]

for not WEAVE mode

\[ = \text{floor}(ys/rv/2+0.5) \]

for WEAVE mode

\[ \text{CIHEND} = \text{CIHSTR}+\text{wd}-1 \]
\[ \text{CIVEND} = \text{CIVSTR}+\text{hd}-1 \]

for not WEAVE mode

\[ = \text{CIVSTR}+\text{hd}/2-1 \]

for WEAVE mode

4) Setting capture buffer and W layer stride

\[ \text{CBW} = \text{WW} = \text{ceil}(\text{wd}/32) \]

*ceil calculates the ceiling function (round up) of the value .

5) Setting buffer size

\[ \text{CBLA} = \text{CBOA} + \text{ceil}(K\times\text{hd})\times\text{CBW}\times64 \]

K, here, shows the buffer size according to the frame size. This value is set to 2.5 from 2.2. K = 2.2 is a general value for use while K = 2.5 is a safer value.

6) Setting L1 layer parameter

\[ \text{L1WW} = xd \]
L1WH = \text{yd}^{-1}