



## MB91F479 Specifications

Product Name		MB91F479
Minimum Instruction Execution Speed		12.5 nanoseconds (80MHz)
Memory	ROM	512 kilobyte (Flash memory)
	RAM	32 kilobyte
Transmission Function	I <sup>2</sup> C Function (*1) UART (*2)	6 channel (ch)
Analog Function	12 Bit A/D	4 ch×2 Units, total 8 ch
	8/10 Bit A/D	12 ch×1 Unit
Timer Function	Multifunction Timer	1 Unit
	16 Bit Reload Timer	2 ch
	8/16 Bit PPG Timer	8 Bit × 8 ch or 16 Bit × 4 ch selectable
	16/32 Bit Base Timer	16 Bit × 4 ch or 32 Bit 2 ch selectable
	8/16 Bit Up-Down Counter	8 Bit × 2 ch or 16 Bit × 1 ch selectable
	Watchdog Timer	1 ch
External Interruptions		Maximum of 11
DMAC		5 ch
Multiplication and Addition Calculator Circuit		1 Unit (32-bit data × 32-bit data + 72-bit data = 72-bit data or 32-bit round-up or clipped data) Calculation results can be round up to 32-bit length
External BUS I/F		Supported
I/O Port		Maximum of 113
Voltage		4.0 - 5.5 Volts
Operating Temperature		From -40 to +85 deg C
Package		LQFP (*3) – 144 Pins (0.4mm Pitch, 16x16mm Body)

\*1. I<sup>2</sup>C: Inter-Integrated Circuit communication protocol. Purchase of Fujitsu I<sup>2</sup>C components conveys a license under the Philips I<sup>2</sup>C Patent Rights to use these components in an I<sup>2</sup>C system provided that the system conforms to the I<sup>2</sup>C Standard Specification as defined by Philips.

\*2. Universal Asynchronous Receiver Transmitter (UART): A macro for asynchronous serial communication that can also provide clock-synchronous communication.

\*3. Low-profile Quad Flat Package (LQFP): A type of plastic surface mount package that has a height of less than 1.7mm.