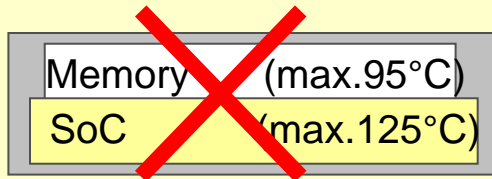
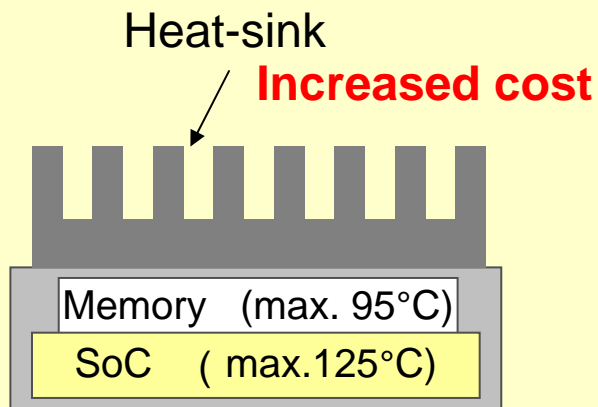


SiP with conventional memory



(a) SiP temperature: 105° C
<SiP Not Feasible>

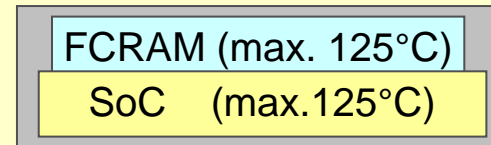
Or



(b) SiP temperature: 90° C
<SiP Feasible but High Cost>



SiP with 125° C-rated FCRAM



(c) SiP temperature: 105° C
<SiP Feasible>

(a) With an SoC with large power consumption, SiP temperature becomes 105°C, however conventional memory can only operate up to 95°C so it cannot use this configuration.

(b) By adding a heat-sink the SiP temperature decreases to an operational range, however there is increased cost.

(c) Using 125°C-rated FCRAM, even if the SiP temperature is 105°C it operates fine, and no heat-sink is necessary.