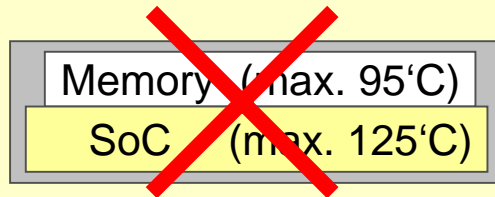


■ SiP with 95deg.C-rated Conventional Memory

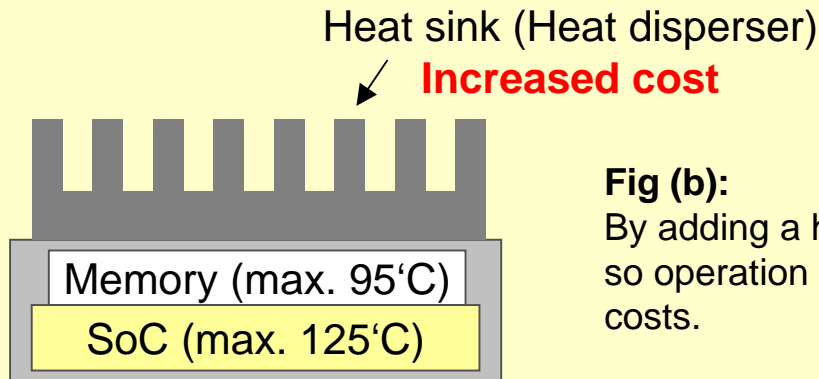


(a) SiP temperature: 105 deg.C
< SiP Not Feasible >

Fig (a):

With an SoC of high power consumption, the SiP temperature becomes 105 deg.C; however conventional memory can only operate up to 95 deg.C, so this configuration cannot be used.

or



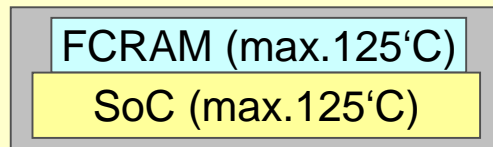
(b) SiP temperature: 90 deg.C
< SiP Feasible but High Cost >

Fig (b):

By adding a heat-sink the SiP temperature decreases so operation is feasible, however there is increased costs.

*: Temperature shown in () of Memory and SoC indicates maximum operating temperature.

■ SiP with 125deg.C-rated FCRAM



(c) SiP temperature: 105 deg.C
< SiP Feasible >

Fig (c):

Using 125 deg.C-rated FCRAM, even if the SiP temperature is 105 deg.C it operates fine, and no heat-sink is necessary.

*: Temperature shown in () of FCRAM and SoC indicates maximum operating temperature.