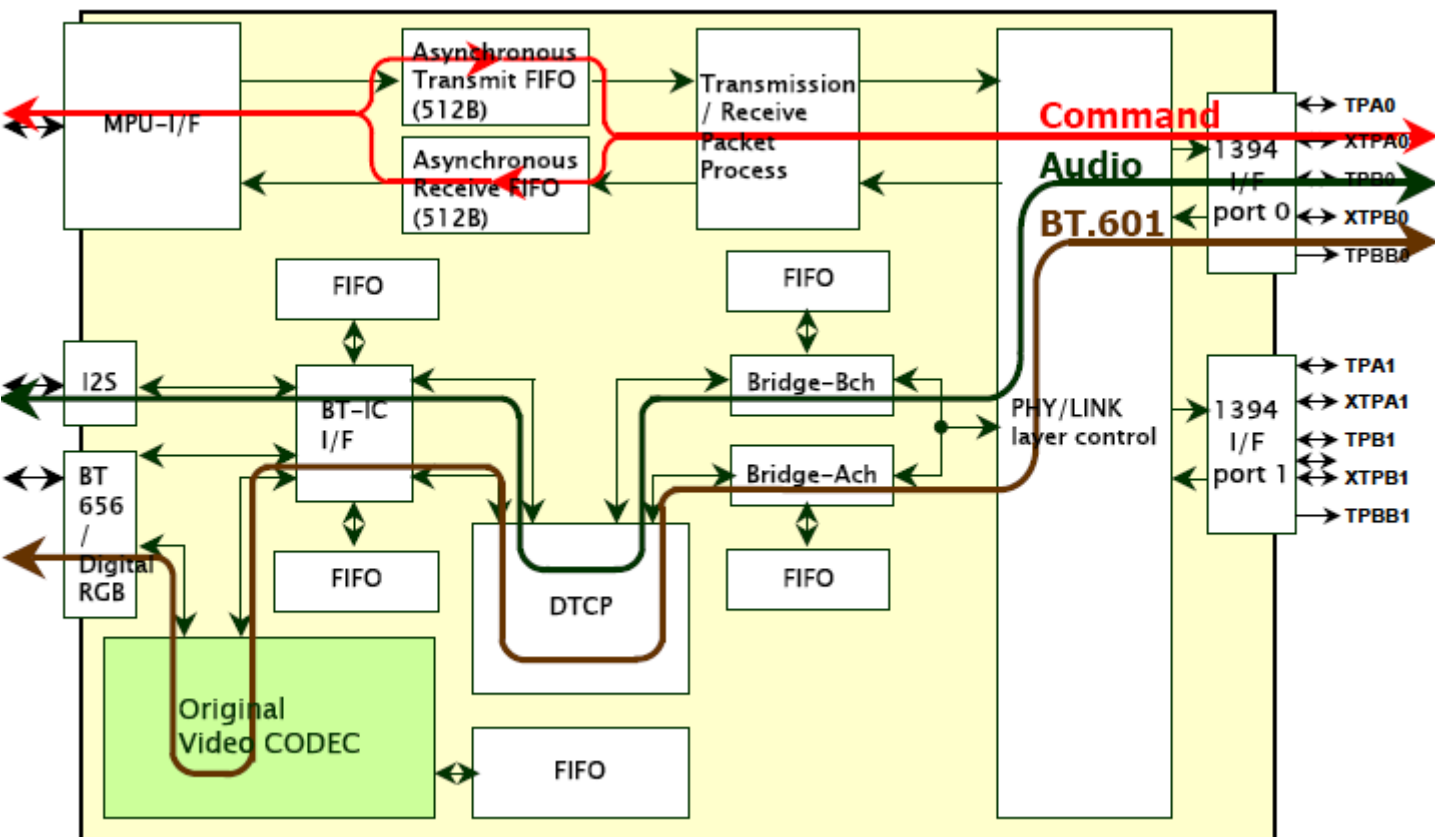


Integrated SmartCODEC

IDB-1394 Controller MB88388A**Features**

- Built in S400, 2port-PHY which is compliant with IEEE Std 1394b-2002
- Built in SmartCODEC [Fujitsu original Video codec]
 - Compression ratio : 1/3 constant
 - Encoding/Decoding time: 2~3ms
- Built in AV protocol (IEC61883) function
 - Correspondence packet format : BT.601[Part 8], Audio[Part 6]
 - Two channels simultaneous transmitting possible
- Video stream-I/F : BT656/Digital RGB x 1ch
- Audio stream-I/F : I²S (8ch) x 2ch
- Isochronous data-FIFO : 32Kbyte x 1ch, 4Kbyte x 1ch
- Content Protection for DTCP standard
 - Cipher/Decipher engine that can process two streams simultaneously
 - AKE accelerator : Oval operation, Digital signature generation/verification, Random numbers generation
- Asynchronous data-FIFO
 - Asynchronous transmit FIFO : 512byte
 - Asynchronous receive FIFO : 512byte
- Asynchronous data-I/F : 16bit MPU/DMA bus
- Power Supply : 3.3V(I/O), 1.8V(Internal)
- Packaging : LQFP-216

Block Diagram



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