

FUJITSU receives Cadence Collaboration Award

On November 1, 2006, FUJITSU received the Collaboration Award in the Cadence Design Chain Partner Event hosted by Cadence Design Systems, which is the biggest EDA in the U.S.

FUJITSU received this award due to the high assessment of our support in the development of Cadence products and in recognition of our cooperation in quality verification and standardization in the Whole Product Support category. Kazuyuki Kawauchi, the CEO of Fujitsu Microelectronics America, participated in the award ceremony and the commemorative plaque was presented by Mr. Michael J. Fister, the President and CEO of Cadence Design Systems.

This Collaboration Award was also received by ARM, TSMC, and ARC in other categories.

To solve problems in the design and manufacture of leading-edge products, Cadence Design Systems promotes collaboration among leading corporations in various fields of the electronics industry, which is called the Design Chain. It also develops its own tools and develops and provides optimal design and manufacture environments for designers.

As a member of this Design Chain, FUJITSU, a world's leading company in the design and manufacture of semiconductors, will continue to make efforts in solving the problems of the semiconductor field of the electronics industry and in playing the role of technical collaboration promoter demanded in next-generation electronics in cooperation with other industry leaders.



Kazuyuki Kawauchi, the CEO of the Fujitsu Microelectronics America (center) and Mr. Michael J. Fister, the President and CEO of Cadence Design Systems (right) at the Collaboration Award Ceremony

Commemorative plaque

