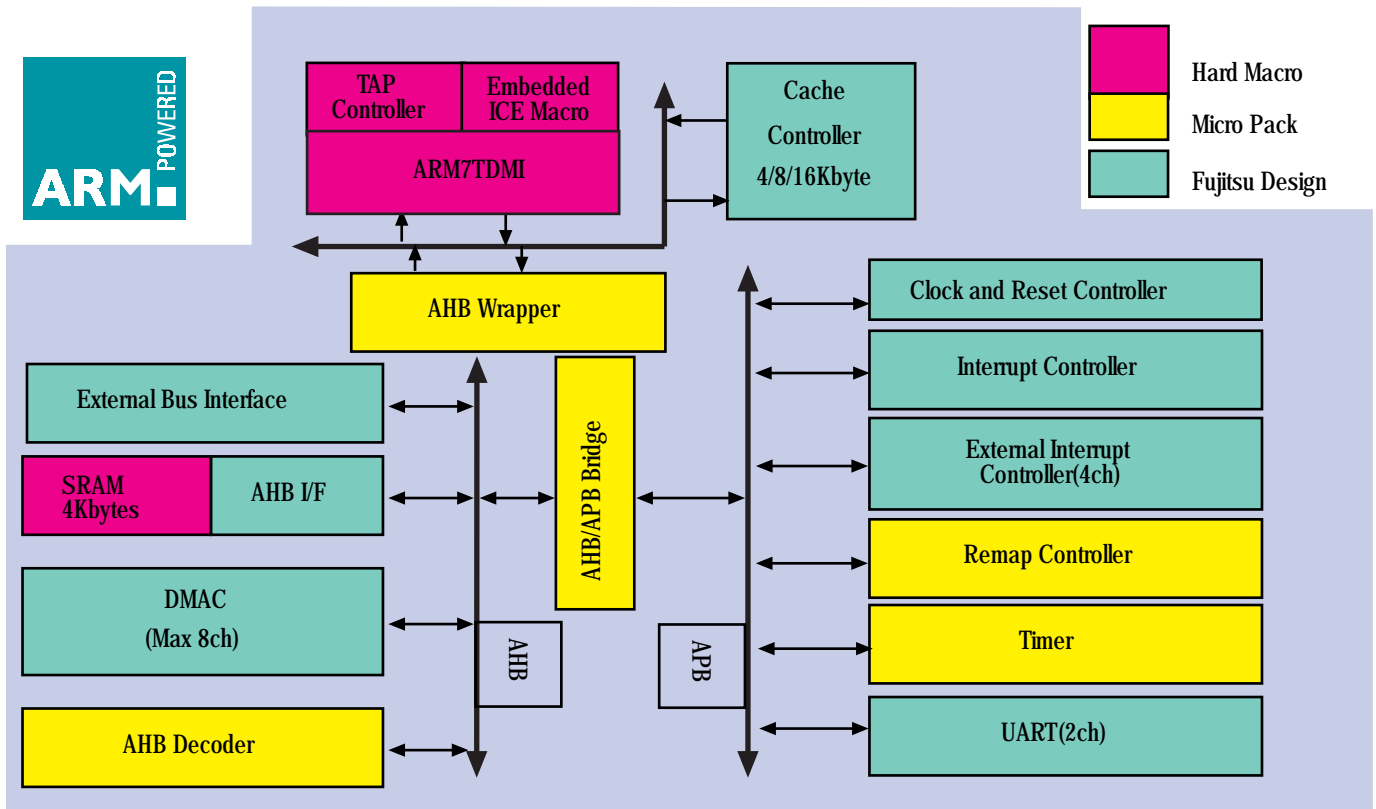


SoC Platform: ARM7TDMI Based



Features

- ARM7TDMI in 0.25 μ m and 0.18 μ m processes. High speed version available in 0.18 μ m (110 MHz)
- Low power (0.3mW/MHz) and small size (0.57mm² in 0.18 μ m) ARM7TDMI macro
- RTL based Peripheral IP's
- AMBA based platform, easy performance upgrade to ARM9 processor
- ARM MicroPack™ 2.0 in RTL codes provide many useful example AMBA modules
- Proven ASIC design methodology
- Evaluation boards (FPGA and ASIC) available for quick system verification
- Shorten Time-to-Market for system-on-chip realization

Overview

Fujitsu ARM7 SoC platform (Pistachio) is based on popular ARM7TDMI processor hard cores. It is proven in both 0.2 μ m and 0.18 μ m processes. The core contains all of ARM7TDMI features such as 32-bit RISC engine, Thumb instruction set, debug functions, multiplier and embedded ICE support logic.

Fujitsu's ARM core is supported by MicroPak™ peripherals provided by ARM, including Advance Peripheral Bus (timer, interrupt controller, remap, and pause controller), and Advanced High-performance Bus Peripherals (external memory interface, test interface controller, decoder, arbiter and reset controller).

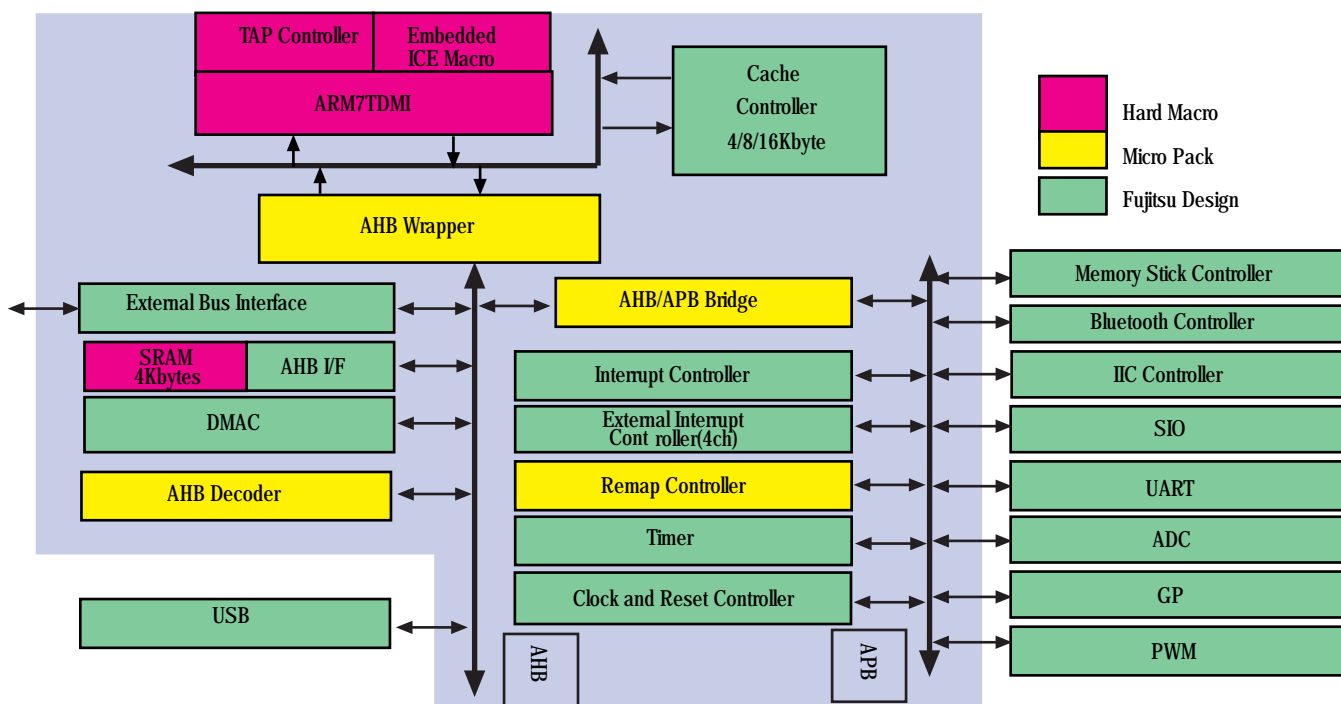
Additional peripherals developed by Fujitsu, enable customer to build up more complex systems. More peripherals are planned. In addition to that, Fujitsu also cooperate with third party IP suppliers to provide a wider range of core selections.

Peripheral IP's

Fujitsu develops the following set of peripheral IP's to ease customer SoC design. Encrypted RTL can be delivered for full chip functional verification. FPGA support is also available. More IP's are being developed.

IPs	Remarks
Cache Controller	16/8/4kB unified Cache
Internal RAM I/F	No wait access using compiled-RAM
External Bus I/F	For Flash/ROM/SRAM
DMAC	8ch/Block,Burst,demand
Interrupt Controller	32IRQ/16Levels
Clock Controller	SLEEP/STOP mode
UART I/F	For F_UART3

Example SoC



With Fujitsu SoC platform, customer only need to concentrate on own application specific IP's to build a system. This approach drastically reduce time to market which is critical in today's market place.

2 versions of evaluation boards (FPGA and ASIC daughter board) are available from Fujitsu to enable customer quick prototype, to verify SoC functions before taping out ASIC.

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