

MB86M28 MPEG-2/H.264 SD Encoder

June, 2014

FUJITSU SEMICONDUCTOR LIMITED

MB86M28 Specifications



Function	Spec.	
Encode	Video	H.264 SD/QVGA, MPEG-2 SD
	Audio	MPEG-2/4 AAC-LC, HE-AAC, MPEG-1 Audio Layer2, Dolby Digital (AC-3)
Stream Format	MPEG-2 TS	
Interfaces	PCIe, USB2.0(Device), Serial(UART), Stream-I/F(Serial or 8bit Parallel), Video I/F(ITU-R BT.656), Audio I/F(I ² S)	
Others	JPEG QVGA Encoder (for Thumbnail) I ² C, SPI-ROM(EEPROM) I/F	
Memory	1Gbit FCRAM embedded	
Power consumption	1.0W or less (typ., H.264 SD + SD Encoding)	
Package [SiP]	FBGA 490pin 21mm square (Ball pitch 0.8mm)	

■ Deliverables

■ Chip: MB86M28

- Ordering part number examples:
 - MB86M28APBS-A101-ME1 (MPEG-1 Audio Layer2)
 - MB86M28APBS-A103-ME1 (MPEG-1 Audio Layer2 + AAC)

■ Documents

- Product Specifications
- Control Command Specifications
- Serial Interface Specifications
- Application Notes
- USB Interface Specifications
- PCI Express Interface Specifications

■ Deliverables

■ Evaluation board

- No specified EVB for MB86M28, however MB86M25(Transcoder, has same package / pin-configuration, and same SD encoding function inside as M28) EVB is usable for VQ evaluation and PCB reference design .
 - Hardware specifications
 - Schematic and BOM list
 - EVB layout data(OrCAD data, Gerber data)

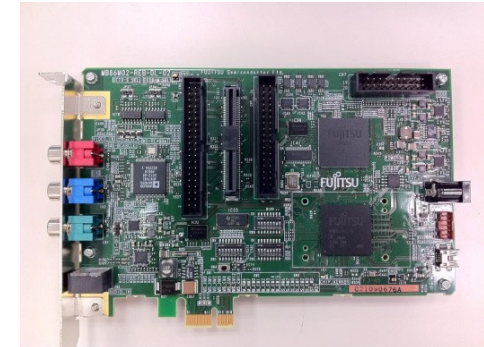
■ Sample software

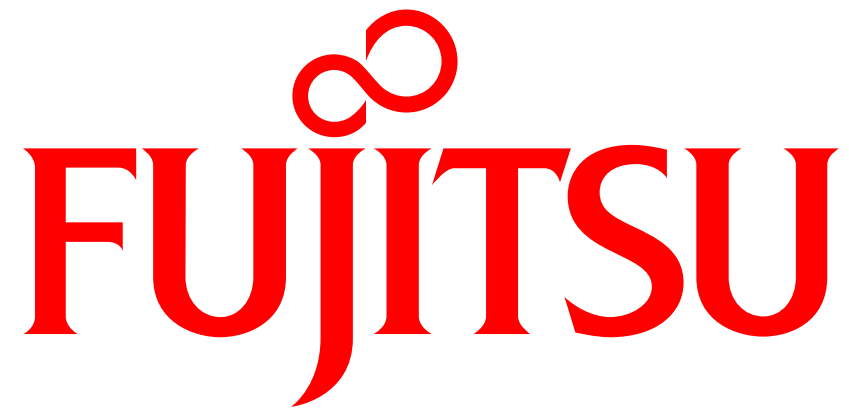
- Windows sample application and driver for USB and PCIe
- Linux sample application and driver for USB, PCIe(option)

■ Targeted schedule

- July/B 2014, all deliverables will be ready.

[MB86M25 evaluation board]





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